# SPEC® CPU2017 Integer Speed Result

**Lenovo Global Technology**

ThinkSystem SR850
(2.60 GHz, Intel Xeon Gold 6126)

<table>
<thead>
<tr>
<th>SPECspeed2017_int_base</th>
<th>8.71</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_int_peak</td>
<td>8.88</td>
</tr>
</tbody>
</table>

## CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

### Hardware

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_int_base (8.71)</th>
<th>SPECspeed2017_int_peak (8.88)</th>
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<tbody>
<tr>
<td>600.perlbench_s 48</td>
<td>6.16</td>
<td>7.34</td>
</tr>
<tr>
<td>602.gcc_s 48</td>
<td>7.04</td>
<td>9.25</td>
</tr>
<tr>
<td>605.mcf_s 48</td>
<td>5.71</td>
<td>10.9</td>
</tr>
<tr>
<td>620.omnetpp_s 48</td>
<td>5.58</td>
<td>10.9</td>
</tr>
<tr>
<td>623.xalancbmk_s 48</td>
<td>9.44</td>
<td>11.6</td>
</tr>
<tr>
<td>625.x264_s 48</td>
<td>10.0</td>
<td>11.6</td>
</tr>
<tr>
<td>631.deepsjeng_s 48</td>
<td>4.97</td>
<td>4.34</td>
</tr>
<tr>
<td>641.leela_s 48</td>
<td>4.34</td>
<td>13.4</td>
</tr>
<tr>
<td>648.exchange2_s 48</td>
<td>13.4</td>
<td>22.7</td>
</tr>
<tr>
<td>657.xz_s 48</td>
<td>22.7</td>
<td>22.7</td>
</tr>
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</table>

### Software

<table>
<thead>
<tr>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS: SUSE Linux Enterprise Server 12 SP2 (x86_64)</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 18.0.0.128 of Intel C/C++</td>
</tr>
<tr>
<td>Compiler for Linux: Fortran: Version 18.0.0.128 of Intel Fortran</td>
</tr>
<tr>
<td>Parallel: Yes</td>
</tr>
<tr>
<td>Firmware: Lenovo BIOS Version TEE115E 1.01 released Aug-2017</td>
</tr>
<tr>
<td>File System: xfs</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>Peak Pointers: 32/64-bit</td>
</tr>
<tr>
<td>Other: jemalloc: jemalloc memory allocator library V5.0.1</td>
</tr>
</tbody>
</table>

---

CPU Name: Intel Xeon Gold 6126
Max MHz.: 3700
Nominal: 2600
Enabled: 48 cores, 4 chips
Orderable: 2,4 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 19.25 MB I+D on chip per core
Other: None
Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)
Storage: 1 x 800 GB SAS SSD
Other: None

Test Date: Dec-2017
Hardware Availability: Aug-2017
Software Availability: Sep-2017
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**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tr>
<td>605.mcf_s</td>
<td>48</td>
<td>433</td>
<td>9.04</td>
<td>436</td>
<td>10.8</td>
<td>430</td>
<td>9.25</td>
<td>48</td>
<td>431</td>
<td>10.9</td>
<td>432</td>
<td>10.9</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>48</td>
<td>285</td>
<td>5.73</td>
<td>303</td>
<td>5.39</td>
<td>48</td>
<td>141</td>
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<td>292</td>
<td>5.58</td>
<td>304</td>
<td>5.37</td>
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<tr>
<td>623.xalanchmk_s</td>
<td>48</td>
<td>150</td>
<td>9.44</td>
<td>150</td>
<td>9.46</td>
<td>150</td>
<td>9.43</td>
<td>48</td>
<td>152</td>
<td>11.6</td>
<td>152</td>
<td>11.6</td>
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<td>625.x264_s</td>
<td>48</td>
<td>152</td>
<td>11.6</td>
<td>152</td>
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<td>152</td>
<td>11.6</td>
<td>48</td>
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<td>11.6</td>
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<td>631.deepsjeng_s</td>
<td>48</td>
<td>289</td>
<td>4.96</td>
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<td>4.97</td>
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<td>648.exchange2_s</td>
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<td>13.3</td>
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<td>272</td>
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<td>22.7</td>
<td>48</td>
<td>272</td>
<td>22.7</td>
<td>273</td>
<td>22.6</td>
</tr>
</tbody>
</table>

**SPECspeed2017_int_base = 8.71**

**SPECspeed2017_int_peak = 8.88**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Operating System Notes**

*Stack size set to unlimited using "ulimit -s unlimited"*

**General Notes**

Environment variables set by runcpu before the start of the run:

- LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
- LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
- OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Third Party Software Used:

- Transparent Huge Pages enabled by default
- Prior to runcpu invocation
- Filesystem page cache synced and cleared with: `sync; echo 3>/proc/sys/vm/drop_caches`
- jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
- jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)
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**General Notes (Continued)**

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page. The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

**Platform Notes**

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disable
DCU Streamer Prefetcher set to Disable
MONITORM/WAIT set to Enable
Trusted Execution Technology set to Enable
XPT Prefetcher set to Enable
DCA set to Enable
Stale AtoS set to Enable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618b60c91c0f
running on Electron-node-02 Wed Dec 27 18:19:42 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6126 CPU @ 2.60GHz
4 "physical id"s (chips)
48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings : 12
physical 0: cores 0 1 2 4 5 6 8 9 10 11 13 14
physical 1: cores 0 1 3 4 5 6 8 9 10 11 12 13
physical 2: cores 0 1 2 4 5 6 8 9 10 11 13 14
physical 3: cores 0 1 2 4 5 6 8 9 10 11 13 14

From lscpu:
Architecture: x86_64

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**Lenovo Global Technology**

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Platform Notes (Continued)

CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 1
Core(s) per socket: 12
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6126 CPU @ 2.60GHz
Stepping: 4
CPU MHz: 2593.910
BogoMIPS: 5187.82
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 19712K
NUMA node0 CPU(s): 0-11
NUMA node1 CPU(s): 12-23
NUMA node2 CPU(s): 24-35
NUMA node3 CPU(s): 36-47
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pclid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ets invpcid rm cxvmpказать clflushopt clwb axv512bw axv512v1 xsaveopt xsavec xgetbv1 cqm_11c cqm_occup_llc

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

(Continued on next page)
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Platform Notes (Continued)

node 2 size: 387057 MB
node 2 free: 386161 MB
node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47
node 3 size: 387054 MB
node 3 free: 386049 MB
node distances:
node 0 1 2 3
0: 10 21 21 31
1: 21 10 31 21
2: 21 31 10 21
3: 31 21 21 10

From /proc/meminfo
MemTotal: 1584976124 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID=12.2
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux Electron-node-02 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Dec 27 18:15

SPEC is set to: /home/cpu2017.1.0.2.ic18.0
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 688G 130G 559G 19% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
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Platform Notes (Continued)

BIOS Lenovo -[TEE115E-1.01]- 08/11/2017
Memory:
  48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC   600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base) 641.leela_s(base)
==============================================================================
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 620.omnetpp_s(peak) 623.xalancbmk_s(peak) 631.deepsjeng_s(peak) 641.leela_s(peak)
==============================================================================
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC  648.exchange2_s(base, peak)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
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### Base Compiler Invocation

C benchmarks:  
`icc`

C++ benchmarks:  
`icpc`

Fortran benchmarks:  
`ifort`

### Base Portability Flags

<table>
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<tr>
<th>Benchmark</th>
<th>Flags</th>
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<td>-DSPEC_LP64</td>
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<td>657.xz_s</td>
<td>-DSPEC_LP64</td>
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</table>

### Base Optimization Flags

C benchmarks:

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

C++ benchmarks:

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

Fortran benchmarks:

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`
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### Base Other Flags

- C benchmarks:  
  -m64  
  -std=c11
- C++ benchmarks:  
  -m64
- Fortran benchmarks:  
  -m64

### Peak Compiler Invocation

- C benchmarks:  
  icc
- C++ benchmarks:  
  icpc
- Fortran benchmarks:  
  ifort

### Peak Portability Flags

- 600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
- 602.gcc_s: -DSPEC_LP64
- 605.mcf_s: -DSPEC_LP64
- 620.omnetpp_s: -DSPEC_LP64
- 623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
- 625.x264_s: -DSPEC_LP64
- 631.deepsjeng_s: -DSPEC_LP64
- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64

### Peak Optimization Flags

- C benchmarks:
  - 600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
  -xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3
  -no-prec-div -DSPEC.Suppress.OpenMP -gopenmp

(Continued on next page)
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### Peak Optimization Flags (Continued)

**600.perlbench_s (continued):**
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

**602.gcc_s:** -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

**605.mcf_s:** -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

**625.x264_s:** -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

**657.xz_s:** Same as 602.gcc_s

### C++ benchmarks:

**620.omnetpp_s:** -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

**623.xalancbmk_s:** -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-32/lib -ljemalloc

**631.deepsjeng_s:** Same as 620.omnetpp_s

**641.leela_s:** Same as 620.omnetpp_s

### Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
Lenovo Global Technology
ThinkSystem SR850
(2.60 GHz, Intel Xeon Gold 6126)

<table>
<thead>
<tr>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
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<td>8.71</td>
<td>8.88</td>
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</tbody>
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Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Peak Other Flags

C benchmarks:
- m64 -std=c11

C++ benchmarks (except as noted below):
- m64

623.xalancbmk_s: -m32

Fortran benchmarks:
- m64

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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