Lenovo Global Technology
ThinkSystem SR850
(2.10 GHz, Intel Xeon Gold 6130)

CPU2017 License: 9017  Test Date: Jan-2018
Test Sponsor: Lenovo Global Technology     Hardware Availability: Aug-2017
Tested by: Lenovo Global Technology

<table>
<thead>
<tr>
<th>Software</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>OS:</td>
<td>SUSE Linux Enterprise Server 12 SP2 (x86_64)</td>
</tr>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 18.0.0.128 of Intel C/C++</td>
</tr>
<tr>
<td>Compiler for Linux:</td>
<td>Version 18.0.0.128 of Intel Fortran</td>
</tr>
<tr>
<td>Firmware:</td>
<td>Lenovo BIOS Version TEE115E 1.01 released Aug-2017</td>
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<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
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</tr>
<tr>
<td>Peak Pointers:</td>
<td>32/64-bit</td>
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<tr>
<td>Other:</td>
<td>jemalloc: jemalloc memory allocator library V5.0.1</td>
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<table>
<thead>
<tr>
<th>Hardware</th>
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<tbody>
<tr>
<td>CPU Name:</td>
<td>Intel Xeon Gold 6130</td>
</tr>
<tr>
<td>Max MHz.:</td>
<td>3700</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2100</td>
</tr>
<tr>
<td>Enabled:</td>
<td>64 cores, 4 chips</td>
</tr>
<tr>
<td>Orderable:</td>
<td>2,4 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1 MB I+D on chip per core</td>
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<tr>
<td>L3:</td>
<td>22 MB I+D on chip per chip</td>
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<td>Other:</td>
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<td>Memory:</td>
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<td>Storage:</td>
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<td>Other:</td>
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SPECspeed2017_int_base = 8.76
SPECspeed2017_int_peak = 9.05

600.perlbench_s 64
Thread 64

602.gcc_s 64
Thread 64

605.mcf_s 64
Thread 64

620.omnetpp_s 64
Thread 64

623.xalancbmk_s 64
Thread 64

625.x264_s 64
Thread 64

631.deepsjeng_s 64
Thread 64

641.leela_s 64
Thread 64

648.exchange2_s 64
Thread 64

657.xz_s 64
Thread 64

--- SPECspeed2017_int_base (8.76) ---
--- SPECspeed2017_int_peak (9.05) ---
### Lenovo Global Technology

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(2.10 GHz, Intel Xeon Gold 6130)

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**SPEC CPU2017 Integer Speed Result**

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**SPECspeed2017_int_peak = 9.05**

---

**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>620.omnetpp_s</td>
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<td>625.x264_s</td>
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<td>641.leela_s</td>
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<tr>
<td>648.exchange2_s</td>
<td>64</td>
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<td>657.xz_s</td>
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<td>23.2</td>
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<tr>
<td>SPECspeed2017_int_base = 8.76</td>
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<td></td>
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<tr>
<td>SPECspeed2017_int_peak = 9.05</td>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

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**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

---

**General Notes**

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"

LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"

OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

csync; echo 3> /proc/sys/vm/drop_caches

jemalloc: configured and built at default for

32bit (i686) and 64bit (x86_64) targets;

jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;


No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
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SPECspeed2017_int_base = 8.76
SPECspeed2017_int_peak = 9.05

General Notes (Continued)

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page. The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disable
DCU Streamer Prefetcher set to Disable
MONITORM/WAIT set to Enable
Trusted Execution Technology set to Enable
XPT Prefetcher set to Enable
DCA set to Enable
Stale AtoS set to Enable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on Electron-node-02 Fri Jan 5 10:39:19 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name: Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
  4 "physical id"s (chips)
  64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores: 16
siblings: 16
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
Architecture: x86_64

(Continued on next page)
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<td>SPECspeed2017_int_peak</td>
<td>9.05</td>
</tr>
</tbody>
</table>

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Platform Notes (Continued)

CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
Stepping: 4
CPU MHz: 2095.077
BogoMIPS: 4190.15
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 22528K
NUMA node0 CPU(s): 0-15
NUMA node1 CPU(s): 16-31
NUMA node2 CPU(s): 32-47
NUMA node3 CPU(s): 48-63
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts mmu fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_time arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfp nni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pni dtsc dtherm intel_pt
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
erms invpcid rtm cqm mpx avx512f avx512d avx512pd rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_1llc cqm_occup_llc

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

<table>
<thead>
<tr>
<th>Available</th>
<th>CPUs</th>
<th>Processor Size</th>
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<tbody>
<tr>
<td>node 0</td>
<td>0</td>
<td>386659 MB</td>
</tr>
<tr>
<td>node 1</td>
<td>34</td>
<td>385615 MB</td>
</tr>
<tr>
<td>node 2</td>
<td>36</td>
<td>386069 MB</td>
</tr>
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</table>

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SPEC CPU2017 Integer Speed Result

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SPECspeed2017_int_base = 8.76
SPECspeed2017_int_peak = 9.05

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Jan-2018
Tested by: Lenovo Global Technology
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Platform Notes (Continued)

node 2 size: 387057 MB
node 2 free: 386067 MB
node 3 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63
node 3 size: 387054 MB
node 3 free: 386071 MB
node distances:
node 0 1 2 3
0:  10 21 21 31
1:  21 10 31 21
2:  21 31 10 21
3:  31 21 21 10

From /proc/meminfo
MemTotal:      1584976124 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
   SUSE Linux Enterprise Server 12 (x86_64)
   VERSION = 12
   PATCHLEVEL = 2
   # This file is deprecated and will be removed in a future service pack or release.
   # Please check /etc/os-release for details about this release.
os-release:
   NAME="SLES"
   VERSION="12-SP2"
   VERSION_ID="12.2"
   PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
   ID="sles"
   ANSI_COLOR="0;32"
   CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux Electron-node-02 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 5 10:36

SPEC is set to: /home/cpu2017.1.0.2.ic18.0
Filesystem Type  Size Used Avail Use% Mounted on
/dev/sda4     xfs 688G 130G 559G 19% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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Lenovo Global Technology

CPU2017 License: 9017
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Tested by: Lenovo Global Technology

Test Date: Jan-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Platform Notes (Continued)

BIOS Lenovo -[TEE115E-1.01]- 08/11/2017
Memory:
48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)
.................................................................................................................................
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC  600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)
.................................................................................................................................
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base) 641.leela_s(base)
.................................................................................................................................
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 620.omnetpp_s(peak) 623.xalancbmk_s(peak) 631.deepsjeng_s(peak) 641.leela_s(peak)
.................................................................................................................................
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC  648.exchange2_s(base, peak)
.................................................................................................................................
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
## Lenovo Global Technology

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<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
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<tbody>
<tr>
<td>SPECspeed2017_int_base</td>
<td>8.76</td>
</tr>
<tr>
<td>SPECspeed2017_int_peak</td>
<td>9.05</td>
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| Tested by                     | Lenovo Global Technology |
| Test Date                     | Jan-2018       |
| Hardware Availability         | Aug-2017       |
| Software Availability         | Sep-2017       |

### Base Compiler Invocation

- **C benchmarks:**  
  - icc

- **C++ benchmarks:**  
  - icpc

- **Fortran benchmarks:**  
  - ifort

### Base Portability Flags

- 600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
- 602.gcc_s: -DSPEC_LP64
- 605.mcf_s: -DSPEC_LP64
- 620.omnetpp_s: -DSPEC_LP64
- 623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
- 625.x264_s: -DSPEC_LP64
- 631.deepsjeng_s: -DSPEC_LP64
- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPEMP`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

**C++ benchmarks:**

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

**Fortran benchmarks:**

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`
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SPECspeed2017_int_base = 8.76
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Base Other Flags

C benchmarks:
- m64 -std=c11

C++ benchmarks:
- m64

Fortran benchmarks:
- m64

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
600.perlbench_s: -Wl, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp

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Peak Optimization Flags (Continued)

600.perlbench_s (continued):
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -gopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: Same as 602.gcc_s

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

623.xalancbmk_s: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-32/lib -ljemalloc

631.deepsjeng_s: Same as 620.omnetpp_s

641.leela_s: Same as 620.omnetpp_s

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
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<tr>
<td>Tested by: Lenovo Global Technology</td>
<td>Software Availability: Sep-2017</td>
</tr>
</tbody>
</table>

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#### SPECspeed2017_int_base = 8.76

#### SPECspeed2017_int_peak = 9.05

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### Peak Other Flags

- **C benchmarks:**
  - `-m64 -std=c11`

- **C++ benchmarks (except as noted below):**
  - `-m64`

  - `623.xalancbmk_s: -m32`

- **Fortran benchmarks:**
  - `-m64`

---

The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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