Lenovo Global Technology
ThinkSystem SR650
(3.20 GHz, Intel Xeon Gold 6134M)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

CPU Name: Intel Xeon Gold 6134M
Max MHz.: 3700
Nominal: 3200
Enabled: 16 cores, 2 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 24.75 MB I+D on chip per chip
Other: None
Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)
Storage: 1 x 800 GB SAS SSD
Other: None

OS: SUSE Linux Enterprise Server 12 SP2 (x86_64)
Kernel 4.4.21-69-default
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++
Compiler for Linux:
Fortran: Version 18.0.0.128 of Intel Fortran
Compiler for Linux:
Parallel: Yes
Firmware: Lenovo BIOS Version IVE113U 1.11 released Dec-2017
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc: jemalloc memory allocator library V5.0.1
SPEC CPU2017 Integer Speed Result

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SPECspeed2017_int_base = 8.73
SPECspeed2017_int_peak = 8.98

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Seconds</th>
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</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>16</td>
<td>286</td>
<td>6.20</td>
<td>290</td>
<td>6.12</td>
<td>286</td>
<td>6.21</td>
<td>16</td>
<td>242</td>
<td>7.34</td>
<td>241</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>16</td>
<td>428</td>
<td>9.29</td>
<td>431</td>
<td>9.23</td>
<td>430</td>
<td>9.27</td>
<td>16</td>
<td>421</td>
<td>9.46</td>
<td>420</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>16</td>
<td>435</td>
<td>9.29</td>
<td>431</td>
<td>9.23</td>
<td>430</td>
<td>9.27</td>
<td>16</td>
<td>421</td>
<td>9.46</td>
<td>420</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>16</td>
<td>271</td>
<td>6.02</td>
<td>257</td>
<td>6.34</td>
<td>256</td>
<td>6.36</td>
<td>16</td>
<td>264</td>
<td>6.18</td>
<td>250</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>16</td>
<td>150</td>
<td>9.43</td>
<td>149</td>
<td>9.50</td>
<td>152</td>
<td>9.34</td>
<td>16</td>
<td>140</td>
<td>10.2</td>
<td>139</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>16</td>
<td>150</td>
<td>9.43</td>
<td>149</td>
<td>9.50</td>
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<td>140</td>
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<td>139</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>16</td>
<td>283</td>
<td>5.05</td>
<td>284</td>
<td>5.05</td>
<td>283</td>
<td>5.05</td>
<td>16</td>
<td>285</td>
<td>5.02</td>
<td>286</td>
</tr>
<tr>
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<td>16</td>
<td>393</td>
<td>4.34</td>
<td>394</td>
<td>4.34</td>
<td>393</td>
<td>4.34</td>
<td>16</td>
<td>392</td>
<td>4.35</td>
<td>392</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>16</td>
<td>220</td>
<td>13.4</td>
<td>221</td>
<td>13.3</td>
<td>220</td>
<td>13.4</td>
<td>16</td>
<td>220</td>
<td>13.4</td>
<td>220</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>16</td>
<td>308</td>
<td>20.1</td>
<td>311</td>
<td>19.9</td>
<td>308</td>
<td>20.1</td>
<td>16</td>
<td>308</td>
<td>20.1</td>
<td>305</td>
</tr>
</tbody>
</table>

**SPEC**

**SPECspeed2017_int_base = 8.73**
**SPECspeed2017_int_peak = 8.98**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**General Notes**

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3> /proc/sys/vm/drop_caches
jemalloc: configured and built at default for
32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4,
and the system compiler gcc 4.8.5;
jemalloc: sources available from jemalloc.net or
No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

(Continued on next page)
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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
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General Notes (Continued)
This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.
The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html
This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disable
MONITORMWAIT set to Enable
Adjacent Cache Prefetch set to Disable
XPT Prefetcher set to Enable
Stale AtoS set to Enable
DCA set to Enable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on Cyborg-SPECcpu2006-SUSE12SP2 Tue Jan 30 23:10:22 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6134M CPU @ 3.20GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 2 3 9 16 19 26 27
physical 1: cores 0 2 3 9 16 19 26 27

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16

(Continued on next page)
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Test Date: Jan-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

**SPECspeed2017_int_base = 8.73**

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---

### Platform Notes (Continued)

- On-line CPU(s) list: 0-15
- Thread(s) per core: 1
- Core(s) per socket: 8
- Socket(s): 2
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Gold 6134M CPU @ 3.20GHz
- Stepping: 4
- CPU MHz: 3192.479
- BogoMIPS: 6384.95
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 25344K
- NUMA node0 CPU(s): 0-7
- NUMA node1 CPU(s): 8-15
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl apic nonstop_tsc aperf mpicmp dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pni pclmulqdq dtes64u smx de smap cmov msrs mxrmx tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erekms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512bw avx512vl xsaveopt xsavesopt xgetbv1 cqm_llc cqm_occup_llc

From /proc/cpuinfo cache data
  cache size: 25344 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7
  node 0 size: 193109 MB
  node 0 free: 192399 MB
  node 1 cpus: 8 9 10 11 12 13 14 15
  node 1 size: 193504 MB
  node 1 free: 192726 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10

From /proc/meminfo

---

(Continued on next page)
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Platform Notes (Continued)

MemTotal: 395892644 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux Cyborg-SPECcpu2006-SUSE12SP2 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67) x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Jan 30 23:09

SPEC is set to: /home/cpu2017.1.0.2.ic18.0

Filesystem Type Size Used Avail Use% Mounted on
/dev/sdb2 btrfs 744G 263G 478G 36% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMIOS" standard.

BIOS Lenovo -[IVE113U-1.11]- 12/12/2017
Memory:
24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666

Compiler Version Notes

==============================================================================
CC 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)
==============================================================================

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Compiler Version Notes (Continued)

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
== C benchmarks ==========
== icc ==========

==============================================================================
== C++ benchmarks ==========
== icpc ==========

==============================================================================
== Fortran benchmarks ==========
== ifort ==========

Base Compiler Invocation

C benchmarks:
iccc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort
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Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64
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Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

(Continued on next page)
Peak Optimization Flags (Continued)

625.x264_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: Same as 602.gcc_s

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

623.xalancbmk_s: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-32/lib -ljemalloc

631.deepsjeng_s: Same as 620.omnetpp_s

641.leela_s: Same as 620.omnetpp_s

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Other Flags

C benchmarks:

-m64 -std=c11

C++ benchmarks (except as noted below):

-m64

623.xalancbmk_s: -m32

Fortran benchmarks:

-m64
## SPEC CPU2017 Integer Speed Result

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</table>

The flags files that were used to format this result can be browsed at:

- [Intel-ic18.0-official-linux64.html](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html)

You can also download the XML flags sources by saving the following links:

- [Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)
- [Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.xml](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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