## Lenovo Global Technology

### ThinkSystem SR630
(2.20 GHz, Intel Xeon Silver 4114T)

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_fp_base =</th>
<th>SPECspeed2017_fp_peak =</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>20</td>
<td>85.9</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>20</td>
<td>34.6</td>
</tr>
<tr>
<td>619.ibm_s</td>
<td>20</td>
<td>57.5</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>20</td>
<td>61.8</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>20</td>
<td>43.7</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>20</td>
<td>53.9</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>20</td>
<td>97.7</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>20</td>
<td>64.2</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>20</td>
<td>68.6</td>
</tr>
</tbody>
</table>

### Hardware
- **CPU Name:** Intel Xeon Silver 4114T
- **Max MHz.:** 3000
- **Nominal:** 2200
- **Enabled:** 20 cores, 2 chips
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 13.75 MB I+D on chip per core
- **Other:** None
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)
- **Storage:** 1 x 800 GB SAS SSD
- **Other:** None

### Software
- **OS:** SUSE Linux Enterprise Server 12 SP2 (x86_64)
- **Kernel:** 4.4.21-69-default
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++
- **Compiler for Linux:** Fortran: Version 18.0.0.128 of Intel Fortran
- **Compiler for Linux:**
- **Parallel:** Yes
- **Firmware:** Lenovo BIOS Version IVE113U 1.11 released Dec-2017
- **File System:** btrfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** None
Lenovo Global Technology
ThinkSystem SR630
(2.20 GHz, Intel Xeon Silver 4114T)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECspeed2017_fp_base = 70.9
SPECspeed2017_fp_peak = 72.0

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>20</td>
<td>168</td>
<td>352</td>
<td>167</td>
<td>353</td>
<td>167</td>
<td>353</td>
<td>20</td>
<td>167</td>
<td>353</td>
<td>167</td>
<td>353</td>
<td>168</td>
<td>352</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>20</td>
<td>195</td>
<td>85.5</td>
<td>194</td>
<td>85.9</td>
<td>194</td>
<td>86.1</td>
<td>20</td>
<td>190</td>
<td>87.9</td>
<td>190</td>
<td>87.9</td>
<td>190</td>
<td>87.8</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>20</td>
<td>152</td>
<td>34.5</td>
<td>152</td>
<td>34.6</td>
<td>151</td>
<td>34.6</td>
<td>20</td>
<td>152</td>
<td>34.4</td>
<td>152</td>
<td>34.3</td>
<td>152</td>
<td>34.5</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>20</td>
<td>230</td>
<td>57.5</td>
<td>231</td>
<td>57.3</td>
<td>230</td>
<td>57.6</td>
<td>20</td>
<td>213</td>
<td>62.0</td>
<td>215</td>
<td>61.6</td>
<td>214</td>
<td>61.8</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>20</td>
<td>203</td>
<td>43.7</td>
<td>203</td>
<td>43.7</td>
<td>203</td>
<td>43.7</td>
<td>20</td>
<td>203</td>
<td>43.6</td>
<td>202</td>
<td>43.8</td>
<td>203</td>
<td>43.7</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>20</td>
<td>225</td>
<td>52.7</td>
<td>227</td>
<td>52.3</td>
<td>227</td>
<td>52.3</td>
<td>20</td>
<td>218</td>
<td>54.5</td>
<td>221</td>
<td>53.6</td>
<td>221</td>
<td>53.6</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>20</td>
<td>268</td>
<td>53.9</td>
<td>267</td>
<td>53.9</td>
<td>268</td>
<td>53.8</td>
<td>20</td>
<td>270</td>
<td>53.4</td>
<td>268</td>
<td>53.9</td>
<td>270</td>
<td>53.5</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>20</td>
<td>179</td>
<td>97.8</td>
<td>179</td>
<td>97.7</td>
<td>179</td>
<td>97.7</td>
<td>20</td>
<td>179</td>
<td>97.7</td>
<td>179</td>
<td>97.8</td>
<td>179</td>
<td>97.8</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>20</td>
<td>144</td>
<td>63.4</td>
<td>142</td>
<td>64.2</td>
<td>142</td>
<td>64.2</td>
<td>20</td>
<td>142</td>
<td>64.1</td>
<td>143</td>
<td>63.6</td>
<td>143</td>
<td>63.5</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>20</td>
<td>230</td>
<td>68.6</td>
<td>231</td>
<td>68.2</td>
<td>229</td>
<td>68.8</td>
<td>20</td>
<td>216</td>
<td>72.8</td>
<td>218</td>
<td>72.3</td>
<td>217</td>
<td>72.6</td>
</tr>
</tbody>
</table>

SPECspeed2017_fp_base = 70.9
SPECspeed2017_fp_peak = 72.0

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Files system page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly

(Continued on next page)
General Notes (Continued)

generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disable
MONITORMWAIT set to Enable
Adjacent Cache Prefetch set to Disable
XPT Prefetcher set to Enable
Stale AtoS set to Enable
DCA set to Enable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on Cable-SPECcpu2006-SUSE12SP2 Wed Jan 24 00:17:22 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Silver 4114T CPU @ 2.20GHz
    2 "physical id"s (chips)
    20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 10
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 20
On-line CPU(s) list: 0-19
Thread(s) per core: 1
Core(s) per socket: 10

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR630
(2.20 GHz, Intel Xeon Silver 4114T)  

SPECspeed2017_fp_base = 70.9
SPECspeed2017_fp_peak = 72.0

Platform Notes (Continued)

Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4114T CPU @ 2.20GHz
Stepping: 4
CPU MHz: 2194.848
BogoMIPS: 4389.69
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 14080K
NUMA node0 CPU(s): 0-9
NUMA node1 CPU(s): 10-19
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmrperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat pni pt dtherm intel_pt
tpr_shadow tm2 tpr_msr tsc_deadline_timer aes f16c rdrand lm lahf pti pt dmb cs cnx
cx16 smep msr pki lse cppllc ml2

/proc/cpuinfo cache data
  cache size: 14080 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9
    node 0 size: 193110 MB
    node 0 free: 191668 MB
    node 1 cpus: 10 11 12 13 14 15 16 17 18 19
    node 1 size: 193504 MB
    node 1 free: 192421 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10

From /proc/meminfo
  MemTotal: 395893352 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR630
(2.20 GHz, Intel Xeon Silver 4114T)

**Platform Notes (Continued)**

From /etc/*release* /etc/*version*

SuSE-release:
- SUSE Linux Enterprise Server 12 (x86_64)
- VERSION = 12
- PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.

os-release:
- NAME="SLES"
- VERSION="12-SP2"
- VERSION_ID="12.2"
- PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
- ID="sles"
- ANSI_COLOR="0;32"
- CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
- Linux Cable-SPECcpu2006-SUSE12SP2 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 23 23:23

**Filesystem** **Type** **Size** **Used** **Avail** **Use%** Mounted on
/dev/sda2  btrfs  744G  253G  490G  34% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- BIOS Lenovo -[IVE113U-1.11]- 12/12/2017
- Memory:
  - 24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

**Compiler Version Notes**

<table>
<thead>
<tr>
<th>CC 619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>icc (ICC) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

(Continued on next page)
<table>
<thead>
<tr>
<th>Compiler Version Notes (Continued)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CC  619.lbm_s(peak)</td>
</tr>
<tr>
<td>icc (ICC) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>FC  607.cactuBSSN_s(base)</th>
</tr>
</thead>
<tbody>
<tr>
<td>icpc (ICC) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>icc (ICC) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>ifort (IFORT) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>FC  607.cactuBSSN_s(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>icpc (ICC) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>icc (ICC) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>ifort (IFORT) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>FC  603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)</th>
</tr>
</thead>
<tbody>
<tr>
<td>ifort (IFORT) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>FC  603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>ifort (IFORT) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CC  621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)</th>
</tr>
</thead>
<tbody>
<tr>
<td>ifort (IFORT) 18.0.0 20170811</td>
</tr>
</tbody>
</table>

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR630
(2.20 GHz, Intel Xeon Silver 4114T)

SPECSpeed2017_fp_base = 70.9
SPECSpeed2017_fp_peak = 72.0

Compiler Version Notes (Continued)
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-----------------------------------------------
CC 621.wrf_s(peak) 628.pop2_s(peak)

-----------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation
C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags
603.bwaves_s: -DSPEC_LP64
607.cactusBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
Lenovo Global Technology
ThinkSystem SR630
(2.20 GHz, Intel Xeon Silver 4114T)

SPECspeed2017_fp_base = 70.9
SPECspeed2017_fp_peak = 72.0

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort
Lenovo Global Technology

ThinkSystem SR630
(2.20 GHz, Intel Xeon Silver 4114T)

Specspeed2017_fp_base = 70.9
Specspeed2017_fp_peak = 72.0

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: Jan-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:

-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR630
(2.20 GHz, Intel Xeon Silver 4114T)

SPECspeed2017_fp_base = 70.9
SPECspeed2017_fp_peak = 72.0

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: Jan-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Peak Optimization Flags (Continued)

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
-align array32byte

Peak Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.xml

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-01-23 11:17:21-0500.
Originally published on 2018-03-06.