Lenovo Global Technology
ThinkSystem SR650
(2.10 GHz, Intel Xeon Platinum 8176M)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>226</td>
<td>229</td>
</tr>
</tbody>
</table>

Test Sponsor: Lenovo Global Technology
Test Date: Jan-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

<table>
<thead>
<tr>
<th>Test</th>
<th>Result</th>
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<tbody>
<tr>
<td>503.bwaves_r</td>
<td>112</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>112</td>
</tr>
<tr>
<td>508.namd_r</td>
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</tr>
<tr>
<td>510.parest_r</td>
<td>112</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>112</td>
</tr>
<tr>
<td>519.lbm_r</td>
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<tr>
<td>521.wrf_r</td>
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<tr>
<td>526.blender_r</td>
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</tr>
<tr>
<td>527.cam4_r</td>
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<tr>
<td>538.imagick_r</td>
<td>112</td>
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<td>544.nab_r</td>
<td>112</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>112</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>112</td>
</tr>
</tbody>
</table>

Hardware:
- CPU Name: Intel Xeon Platinum 8176M
- Max MHz.: 3800
- Nominal: 2100
- Enabled: 56 cores, 2 chips, 2 threads/core
- Orderable: 1.2 chips
- Cache L1: 32 KB I + 32 KB D on chip per core
- L2: 1 MB I+D on chip per core
- L3: 38.5 MB I+D on chip per core
- Other: None
- Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)
- Storage: 1 x 800 GB SAS SSD
- Other: None

Software:
- OS: SUSE Linux Enterprise Server 12 SP2 (x86_64)
- Compiler: C/C++: Version 18.0.0.128 of Intel C/C++
- Fortran: Version 18.0.0.128 of Intel Fortran
- Compiler for Linux
- Parallel: No
- Firmware: Lenovo BIOS Version IVE113U 1.11 released Dec-2017
- File System: xfs
- System State: Run level 3 (multi-user)
- Base Pointers: 64-bit
- Peak Pointers: 64-bit
- Other: None
**SPEC CPU2017 Floating Point Rate Result**

**Lenovo Global Technology**

ThinkSystem SR650
(2.10 GHz, Intel Xeon Platinum 8176M)

---

**SPECrate2017_fp_base = 226**

**SPECrate2017_fp_peak = 229**

**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Second</th>
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<td>507.cactuBSSN_r</td>
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<td>648</td>
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<td>219</td>
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<td>508.namd_r</td>
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<tr>
<td>510.parest_r</td>
<td>112</td>
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<td>2348</td>
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<td>2352</td>
<td>125</td>
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<tr>
<td>511.povray_r</td>
<td>112</td>
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<td>311</td>
<td>838</td>
<td>312</td>
<td>841</td>
<td>311</td>
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<tr>
<td>519.lbm_r</td>
<td>112</td>
<td>1007</td>
<td>117</td>
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<td>117</td>
<td>1008</td>
<td>117</td>
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<td>521.wrf_r</td>
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<tr>
<td>526.blender_r</td>
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<td>278</td>
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<td>527.cam4_r</td>
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<td>538.imagick_r</td>
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<td>108</td>
<td>1647</td>
<td>108</td>
<td>1647</td>
<td>108</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using \"ulimit -s unlimited\"

**General Notes**

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
Lenovo Global Technology
ThinkSystem SR650
(2.10 GHz, Intel Xeon Platinum 8176M)

SPECrate2017_fp_base = 226
SPECrate2017_fp_peak = 229

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

General Notes (Continued)

is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
SNC set to Enable
Hardware Prefetcher set to Disable
MONITORWAIT set to Enable
Execute Disable Bit set to Disable
Trusted Execution Technology set to Enable
Stale AtoS set to Enable
LLC Deadline Alloc set to Disable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on Cyborg-SUT4 Wed Jan 17 14:49:06 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Platinum 8176M CPU @ 2.10GHz
  2 "physical id"s (chips)
  112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 28

(Continued on next page)
### Lenovo Global Technology

**ThinkSystem SR650**  
(2.10 GHz, Intel Xeon Platinum 8176M)  

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</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

---

**Platform Notes (Continued)**

```plaintext
siblings : 56  
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30  
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30
```

From `lscpu`:

- **Architecture:** x86_64  
- **CPU op-mode(s):** 32-bit, 64-bit  
- **Byte Order:** Little Endian  
- **CPU(s):** 112  
- **On-line CPU(s) list:** 0-111  
- **Thread(s) per core:** 2  
- **Core(s) per socket:** 28  
- **Socket(s):** 2  
- **NUMA node(s):** 4  
- **Vendor ID:** GenuineIntel  
- **CPU family:** 6  
- **Model:** 85  
- **Model name:** Intel(R) Xeon(R) Platinum 8176M CPU @ 2.10GHz  
- **Stepping:** 4  
- **CPU MHz:** 2095.083  
- **BogoMIPS:** 4190.16  
- **Virtualization:** VT-x  
- **L1d cache:** 32K  
- **L1i cache:** 32K  
- **L2 cache:** 1024K  
- **L3 cache:** 39424K  
- **NUMA node0 CPU(s):** 0-3, 7-9, 14-17, 21-23, 56-59, 63-65, 70-73, 77-79  
- **NUMA node1 CPU(s):** 4-6, 10-13, 18-20, 24-27, 60-62, 66-69, 74-76, 80-83  
- **NUMA node2 CPU(s):** 28-31, 35-37, 42-45, 49-51, 84-87, 91-93, 98-101, 105-107  
- **NUMA node3 CPU(s):** 32-34, 38-41, 46-48, 52-55, 88-90, 94-97, 102-104, 108-111  

**Flags:**

- fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts aperfmperf acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pti pclmulqdq dtes64aktualept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2  
- erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc  

/proc/cpuinfo cache data  
- **cache size:** 39424 KB

---

From `numactl --hardware` WARNING: a numactl 'node' might or might not correspond to a physical chip.
### Platform Notes (Continued)

```plaintext
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 7 8 9 14 15 16 17 21 22 23 56 57 58 59 63 64 65 70 71 72 73 77 78
node 0 size: 96356 MB
node 0 free: 95643 MB
node 1 cpus: 4 5 6 10 11 12 13 18 19 20 24 25 26 27 60 61 62 66 67 68 69 74 75 76 80 81
node 1 size: 96753 MB
node 1 free: 95978 MB
node 2 cpus: 28 29 30 31 35 36 37 42 43 44 45 49 50 51 84 85 86 87 91 92 93 98 99 100
node 2 size: 96753 MB
node 2 free: 96164 MB
node 3 cpus: 32 33 34 38 39 40 41 46 47 48 52 53 54 55 88 89 90 94 95 96 97 102 103 104
node 3 size: 96750 MB
node 3 free: 96114 MB
node distances:
  node  0  1  2  3
  0:  10  11  21  21
  1:  11  10  21  21
  2:  21  21  10  11
  3:  21  21  11  10
```

From `/proc/meminfo`

```
MemTotal:       395892068 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
```

From `/etc/*release* /etc/*version`

```
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.

os-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME=cpe:/o:suse:sles:12:sp2
```

```
uname -a:
  Linux Cyborg-SUT4 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
```
**SPEC CPU2017 Floating Point Rate Result**

**Lenovo Global Technology**  
ThinkSystem SR650  
(2.10 GHz, Intel Xeon Platinum 8176M)  

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9017</th>
<th>Test Date:</th>
<th>Jan-2018</th>
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<tr>
<td>Test Sponsor:</td>
<td>Lenovo Global Technology</td>
<td>Hardware Availability:</td>
<td>Aug-2017</td>
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<td>Tested by:</td>
<td>Lenovo Global Technology</td>
<td>Software Availability:</td>
<td>Sep-2017</td>
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</table>

**SPECrate2017_fp_base = 226**  
**SPECrate2017_fp_peak = 229**

---

**Platform Notes (Continued)**

x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 17 03:42

SPEC is set to: /home/cpu2017.1.0.2.ic18.0

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda3</td>
<td>xfs</td>
<td>445G</td>
<td>67G</td>
<td>378G</td>
<td>15%</td>
<td>/</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[IVE113U-1.11]- 12/12/2017  
Memory:  
24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)

---

**Compiler Version Notes**

```
CC 519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
```

```
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```
CC 519.lbm_r(peak) 544.nab_r(peak)
```

```
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```
CXXC 508.namd_r(base) 510.parest_r(base)
```

```
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```
CXXC 508.namd_r(peak) 510.parest_r(peak)
```

```
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
(2.10 GHz, Intel Xeon Platinum 8176M)

---

**Compiler Version Notes (Continued)**

---

```plaintext
CC 511.povray_r(base) 526.blender_r(base)
```

---

```plaintext
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

---

```plaintext
CC 511.povray_r(peak) 526.blender_r(peak)
```

---

```plaintext
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

---

```plaintext
FC 507.cactuBSSN_r(base)
```

---

```plaintext
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

---

```plaintext
FC 507.cactuBSSN_r(peak)
```

---

```plaintext
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

---

```plaintext
FC 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
```

---

```plaintext
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

---

(Continued on next page)
Lenovo Global Technology
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SPECrater2017_fp_base = 226
SPECrater2017_fp_peak = 229

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Compiler Version Notes (Continued)

==============================================================================
FC  554.roms_r(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
CC  521.wrf_r(base) 527.cam4_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
CC   521.wrf_r(peak) 527.cam4_r(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
  icc

C++ benchmarks:
  icpc

Fortran benchmarks:
  ifort

Benchmarks using both Fortran and C:
  ifort icc

Benchmarks using both C and C++:
  icpc icc

Benchmarks using Fortran, C, and C++:
  icpc icc ifort
Lenovo Global Technology
ThinkSystem SR650
(2.10 GHz, Intel Xeon Platinum 8176M)

SPECrate2017_fp_base = 226
SPECrate2017_fp_peak = 229

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-DSPEC_LINUX -funsigned-char
### Lenovo Global Technology

**ThinkSystem SR650**

*(2.10 GHz, Intel Xeon Platinum 8176M)*

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<td>Lenovo Global Technology</td>
</tr>
</tbody>
</table>

---

#### Base Other Flags

- **C benchmarks:**
  - `m64 -std=c11`

- **C++ benchmarks:**
  - `m64`

- **Fortran benchmarks:**
  - `m64`

- **Benchmarks using both Fortran and C:**
  - `m64 -std=c11`

- **Benchmarks using both C and C++:**
  - `m64 -std=c11`

- **Benchmarks using Fortran, C, and C++:**
  - `m64 -std=c11`

---

#### Peak Compiler Invocation

- **C benchmarks:**
  - `icc`

- **C++ benchmarks:**
  - `icpc`

- **Fortran benchmarks:**
  - `ifort`

- **Benchmarks using both Fortran and C:**
  - `ifort icc`

- **Benchmarks using both C and C++:**
  - `icpc icc`

- **Benchmarks using Fortran, C, and C++:**
  - `icpc icc ifort`

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#### Peak Portability Flags

*Same as Base Portability Flags*
Lenovo Global Technology
ThinkSystem SR650
(2.10 GHz, Intel Xeon Platinum 8176M)

SPECrate2017_fp_base = 226
SPECrate2017_fp_peak = 229

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: Jan-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Peak Optimization Flags

C benchmarks:

519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: Same as 519.lbm_r

C++ benchmarks:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

Fortran benchmarks:

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
Lenovo Global Technology
ThinkSystem SR650
(2.10 GHz, Intel Xeon Platinum 8176M)

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Peak Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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