Lenovo Global Technology
ThinkSystem SR550
(3.40 GHz, Intel Xeon Gold 6128)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

CPU Name: Intel Xeon Gold 6128
Max MHz.: 3700
Nominal: 3400
Enabled: 2 cores, 2 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 19.25 MB I+D on chip per core
Other: None
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)
Storage: 1 x 800 GB SAS SSD
Other: None

SPECspeed2017_fp_base = 70.7
SPECspeed2017_fp_peak = 72.2

Test Date: Feb-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017
Lenovo Global Technology
ThinkSystem SR550
(3.40 GHz, Intel Xeon Gold 6128)

SPECspeed2017_fp_base = 70.7
SPECspeed2017_fp_peak = 72.2

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>12</td>
<td>170</td>
<td>346</td>
<td>171</td>
<td>346</td>
<td>173</td>
<td>341</td>
<td>12</td>
<td>170</td>
<td>346</td>
<td>171</td>
<td>346</td>
<td>171</td>
<td>346</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>12</td>
<td>228</td>
<td>73.3</td>
<td>228</td>
<td>73.2</td>
<td>228</td>
<td>73.2</td>
<td>12</td>
<td>218</td>
<td>76.4</td>
<td>219</td>
<td>76.3</td>
<td>218</td>
<td>76.3</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>12</td>
<td>149</td>
<td>35.2</td>
<td>148</td>
<td>35.3</td>
<td>148</td>
<td>35.3</td>
<td>12</td>
<td>143</td>
<td>36.6</td>
<td>145</td>
<td>36.2</td>
<td>143</td>
<td>36.5</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>12</td>
<td>198</td>
<td>66.8</td>
<td>198</td>
<td>66.8</td>
<td>198</td>
<td>66.7</td>
<td>12</td>
<td>186</td>
<td>71.0</td>
<td>187</td>
<td>70.7</td>
<td>186</td>
<td>71.0</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>12</td>
<td>213</td>
<td>41.6</td>
<td>213</td>
<td>41.6</td>
<td>212</td>
<td>41.8</td>
<td>12</td>
<td>213</td>
<td>41.7</td>
<td>214</td>
<td>41.5</td>
<td>212</td>
<td>41.7</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>12</td>
<td>214</td>
<td>55.4</td>
<td>214</td>
<td>55.4</td>
<td>214</td>
<td>55.6</td>
<td>12</td>
<td>206</td>
<td>57.6</td>
<td>205</td>
<td>57.9</td>
<td>204</td>
<td>58.3</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>12</td>
<td>295</td>
<td>48.8</td>
<td>293</td>
<td>49.2</td>
<td>294</td>
<td>49.0</td>
<td>12</td>
<td>295</td>
<td>48.9</td>
<td>295</td>
<td>48.9</td>
<td>295</td>
<td>49.0</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>12</td>
<td>197</td>
<td>88.7</td>
<td>197</td>
<td>88.7</td>
<td>197</td>
<td>88.7</td>
<td>12</td>
<td>197</td>
<td>88.7</td>
<td>197</td>
<td>88.6</td>
<td>197</td>
<td>88.7</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>12</td>
<td>132</td>
<td>69.0</td>
<td>134</td>
<td>68.3</td>
<td>131</td>
<td>69.3</td>
<td>12</td>
<td>134</td>
<td>68.0</td>
<td>134</td>
<td>67.8</td>
<td>135</td>
<td>67.7</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>12</td>
<td>207</td>
<td>75.9</td>
<td>208</td>
<td>75.6</td>
<td>208</td>
<td>75.8</td>
<td>12</td>
<td>201</td>
<td>78.4</td>
<td>200</td>
<td>78.7</td>
<td>199</td>
<td>79.0</td>
</tr>
</tbody>
</table>

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly

(Continued on next page)
General Notes (Continued)

Generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disable
MONITORMWAIT set to Enable
Adjacent Cache Prefetch set to Disable
LLC dead line alloc set to Disable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-g50d Fri Feb 2 01:15:34 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6128 CPU @ 3.40GHz
  2 "physical id"s (chips)
  12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
  siblings : 6
physical 0: cores 2 3 4 5 10 11
physical 1: cores 0 6 9 10 11 13

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Thread(s) per core: 1
Core(s) per socket: 6
Socket(s): 2
NUMA node(s): 2

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR550
(3.40 GHz, Intel Xeon Gold 6128)

SPECspeed2017_fp_base = 70.7
SPECspeed2017_fp_peak = 72.2

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Lenovo Global Technology</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Lenovo Global Technology</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Feb-2018</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Aug-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

Platform Notes (Continued)

Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Gold 6128 CPU @ 3.40GHz
Stepping:              4
CPU MHz:               3392.021
BogoMIPS:              6784.04
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              19712K
NUMA node0 CPU(s):     0-5
NUMA node1 CPU(s):     6-11
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                       pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
                       aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
                       fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
                       xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
                       tpr_shadow vmp首 flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
                       erms invpcid rtm cqm mpx avx512f avx512dav1 rdseed adx smap clflushopt clwb avx512cd
                       avx512bw avx512vl xsaveopt xsavec xgetbv1 cvm_llc cvm_occup_llc

/proc/cpuinfo cache data
cache size : 19712 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
   available: 2 nodes (0-1)
   node 0 cpus: 0 1 2 3 4 5
   node 0 size: 193110 MB
   node 0 free: 191468 MB
   node 1 cpus: 6 7 8 9 10 11
   node 1 size: 193504 MB
   node 1 free: 192671 MB
   node distances:
      node 0 1
       0: 10 21
       1: 21 10

From /proc/meminfo
MemTotal:       395893764 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR550
(3.40 GHz, Intel Xeon Gold 6128)

SPECspeed2017_fp_base = 70.7
SPECspeed2017_fp_peak = 72.2

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Feb-2018
Tested by: Lenovo Global Technology
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Platform Notes (Continued)

SuSE-release:
   SUSE Linux Enterprise Server 12 (x86_64)
   VERSION = 12
   PATCHLEVEL = 2
   # This file is deprecated and will be removed in a future service pack or release.
   # Please check /etc/os-release for details about this release.

os-release:
   NAME="SLES"
   VERSION="12-SP2"
   VERSION_ID="12.2"
   PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
   ID="sles"
   ANSI_COLOR="0;32"
   CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux linux-g50d 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 1 19:43

SPEC is set to: /home/cpu2017.1.0.2.ic18.0

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 btrfs 744G 158G 585G 22% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[TEE119Q-1.21]- 12/12/2017
Memory:
   12x Hynix HMA84GR7AFR4N-VK 32 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes
==============================================================================
CC  619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC  619.lbm_s(peak)

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR550
(3.40 GHz, Intel Xeon Gold 6128)

**SPEC CPU2017 Floating Point Speed Result**

<table>
<thead>
<tr>
<th>CPU2017 License: 9017</th>
<th>Test Date:</th>
<th>Feb-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Lenovo Global Technology</td>
<td>Hardware Availability:</td>
<td>Aug-2017</td>
</tr>
<tr>
<td>Tested by: Lenovo Global Technology</td>
<td>Software Availability:</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

**SPECspeed2017_fp_base = 70.7**

**SPECspeed2017_fp_peak = 72.2**

---

**Compiler Version Notes (Continued)**

```plaintext
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

--------------------------------------------------------------------------------
FC 607.cactuBSSN_s(base)
--------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------
--------------------------------------------------------------------------------
FC 607.cactuBSSN_s(peak)
--------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------
--------------------------------------------------------------------------------
FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
--------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------
--------------------------------------------------------------------------------
FC 603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)
--------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------
--------------------------------------------------------------------------------
CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
--------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
```

(Continued on next page)
**Lenovo Global Technology**  
ThinkSystem SR550  
(3.40 GHz, Intel Xeon Gold 6128)  

<table>
<thead>
<tr>
<th>CPU2017 License: 9017</th>
<th>Test Date: Feb-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lenovo Global Technology</td>
<td>Hardware Availability: Aug-2017</td>
</tr>
<tr>
<td>Tested by: Lenovo Global Technology</td>
<td>Software Availability: Sep-2017</td>
</tr>
</tbody>
</table>

**SPECspeed2017 fp_base = 70.7**  
**SPECspeed2017 fp_peak = 72.2**

### Compiler Version Notes (Continued)

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
<table>
<thead>
<tr>
<th>CC</th>
<th>621.wrf_s(peak) 628.pop2_s(peak)</th>
</tr>
</thead>
</table>
------------------------------------------------------------------------------
| ifort (IFORT) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |
| icc (ICC) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |

### Base Compiler Invocation

**C benchmarks:**

- icc

**Fortran benchmarks:**

- ifort

**Benchmarks using both Fortran and C:**

- ifort icc

**Benchmarks using Fortran, C, and C++:**

- icpc icc ifort

### Base Portability Flags

- 603.bwaves_s: -DSPEC_LP64
- 607.cactuBSSN_s: -DSPEC_LP64
- 619.lbm_s: -DSPEC_LP64
- 621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl
- 638.imagick_s: -DSPEC_LP64
- 644.nab_s: -DSPEC_LP64
- 649.fotonik3d_s: -DSPEC_LP64
- 654.roms_s: -DSPEC_LP64
## SPEC CPU2017 Floating Point Speed Result

### Lenovo Global Technology

**ThinkSystem SR550**  
(3.40 GHz, Intel Xeon Gold 6128)

| SPECspeed2017_fp_base | 70.7 |
| SPECspeed2017_fp_peak | 72.2 |

**CPU2017 License:** 9017  
**Test Date:** Feb-2018  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Hardware Availability:** Aug-2017  
**Software Availability:** Sep-2017

### Base Optimization Flags

**C benchmarks:**
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-qopenmp`  
- `-DSPEC_OPENMP`  

**Fortran benchmarks:**
- `-DSPEC_OPENMP`  
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-qopenmp`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

**Benchmarks using both Fortran and C:**
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-qopenmp`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

**Benchmarks using Fortran, C, and C++:**
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-qopenmp`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

### Base Other Flags

**C benchmarks:**
- `-m64`  
- `-std=c11`

**Fortran benchmarks:**
- `-m64`

**Benchmarks using both Fortran and C:**
- `-m64`  
- `-std=c11`

**Benchmarks using Fortran, C, and C++:**
- `-m64`  
- `-std=c11`

### Peak Compiler Invocation

**C benchmarks:**
- `icc`

**Fortran benchmarks:**
- `ifort`

(Continued on next page)
**Peak Compiler Invocation (Continued)**

Benchmarks using both Fortran and C:

```c
ifort icc
```

Benchmarks using Fortran, C, and C++:

```c
icpc icc ifort
```

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

C benchmarks:

- 619.lbm_s: `-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
  -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
  -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
  -DSPEC_OPENMP`

- 638.imagick_s: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
  -DSPEC_OPENMP`

- 644.nab_s: `Same as 638.imagick_s`

Fortran benchmarks:

- `-prof-gen(pass l) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
  -DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3
  -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
  -nostandard-realloc-lhs -align array32byte`

Benchmarks using both Fortran and C:

- 621.wrf_s: `-prof-gen(pass l) -prof-use(pass 2) -O2 -xCORE-AVX2
  -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
  -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
  -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte`

- 627.cam4_s: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
  -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte`

*(Continued on next page)*
Lenovo Global Technology  
ThinkSystem SR550  
(3.40 GHz, Intel Xeon Gold 6128)

**SPECspeed2017_fp_base = 70.7**  
**SPECspeed2017_fp_peak = 72.2**

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Feb-2018  
**Hardware Availability:** Aug-2017  
**Software Availability:** Sep-2017

---

**Peak Optimization Flags (Continued)**

```
628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
-align array32byte
```

---

**Peak Other Flags**

**C benchmarks:**
- m64 -std=c11

**Fortran benchmarks:**
- m64

**Benchmarks using both Fortran and C:**
- m64 -std=c11

**Benchmarks using Fortran, C, and C++:**
- m64 -std=c11

---

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.xml

---

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-02-01 12:15:34-0500.  
Originally published on 2018-03-06.