**SPEC® CPU2017 Integer Speed Result**

**Dell Inc.**

PowerEdge M640 (Intel Xeon Gold 6144, 3.50Ghz)  

**SPECspeed2017_int_base = 9.66**  
**SPECspeed2017_int_peak = Not Run**

---

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Nov-2017  
**Hardware Availability:** Sep-2017  
**Software Availability:** Sep-2017

---

**Threads**  
<table>
<thead>
<tr>
<th>Test</th>
<th>Threads</th>
<th>SPECspeed2017_int_base</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench_s</td>
<td>32</td>
<td>6.91</td>
</tr>
<tr>
<td>gcc_s</td>
<td>32</td>
<td>10.1</td>
</tr>
<tr>
<td>mcf_s</td>
<td>32</td>
<td>12.0</td>
</tr>
<tr>
<td>omnetpp_s</td>
<td>32</td>
<td>6.41</td>
</tr>
<tr>
<td>xalancmk_s</td>
<td>32</td>
<td>10.6</td>
</tr>
<tr>
<td>x264_s</td>
<td>32</td>
<td>13.2</td>
</tr>
<tr>
<td>deepsjeng_s</td>
<td>32</td>
<td>5.59</td>
</tr>
<tr>
<td>leela_s</td>
<td>32</td>
<td>4.92</td>
</tr>
<tr>
<td>exchange2_s</td>
<td>32</td>
<td>15.2</td>
</tr>
<tr>
<td>xz_s</td>
<td>32</td>
<td>22.8</td>
</tr>
</tbody>
</table>

---

**Hardware**  
**CPU Name:** Intel Xeon Gold 6144  
**Max MHz.:** 4200  
**Nominal:** 3500  
**Enabled:** 16 cores, 2 chips, 2 threads/core  
**Orderable:** 1.2 chips  
**Cache L1:** 32 KB I + 32 KB D on chip per core  
**L2:** 1 MB I+D on chip per core  
**L3:** 24.75 MB I+D on chip per chip  
**Other:** None  
**Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R)  
**Storage:** 960 GB SATA SSD  
**Other:** None

---

**Software**  
**OS:** SUSE Linux Enterprise Server 12 SP3 (x86_64)  
**Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
**Parallel:** Yes  
**Firmware:** Version 1.0.0 released Aug-2017  
**File System:** btrfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** 32/64-bit  
**Other:** jemalloc: jemalloc memory allocator library V5.0.1;
Dell Inc.

PowerEdge M640 (Intel Xeon Gold 6144, 3.50Ghz)  

**SPECspeed2017_int_base = 9.66**  
**SPECspeed2017_int_peak = Not Run**

**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base Seconds</th>
<th>Ratio</th>
<th>Base Seconds</th>
<th>Ratio</th>
<th>Peak Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>32</td>
<td>257</td>
<td>6.91</td>
<td>256</td>
<td>6.95</td>
<td>258</td>
<td>6.88</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>32</td>
<td>392</td>
<td>10.2</td>
<td>397</td>
<td>10.0</td>
<td>394</td>
<td>10.1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>32</td>
<td>394</td>
<td>12.0</td>
<td>390</td>
<td>12.1</td>
<td>396</td>
<td>11.9</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>32</td>
<td>258</td>
<td>6.32</td>
<td>244</td>
<td>6.69</td>
<td>255</td>
<td>6.41</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>32</td>
<td>133</td>
<td>10.7</td>
<td>135</td>
<td>10.5</td>
<td>134</td>
<td>10.6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>625.x264_s</td>
<td>32</td>
<td>134</td>
<td>13.1</td>
<td>134</td>
<td>13.2</td>
<td>134</td>
<td>13.2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>32</td>
<td>256</td>
<td>5.61</td>
<td>256</td>
<td>5.59</td>
<td>262</td>
<td>5.47</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>641.leela_s</td>
<td>32</td>
<td>347</td>
<td>4.91</td>
<td>347</td>
<td>4.92</td>
<td>347</td>
<td>4.92</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>32</td>
<td>195</td>
<td>15.0</td>
<td>194</td>
<td>15.2</td>
<td>194</td>
<td>15.2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>657.xz_s</td>
<td>32</td>
<td>271</td>
<td>22.8</td>
<td>273</td>
<td>22.7</td>
<td>269</td>
<td>23.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**General Notes**

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
OMP_STACKSIZE = "192M"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
jemalloc: sources available via jemalloc.net

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
Dell Inc. PowerEdge M640 (Intel Xeon Gold 6144, 3.50Ghz)  SPECspeed2017_int_base = 9.66  SPECspeed2017_int_peak = Not Run

General Notes (Continued)

is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Platform Notes

BIOS settings:
Virtualization Technology disabled
System Profile set to Custom
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost enabled
C States disabled
Memory Patrol Scrub disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /root/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-8d7c Fri Nov 10 07:07:28 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6144 CPU @ 3.50GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.):
cpu cores : 8
siblings : 16
  physical 0: cores 0 2 3 9 16 19 26 27
  physical 1: cores 0 2 3 9 16 19 26 27

(Continued on next page)
SPEC CPU2017 Integer Speed Result

Dell Inc. PowerEdge M640 (Intel Xeon Gold 6144, 3.50Ghz)

SPECspeed2017_int_base = 9.66
SPECspeed2017_int_peak = Not Run

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
Test Date: Nov-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Platform Notes (Continued)

From lscpu:
Architectures: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6144 CPU @ 3.50GHz
Stepping: 4
CPU MHz: 3491.795
BogoMIPS: 6983.59
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30
NUMA node1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdelgb rdtsscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpre pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
erms invpcid rtm cqm mpx avx512f avx512d avx512vd rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_11c cqm_occup_11c pku ospke

/proc/cpuinfo cache data

cache size : 25344 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30
node 0 size: 95335 MB
node 0 free: 94843 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31
node 1 size: 96736 MB
node 1 free: 96232 MB
node distances:

(Continued on next page)
SPEC CPU2017 Integer Speed Result

Dell Inc.
PowerEdge M640 (Intel Xeon Gold 6144, 3.50Ghz)

SPECspeed2017_int_base = 9.66
SPECspeed2017_int_peak = Not Run

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Nov-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Platform Notes (Continued)

From /proc/meminfo
MemTotal: 196682072 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
Linux linux-8d7c 4.4.70-2-default #1 SMP Wed Jun 7 15:12:06 UTC 2017 (4502c76) x86_64
x86_64 x86_64 GNU/Linux
run-level 3 Nov 10 06:46

SPEC is set to: /root/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 btrfs 855G 25G 831G 3% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS Dell Inc. 1.0.0 08/10/2017
Memory:
12x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666
4x Not Specified Not Specified

(End of data from sysinfo program)
Dell Inc. PowerEdge M640 (Intel Xeon Gold 6144, 3.50Ghz)

| SPECspeed2017_int_base = | 9.66 |
| SPECspeed2017_int_peak = | Not Run |

Dell Inc.

**CPU2017 License:** 55
**Test Sponsor:** Dell Inc.
**Test Date:** Nov-2017
**Hardware Availability:** Sep-2017
**Tested by:** Dell Inc.
**Software Availability:** Sep-2017

---

**Compiler Version Notes**

---

**Base Compiler Invocation**

- **C benchmarks:**
  - icc

- **C++ benchmarks:**
  - icpc

- **Fortran benchmarks:**
  - ifort

---

**Base Portability Flags**

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
```

(Continued on next page)
### Dell Inc.

**PowerEdge M640 (Intel Xeon Gold 6144, 3.50Ghz)**

<table>
<thead>
<tr>
<th>SPECspeed2017_int_base =</th>
<th>9.66</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_int_peak =</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test Date:** Nov-2017

**Hardware Availability:** Sep-2017

**Software Availability:** Sep-2017

---

#### Base Portability Flags (Continued)

- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64

#### Base Optimization Flags

**C benchmarks:**

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

**C++ benchmarks:**

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

**Fortran benchmarks:**

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

#### Base Other Flags

**C benchmarks:**

- `-m64 -std=c11`

**C++ benchmarks:**

- `-m64`

**Fortran benchmarks:**

- `-m64`

---

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

<table>
<thead>
<tr>
<th>SPECs speed2017_int_base</th>
<th>9.66</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECs speed2017_int_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

Dell Inc.  
PowerEdge M640 (Intel Xeon Gold 6144, 3.50Ghz)  

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>Dell Inc.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Test Date</td>
<td>Nov-2017</td>
</tr>
<tr>
<td>Hardware Availability</td>
<td>Sep-2017</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2017-11-10 08:07:27-0500.  
Originally published on 2018-02-27.