**Dell Inc.**  
PowerEdge C6420 (Intel Xeon Silver 4108, 1.80 GHz)  

<table>
<thead>
<tr>
<th>Test Date:</th>
<th>Feb-2018</th>
</tr>
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<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Sep-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
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**spec CPU2017 Integer Rate Result**  

<table>
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<th>55</th>
</tr>
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<tbody>
<tr>
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<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Silver 4108  
  - Max MHz.: 3000  
  - Nominal: 1800  
  - Enabled: 16 cores, 2 chips  
  - Orderable: 1,2 chips  
  - Cache L1: 32 KB I + 32 KB D on chip per core  
  - L2: 1 MB I+D on chip per core  
  - L3: 11 MB I+D on chip per chip  
  - Other: None  
  - Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
  - Storage: 1 TB SATA 7200 RPM  
  - Other: None

### Software

- **OS:** SUSE Linux Enterprise Server 12 SP3 (x86_64)  
  - 4.4.114-94.11-default  
  - Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux  
  - Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
  - Parallel: No  
  - Firmware: Version 1.3.7 released Feb-2018  
  - File System: btrfs  
  - System State: Run level 3 (multi-user)  
  - Base Pointers: 64-bit  
  - Peak Pointers: 32/64-bit  
  - Other: Jemalloc memory allocator library, version 5.0.1

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>53.5</th>
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<tr>
<td>SPECrate2017_int_peak</td>
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<th>gcc_r</th>
<th>mcf_r</th>
<th>omnetpp_r</th>
<th>xalancbmk_r</th>
<th>x264_r</th>
<th>deepsjeng_r</th>
<th>leela_r</th>
<th>exchange2_r</th>
<th>xz_r</th>
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<td>52.6</td>
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<td>37.8</td>
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500.perlbench_r: SPECrate2017_int_base (53.5)  
502.gcc_r: SPECrate2017_int_base (53.5)  
505.mcf_r: SPECrate2017_int_base (53.5)  
520.omnetpp_r: SPECrate2017_int_base (53.5)  
523.xalancbmk_r: SPECrate2017_int_base (53.5)  
525.x264_r: SPECrate2017_int_base (53.5)  
531.deepsjeng_r: SPECrate2017_int_base (53.5)  
541.leela_r: SPECrate2017_int_base (53.5)  
548.exchange2_r: SPECrate2017_int_base (53.5)  
557.xz_r: SPECrate2017_int_base (53.5)
Dell Inc.
PowerEdge C6420 (Intel Xeon Silver 4108, 1.80 GHz)

SPEC CPU2017 Integer Rate Result

SPECrate2017_int_base = 53.5
SPECrate2017_int_peak = 54.8

Results Table

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
jemalloc: sources available via jemalloc.net
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
Dell Inc.

**PowerEdge C6420 (Intel Xeon Silver 4108, 1.80 GHz)**

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</table>

General Notes (Continued)

```plaintext
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
```

Platform Notes

BIOS settings:
- Sub NUMA Cluster disabled
- Virtualization Technology disabled
- System Profile set to Custom
- CPU Performance set to Maximum Performance
- C States set to Autonomous
- C1EE disabled
- Uncore Frequency set to Dynamic
- Energy Efficiency Policy set to Performance
- Memory Patrol Scrub disabled
- Logical Processor disabled
- CPU Interconnect Bus Link Power Management disabled
- PCI ASPM L1 Link Power Management disabled
- Sysinfo program /root/cpu2017/bin/sysinfo
- Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bdc091c0f
- running on linux-vfov Thu Feb 15 15:37:17 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```plaintext
model name : Intel(R) Xeon(R) Silver 4108 CPU @ 1.80GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 8
  siblings : 8
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
```

From lscpu:

```plaintext
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Thread(s) per core: 1
Core(s) per socket: 8
```

(Continued on next page)
Dell Inc.
PowerEdge C6420 (Intel Xeon Silver 4108, 1.80 GHz)

| SPECrate2017_int_base = 53.5 |
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CPU2017 License: 55
Test Date: Feb-2018
Hardware Availability: Sep-2017
Test Sponsor: Dell Inc.
Software Availability: Sep-2017
Tested by: Dell Inc.

Platform Notes (Continued)

Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4108 CPU @ 1.80GHz
Stepping: 4
CPU MHz: 1795.777
BogoMIPS: 3591.55
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 11264K
NUMA node0 CPU(s): 0,2,4,6,8,10,12,14
NUMA node1 CPU(s): 1,3,5,7,9,11,13,15
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtrr pdcm pclid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
d Humph intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow vmi flexpriority
ep vpid fsgsb base tsc_adjust bmi1 hle avx2 smep bmi2 ertm cqm mpx
avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt
xsaves xgetbv1 cqm_l1c cqm_occup_l1c pku ospke

/proc/cpuinfo cache data
  cache size : 11264 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 2 4 6 8 10 12 14
  node 0 size: 95354 MB
  node 0 free: 95004 MB
  node 1 cpus: 1 3 5 7 9 11 13 15
  node 1 size: 96749 MB
  node 1 free: 96428 MB
  node distances:
    node  0 1
    0: 10 21
    1: 21 10

From /proc/meminfo
  MemTotal: 196715332 kB
  HugePages_Total: 0

(Continued on next page)
**SPEC CPU2017 Integer Rate Result**

**Dell Inc.**

PowerEdge C6420 (Intel Xeon Silver 4108, 1.80 GHz)

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**SPECrate2017_int_base = 53.5**

**SPECrate2017_int_peak = 54.8**

---

**Platform Notes (Continued)**

- Hugepagesize: 2048 kB

  From /etc/*release*/etc/*version*
  
  SuSE-release:
  
  SUSE Linux Enterprise Server 12 (x86_64)
  
  VERSION = 12
  
  PATCHLEVEL = 3
  
  # This file is deprecated and will be removed in a future service pack or release.
  
  # Please check /etc/os-release for details about this release.
  
  os-release:
  
  NAME="SLES"
  
  VERSION="12-SP3"
  
  VERSION_ID="12.3"
  
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
  
  ID="sles"
  
  ANSI_COLOR="0;32"
  
  CPE_NAME="cpe:/o:suse:sles:12:sp3"

  uname -a:
  
  Linux linux-vfov 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
  
  x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Feb 14 18:43

  SPEC is set to: /root/cpu2017

  Filesystem Type Size Used Avail Use% Mounted on
  /dev/sda2 btrfs 928G 25G 903G 3% /

  Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS Dell Inc. 1.3.7 02/09/2018

  Memory:
  
  6x 002C00B3002C 18ASF272PDZ-2G6D1 16 GB 2 rank 2666, configured at 2400
  
  6x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666, configured at 2400
  
  4x Not Specified Not Specified

  (End of data from sysinfo program)

---

**Compiler Version Notes**

```
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
    525.x264_r(base, peak) 557.xz_r(base, peak)
```

(Continued on next page)
Dell Inc.
PowerEdge C6420 (Intel Xeon Silver 4108, 1.80 GHz)

SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Feb-2018
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Compiler Version Notes (Continued)

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------------------------
CC 500.perlbench_r(peak) 502.gcc_r(peak)
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icc (ICC) 18.0.0 20170811
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-------------------------------
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
  541.leela_r(base)
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icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

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CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
  541.leela_r(peak)
-------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------------------------
FC 548.exchange2_r(base, peak)
-------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------------------------
Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort
## SPEC CPU2017 Integer Rate Result

Dell Inc.  
PowerEdge C6420 (Intel Xeon Silver 4108, 1.80 GHz)  

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- **Hardware Availability:** Sep-2017  
- **Software Availability:** Sep-2017  

### Base Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
- 502.gcc_r: -DSPEC_LP64  
- 505.mcf_r: -DSPEC_LP64  
- 520.omnetpp_r: -DSPEC_LP64  
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
- 525.x264_r: -DSPEC_LP64  
- 531.deepsjeng_r: -DSPEC_LP64  
- 541.leela_r: -DSPEC_LP64  
- 548.exchange2_r: -DSPEC_LP64  
- 557.xz_r: -DSPEC_LP64  

### Base Optimization Flags

**C benchmarks:**  
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc  

**C++ benchmarks:**  
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc  

**Fortran benchmarks:**  
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc  

### Base Other Flags

**C benchmarks:**  
-m64 -std=c11  

**C++ benchmarks:**  
-m64  

**Fortran benchmarks:**  
-m64
Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -03 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/jemalloc

502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -03 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/jemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/jemalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=3 -fno-alias
Peak Optimization Flags (Continued)

525.x264_r (continued):
-L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Other Flags

C benchmarks (except as noted below):
-m64 -std=c11

502.gcc_r: -m32 -std=c11

C++ benchmarks (except as noted below):
-m64

523.xalancbmk_r: -m32

Fortran benchmarks:
-m64

The flags files that were used to format this result can be browsed at
# SPEC CPU2017 Integer Rate Result

**Dell Inc.**  
**PowerEdge C6420 (Intel Xeon Silver 4108, 1.80 GHz)**  

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You can also download the XML flags sources by saving the following links:

- [Dell-Platform-Flags-PowerEdge14G-revC.xml](http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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