**Epsylon Sp. z o.o. Sp. Komandytowa**

**eterio 115 RE1 (Intel Xeon Silver 4109T, 2.00 GHz)**

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS: Red Hat Enterprise Linux Server release 7.4 (Maipo) 3.10.0-693.21.1.el7.x86_64</td>
<td>CPU Name: Intel Xeon Silver 4109T Max MHz.: 3000 Nominal: 2000</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux: Fortran: Version 18.0.0.128 of Intel Fortran</td>
<td>Orderable: 1 chip</td>
</tr>
<tr>
<td>Parallel: No</td>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Firmware: Version BIOS 2.0b released Feb-2018</td>
<td>L2: 1 MB I+D on chip per core</td>
</tr>
<tr>
<td>File System: ext4</td>
<td>L3: 11 MB I+D on chip per chip</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
<td>Other: None</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
<td>Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2666V-R, running at 2400)</td>
</tr>
<tr>
<td>Peak Pointers: 32/64-bit</td>
<td>Storage: 1 x 960 GB SSD SATA III</td>
</tr>
<tr>
<td>Other: jemalloc memory allocator library V5.0.1</td>
<td>Other: None</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9081  **Test Date:** Apr-2018

**Test Sponsor:** Epsylon Sp. z o.o. Sp. Komandytowa  **Hardware Availability:** Sep-2017

**Tested by:** Epsylon Sp. z o.o. Sp. Komandytowa  **Software Availability:** Mar-2018

| SPECrate2017_int_base = 36.4 |
| SPECrate2017_int_peak = 37.1 |

**CPU2017 Integer Rate Result**

Copyright 2017-2018 Standard Performance Evaluation Corporation
SPEC CPU2017 Integer Rate Result

Epsylon Sp. z o.o. Sp. Komandytowa

erio 115 RE1 (Intel Xeon Silver 4109T, 2.00 GHz)

SPECrate2017_int_base = 36.4
SPECrate2017_int_peak = 37.1

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
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<tbody>
<tr>
<td>500.perlbench_r</td>
<td>16</td>
<td>931</td>
<td>27.4</td>
<td>935</td>
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<td>27.3</td>
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<td>505.mcf_r</td>
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<td>555</td>
<td>46.6</td>
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<td>27.9</td>
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<td>961</td>
<td>27.6</td>
<td>946</td>
<td>28.0</td>
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<tr>
<td>548.exchange2_r</td>
<td>16</td>
<td>638</td>
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<td>643</td>
<td>65.2</td>
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<td>65.7</td>
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<td>708</td>
<td>24.4</td>
</tr>
</tbody>
</table>

SPECrate2017_int_base = 36.4
SPECrate2017_int_peak = 37.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/cpu2017.1.0/lib/ia32:/cpu2017.1.0/lib/intel64:/cpu2017.1.0/je5.0.1-32:/cpu2017.1.0/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32 GB RAM memory using Redhat Enterprise Linux 7.4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:

(Continued on next page)
Epsylon Sp. z o.o. Sp. Komandytowa
eterio 115 RE1 (Intel Xeon Silver 4109T, 2.00 GHz)

SPECrate2017_int_base = 36.4
SPECrate2017_int_peak = 37.1

CPU2017 License: 9081
Test Date: Apr-2018
Test Sponsor: Epsylon Sp. z o.o. Sp. Komandytowa
Hardware Availability: Sep-2017
Tested by: Epsylon Sp. z o.o. Sp. Komandytowa
Software Availability: Mar-2018

General Notes (Continued)

sync; echo 3> /proc/sys/vm/drop_caches

ejemalloc:
configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
sources available via jemalloc.net or https://github.com/jemalloc/jemalloc/releases

Platform Notes

BIOS Settings:
Power Technology = Custom
Turbo Mode = Enable
Enhanced Halt State (C1E) = Disable
CPU C6 report = Disabled
Package C State = No limit
Software Controlled T-States = Disable
Hyper-Threading (All) = Enable
Enforce POR = Disable
Memory Frequency = Auto
Patrol Scrub = Disabled
IMC Interleaving = Auto
SNC = Disabled

Sysinfo program /cpu2017.1.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on SUT Wed Apr 25 14:31:14 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4109T CPU @ 2.00GHz
 1 "physical id"s (chips)
16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)
### SPEC CPU2017 Integer Rate Result

**Epsylon Sp. z o.o. Sp. Komandytowa**

**eterio 115 RE1 (Intel Xeon Silver 4109T, 2.00 GHz)**

**SPECrate2017_int_base = 36.4**

**SPECrate2017_int_peak = 37.1**

---

**CPU2017 License:** 9081  
**Test Sponsor:** Epsylon Sp. z o.o. Sp. Komandytowa  
**Test Date:** Apr-2018  
**Tested by:** Epsylon Sp. z o.o. Sp. Komandytowa

**Hardware Availability:** Sep-2017  
**Software Availability:** Mar-2018

---

**Platform Notes (Continued)**

- **Byte Order:** Little Endian
- **CPU(s):** 16
- **On-line CPU(s) list:** 0-15
- **Thread(s) per core:** 2
- **Core(s) per socket:** 8
- **Socket(s):** 1
- **NUMA node(s):** 1
- **Vendor ID:** GenuineIntel
- **CPU family:** 6
- **Model:** 85
- **Model name:** Intel(R) Xeon(R) Silver 4109T CPU @ 2.00GHz
- **Stepping:** 4
- **CPU MHz:** 2001.000
- **CPU max MHz:** 2001.0000
- **CPU min MHz:** 800.0000
- **BogoMIPS:** 4000.00
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 11264K
- **NUMA node0 CPU(s):** 0-15
- **Flags:** fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma cx16 xptr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 invpcid_single intel_pt spec_ctrl ibpb_support tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erts invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts

```
"/proc/cpuinfo cache data
  cache size : 11264 KB
"
```

---

From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a physical chip.

- **available:** 1 nodes (0)
- **node 0 cpus:** 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
- **node 0 size:** 129708 MB
- **node 0 free:** 126090 MB
- **node distances:**
  - node 0
  - 0: 10

---

From /proc/meminfo

---

(Continued on next page)
SPEC CPU2017 Integer Rate Result

Epsylon Sp. z o.o. Sp. Komandytowa

Eterio 115 RE1 (Intel Xeon Silver 4109T, 2.00 GHz)

SPECrate2017_int_base = 36.4
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CPU2017 License: 9081
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Software Availability: Mar-2018

Platform Notes (Continued)

MemTotal: 130428632 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
    os-release:
        NAME="Red Hat Enterprise Linux Server"
        VERSION="7.4 (Maipo)"
        ID="rhel"
        ID_LIKE="fedora"
        VARIANT="Server"
        VARIANT_ID="server"
        VERSION_ID="7.4"
        PRETTY_NAME="Red Hat Enterprise Linux"
    redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
    system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:
  Linux SUT 3.10.0-693.21.1.el7.x86_64 #1 SMP Fri Feb 23 18:54:16 UTC 2018 x86_64 x86_64
  x86_64 GNU/Linux

run-level 3 Apr 25 14:28

SPEC is set to: /cpu2017.1.0
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/sda1      ext4  825G   68G  716G   9% /

    Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    BIOS American Megatrends Inc. 2.0b 02/26/2018
    Memory:
        8x Samsung M393A2G40EB2-CTD 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
     525.x264_r(base, peak) 557.xz_r(base, peak)
==============================================================================

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

(Continued on next page)
## Epsylon Sp. z o.o. Sp. Komandytowa

Eterio 115 RE1 (Intel Xeon Silver 4109T, 2.00 GHz)

<table>
<thead>
<tr>
<th>SPECrate2017_int_peak</th>
<th>SPECrate2017_int_base</th>
</tr>
</thead>
<tbody>
<tr>
<td>37.1</td>
<td>36.4</td>
</tr>
</tbody>
</table>

### Compiler Version Notes (Continued)

```plaintext
CC  500.perlbach_r(peak) 502.gcc_r(peak)

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXCC 520.omnetpp_r(base) 523.xalanchbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXCC 520.omnetpp_r(peak) 523.xalanchbmk_r(peak) 531.deepsjeng_r(peak)
541.leela_r(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC  548.exchange2_r(base, peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

### Base Compiler Invocation

C benchmarks:

- icc

C++ benchmarks:

- icpc

Fortran benchmarks:

- ifort
## SPEC CPU2017 Integer Rate Result

<table>
<thead>
<tr>
<th>Epsylon Sp. z o.o. Sp. Komandytowa</th>
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### CPU2017 License: 9081

#### Test Sponsor:
Epsylon Sp. z o.o. Sp. Komandytowa

#### Tested by:
Epsylon Sp. z o.o. Sp. Komandytowa

### Test Date:
Apr-2018

### Hardware Availability:
Sep-2017

### Software Availability:
Mar-2018

---

### Base Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td><code>-DSPEC_LP64 -DSPEC_LINUX_X64</code></td>
</tr>
<tr>
<td>502.gcc_r</td>
<td><code>-DSPEC_LP64</code></td>
</tr>
<tr>
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<td>523.xalancbmk_r</td>
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<tr>
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<td><code>-DSPEC_LP64</code></td>
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</tr>
<tr>
<td>548.exchange2_r</td>
<td><code>-DSPEC_LP64</code></td>
</tr>
<tr>
<td>557.xz_r</td>
<td><code>-DSPEC_LP64</code></td>
</tr>
</tbody>
</table>

---

### Base Optimization Flags

#### C benchmarks:

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`  
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

#### C++ benchmarks:

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`  
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

#### Fortran benchmarks:

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`  
- `-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`  
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

---

### Base Other Flags

#### C benchmarks:

- `-m64 -std=c11`

#### C++ benchmarks:

- `-m64`

#### Fortran benchmarks:

- `-m64`
## SPEC CPU2017 Integer Rate Result

**Epsylon Sp. z o.o. Sp. Komandytowa**

**eterio 115 RE1 (Intel Xeon Silver 4109T, 2.00 GHz)**

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<thead>
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<th>CPU2017 License: 9081</th>
<th>Test Date: Apr-2018</th>
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<tbody>
<tr>
<td>Tested by: Epsylon Sp. z o.o. Sp. Komandytowa</td>
<td>Software Availability: Mar-2018</td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base = 36.4**

**SPECrate2017_int_peak = 37.1**

### Peak Compiler Invocation

C benchmarks:
- icc

C++ benchmarks:
- icpc

Fortran benchmarks:
- ifort

### Peak Portability Flags

- 500.perlbench_r: `-DSPEC_LP64` `-DSPEC_LINUX_X64`
- 502.gcc_r: `-D_FILE_OFFSET_BITS=64`
- 505.mcf_r: `-DSPEC_LP64`
- 520.omnetpp_r: `-DSPEC_LP64`
- 523.xalancbmk_r: `-D_FILE_OFFSET_BITS=64` `-DSPEC_LINUX`
- 525.x264_r: `-DSPEC_LP64`
- 531.deepsjeng_r: `-DSPEC_LP64`
- 541.leela_r: `-DSPEC_LP64`
- 548.exchange2_r: `-DSPEC_LP64`
- 557.xz_r: `-DSPEC_LP64`

### Peak Optimization Flags

**C benchmarks:**

- 500.perlbench_r: `-Wl,-z,muldefs` `-prof-gen(pass 1)` `-prof-use(pass 2)` `-ipo` `-xCORE-AVX512` `-O3` `-no-prec-div` `-qopt-mem-layout-trans=3` `-fno-strict-overflow` `-L/usr/local/je5.0.1-64/lib` `-ljemalloc`


- 505.mcf_r: `-Wl,-z,muldefs` `-xCORE-AVX512` `-ipo` `-03` `-no-prec-div` `-qopt-mem-layout-trans=3` `-L/usr/local/je5.0.1-64/lib` `-ljemalloc`

- 525.x264_r: `-Wl,-z,muldefs` `-xCORE-AVX512` `-ipo` `-03` `-no-prec-div` `-qopt-mem-layout-trans=3` `-fno-alias`

*(Continued on next page)*
SPEC CPU2017 Integer Rate Result

Epsylon Sp. z o.o. Sp. Komandytowa
Eterio 115 RE1 (Intel Xeon Silver 4109T, 2.00 GHz)

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| CPU2017 License: | 9081 |
| Test Sponsor: | Epsylon Sp. z o.o. Sp. Komandytowa |
| Tested by: | Epsylon Sp. z o.o. Sp. Komandytowa |
| Test Date: | Apr-2018 |
| Hardware Availability: | Sep-2017 |
| Software Availability: | Mar-2018 |

Peak Optimization Flags (Continued)

525.x264_r (continued):
- L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -03 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -03 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Other Flags

C benchmarks (except as noted below):
-m64 -std=c11

502.gcc_r: -m32 -std=c11

C++ benchmarks (except as noted below):
-m64

523.xalancbmk_r: -m32

Fortran benchmarks:
-m64

The flags files that were used to format this result can be browsed at
<table>
<thead>
<tr>
<th>SPEC CPU2017 Integer Rate Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>Epsylon Sp. z o.o. Sp. Komandytowa</td>
</tr>
<tr>
<td>eterio 115 RE1 (Intel Xeon Silver 4109T, 2.00 GHz)</td>
</tr>
<tr>
<td>SPECrate2017_int_base = 36.4</td>
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<tr>
<td>SPECrate2017_int_peak = 37.1</td>
</tr>
</tbody>
</table>

| CPU2017 License: | 9081 |
| Test Sponsor: | Epsylon Sp. z o.o. Sp. Komandytowa |
| Tested by: | Epsylon Sp. z o.o. Sp. Komandytowa |
| Test Date: | Apr-2018 |
| Hardware Availability: | Sep-2017 |
| Software Availability: | Mar-2018 |

You can also download the XML flags sources by saving the following links:


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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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