**Lenovo Global Technology**  
ThinkSystem SD530  
(2.10 GHz, Intel Xeon Platinum 8176M)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base = 119</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_fp_peak = 120</td>
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</tbody>
</table>

**CPU2017 License:** 9017  
**Test Date:** Apr-2018  
**Test Sponsor:** Lenovo Global Technology  
**Hardware Availability:** Aug-2017  
**Tested by:** Lenovo Global Technology  
**Software Availability:** Sep-2017

**Threads**

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base (119)</th>
<th>SPECspeed2017_fp_peak (120)</th>
</tr>
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<tbody>
<tr>
<td>603.bwaves_s 56</td>
<td>177</td>
</tr>
<tr>
<td>607.cactuBSSN_s 56</td>
<td>42.7</td>
</tr>
<tr>
<td>619.lbm_s 56</td>
<td>42.5</td>
</tr>
<tr>
<td>621.wrf_s 56</td>
<td>82.9</td>
</tr>
<tr>
<td>627.cam4_s 56</td>
<td>86.3</td>
</tr>
<tr>
<td>628.pop2_s 56</td>
<td>97.3</td>
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<tr>
<td>628.pop2_s 56</td>
<td>97.2</td>
</tr>
<tr>
<td>638.imagick_s 56</td>
<td>50.6</td>
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<tr>
<td>644.nab_s 56</td>
<td>37.2</td>
</tr>
<tr>
<td>649.fotonik3d_s 56</td>
<td>139</td>
</tr>
<tr>
<td>654.roms_s 56</td>
<td>261</td>
</tr>
<tr>
<td>654.roms_s 56</td>
<td>262</td>
</tr>
<tr>
<td>654.roms_s 56</td>
<td>77.5</td>
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<tr>
<td>654.roms_s 56</td>
<td>79.3</td>
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<tr>
<td>654.roms_s 56</td>
<td>141</td>
</tr>
<tr>
<td>654.roms_s 56</td>
<td>144</td>
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</tbody>
</table>

**Hardware**

<table>
<thead>
<tr>
<th>CPU Name: Intel Xeon Platinum 8176M</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz.: 3800</td>
</tr>
<tr>
<td>Nominal: 2100</td>
</tr>
<tr>
<td>Enabled: 56 cores, 2 chips</td>
</tr>
<tr>
<td>Orderable: 1.2 chips</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2: 1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3: 38.5 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other: None</td>
</tr>
<tr>
<td>Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)</td>
</tr>
<tr>
<td>Storage: 1 x 800 GB SAS SSD</td>
</tr>
<tr>
<td>Other: None</td>
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</table>

**Software**

<table>
<thead>
<tr>
<th>OS: SUSE Linux Enterprise Server 12 SP2 (x86_64)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Kernel 4.4.114-92.64-default</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 18.0.0.128 of Intel C/C++</td>
</tr>
<tr>
<td>Compiler for Linux:</td>
</tr>
<tr>
<td>Fortran: Version 18.0.0.128 of Intel Fortran</td>
</tr>
<tr>
<td>Compiler for Linux:</td>
</tr>
<tr>
<td>Parallel: Yes</td>
</tr>
<tr>
<td>Firmware: Lenovo BIOS Version TEE121Q 1.30 released Feb-2018</td>
</tr>
<tr>
<td>File System: xfs</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>Peak Pointers: 64-bit</td>
</tr>
<tr>
<td>Other: None</td>
</tr>
</tbody>
</table>
# SPEC CPU2017 Floating Point Speed Result

**Lenovo Global Technology**

ThinkSystem SD530  
(2.10 GHz, Intel Xeon Platinum 8176M)

---

**SPECspeed2017_fp_base = 119**  
**SPECspeed2017_fp_peak = 120**

---

## Results Table

<table>
<thead>
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<th>Benchmark</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>131</td>
<td>449</td>
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<td>97.3</td>
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<td>644.nab_s</td>
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<tr>
<td>649.fotonik3d_s</td>
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<td>654.roms_s</td>
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<td>141</td>
<td>109</td>
<td>145</td>
<td>110</td>
<td>143</td>
</tr>
</tbody>
</table>

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
OMP_STACKSIZE = "192M"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop_caches  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance  
Hyper-Threading set to Disable

(Continued on next page)
Lenovo Global Technology
ThinkSystem SD530
(2.10 GHz, Intel Xeon Platinum 8176M)

SPECspeed2017_fp_base = 119
SPECspeed2017_fp_peak = 120

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Apr-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Platform Notes (Continued)

DCU Streamer Prefetcher set to Disable
MONITOR/MWAIT set to Enable
Trusted Execution Technology set to Enable
DCA set to Enable
Stale AtoS set to Enable
LLC dead line alloc set to Disable

Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on Stark-02 Thu Apr 26 14:52:43 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8176M CPU @ 2.10GHz
2 "physical id"s (chips)
56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 28
siblings : 28
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 56
On-line CPU(s) list: 0-55
Thread(s) per core: 1
Core(s) per socket: 28
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Platinum 8176M CPU @ 2.10GHz
Stepping: 4
CPU MHz: 2095.070
BogoMIPS: 4190.14
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K

(Continued on next page)
Lenovo Global Technology
ThinkSystem SD530
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Platform Notes (Continued)

L2 cache: 1024K
L3 cache: 39424K
NUMA node0 CPU(s): 0-27
NUMA node1 CPU(s): 28-55
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl x87 vmx smt cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl x87 vmx smt cmov

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
    available: 2 nodes (0-1)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
    node 0 size: 193109 MB
    node 0 free: 192184 MB
    node 1 cpus: 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
    node 1 size: 193504 MB
    node 1 free: 192690 MB
    node distances:
    node 0 1
    0: 10 21
    1: 21 10

From /proc/meminfo
    MemTotal: 395892652 kB
    HugePages_Total: 0
    Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
    SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 2
        # This file is deprecated and will be removed in a future service pack or release.
        # Please check /etc/os-release for details about this release.
        os-release:
            NAME="SLES"

(Continued on next page)
Lenovo Global Technology
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SPECspeed2017_fp_base = 119
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CPU2017 License: 9017
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Platform Notes (Continued)

VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux Stark-02 4.4.114-92.64-default #1 SMP Thu Feb 1 19:18:19 UTC 2018 (c6ce5db)
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Apr 26 09:50
SPECSPEC is set to: /home/cpu2017.1.0.2.ic18.0

Filesystem   Type   Size  Used   Avail Use% Mounted on
/dev/sda4    xfs    689G  114G  576G  17%  /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[TEE121Q-1.30]- 02/07/2018
Memory:
4x NO DIMM NO DIMM
12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

--------------------------------------------------------------------------------------------------
CC  619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
--------------------------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------

--------------------------------------------------------------------------------------------------
CC  619.lbm_s(peak)
--------------------------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------

--------------------------------------------------------------------------------------------------
FC  607.cactuBSSN_s(base)

(Continued on next page)
Lenovo Global Technology
ThinkSystem SD530
(2.10 GHz, Intel Xeon Platinum 8176M)

SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed2017_fp_base = 119
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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Apr-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Compiler Version Notes (Continued)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

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---

FC 607.cactuBSSN_s(peak)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------

---

FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------

---

FC 603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------

---

CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
iccc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

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CC 621.wrf_s(peak) 628.pop2_s(peak)
ifort (IFORT) 18.0.0 20170811

(Continued on next page)
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Compiler Version Notes (Continued)

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icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

(Continued on next page)
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Base Optimization Flags (Continued)

Fortran benchmarks (continued):
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort
SPEC CPU2017 Floating Point Speed Result

Lenovo Global Technology
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Hardware Availability: Aug-2017
Tested by: Lenovo Global Technology
Software Availability: Sep-2017

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
-align array32byte
# SPEC CPU2017 Floating Point Speed Result

## Lenovo Global Technology

ThinkSystem SD530  
(2.10 GHz, Intel Xeon Platinum 8176M)

---

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_peak</th>
<th>SPECspeed2017_fp_base</th>
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**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
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**Test Date:** Apr-2018  
**Hardware Availability:** Aug-2017  
**Software Availability:** Sep-2017

---

### Peak Other Flags

- C benchmarks:  
  - `-m64 -std=c11`

- Fortran benchmarks:  
  - `-m64`

- Benchmarks using both Fortran and C:  
  - `-m64 -std=c11`

- Benchmarks using Fortran, C, and C++:  
  - `-m64 -std=c11`

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The flags files that were used to format this result can be browsed at:  
- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html)  
- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.html](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.html)

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You can also download the XML flags sources by saving the following links:  
- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)  
- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.xml](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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