Huawei

Huawei XH321 V5 (Intel Xeon Gold 6152)

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
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</tr>
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<tbody>
<tr>
<td>SPECrate2017_int_peak</td>
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CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

<table>
<thead>
<tr>
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<td>500.perlbench_r</td>
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<td>502.gcc_r</td>
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<td>244</td>
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</tr>
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<td>232</td>
</tr>
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</tr>
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<td>146</td>
</tr>
</tbody>
</table>

Hardware

CPU Name: Intel Xeon Gold 6152
Max MHz.: 3700
Nominal: 2100
Enabled: 44 cores, 2 chips, 2 threads/core
Orderable: 1.2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 30.25 MB I+D on chip per chip
Other: None
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)
Storage: 1 x 1200 GB SAS, 10000 RPM
Other: None

Software

OS: Red Hat Enterprise Linux Server release 7.3 (Maipo) 3.10.0-693.11.6.el7.x86_64
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;
Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
Parallel: No
Firmware: Version 0.59 Released Feb-2018
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc: jemalloc memory allocator library V5.0.1;
**Huawei**

**Huawei XH321 V5 (Intel Xeon Gold 6152)**

**SPECrate2017_int_base = 205**

**SPECrate2017_int_peak = 219**

**Results Table**

<table>
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<tr>
<th>Benchmark</th>
<th>Copies</th>
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<td>146</td>
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</tr>
</tbody>
</table>

**Results appear in the order in which they were run. Bold underlined text indicates a median measurement.**

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**General Notes**

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/spec/lib/ia32:/spec/lib/intel64:/spec/je5.0.1-32:/spec/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
 sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc: configured and built at default for
32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4,
and the system compiler gcc 4.8.5;
jemalloc: sources available from jemalloc.net or

(Continued on next page)
## SPEC CPU2017 Integer Rate Result

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### General Notes (Continued)


Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

### Platform Notes

- BIOS configuration:
- Power Policy Set to Performance
- SNC Set to Enabled
- IMC Interleaving Set to 1-way Interleave
- XPT Prefetch Set to Enabled
- ADDDC Sparing Set to Disabled
- Sysinfo program /spec/bin/sysinfo
- Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
- running on localhost.localdomain Fri May 18 14:40:07 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz
- 2 "physical id"s (chips)
- 88 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 22
  - siblings: 44
  - physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
  - physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 88
- On-line CPU(s) list: 0-87
- Thread(s) per core: 2
- Core(s) per socket: 22
- Socket(s): 2
- NUMA node(s): 4
- Vendor ID: GenuineIntel

(Continued on next page)
Huawei
Huawei XH321 V5 (Intel Xeon Gold 6152)

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Platform Notes (Continued)

CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz
Stepping: 4
CPU MHz: 2100.000
BogoMIPS: 4204.47
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 30976K
NUMA node0 CPU(s): 0-2,6-8,11-13,17,18,44-46,50-52,55-57,61,62
NUMA node1 CPU(s): 3-5,9,10,14-16,19-21,47-49,53,54,58-60,63-65
NUMA node2 CPU(s): 22-24,28-30,33-35,39,40,66-68,72-74,77-79,83,84
NUMA node3 CPU(s): 25-27,31,32,36-38,41-43,69-71,75,76,80-82,85-87

/platform/cpuinfo cache data
  cache size : 30976 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0-3)
  node 0 cpus: 0 1 2 6 7 8 11 12 13 17 18 44 45 46 50 51 52 55 56 57 61 62
  node 0 size: 96437 MB
  node 0 free: 93113 MB
  node 1 cpus: 3 4 5 9 10 14 15 16 19 20 21 47 48 49 53 54 58 59 60 63 64 65
  node 1 size: 98304 MB
  node 1 free: 95997 MB
  node 2 cpus: 22 23 24 28 29 30 33 34 35 39 40 66 67 68 72 73 74 77 78 79 83 84
  node 2 size: 98304 MB
  node 2 free: 95945 MB
  node 3 cpus: 25 26 27 31 32 36 37 38 41 42 43 69 70 71 75 76 80 81 82 85 86 87
  node 3 size: 98304 MB
  node 3 free: 96004 MB
  node distances:
  node 0 1 2 3
  0: 10 11 21 21
  1: 11 10 21 21
  2: 21 21 10 11
  3: 21 21 11 10

From /proc/meminfo
  MemTotal: 394174484 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

(Continued on next page)
## SPEC CPU2017 Integer Rate Result

### Huawei

**Huawei XH321 V5 (Intel Xeon Gold 6152)**

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### Platform Notes (Continued)

```
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.3 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.3"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
    redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
    system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

uname -a:
    Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 18 14:39

SPEC is set to: /spec
    Filesystem  Type  Size  Used  Avail  Use%  Mounted on
    /dev/sda8    xfs    325G   26G   300G   8%  /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the 'DMTF SMBIOS' standard.

BIOS INSYDE Corp. 0.59 02/24/2018

Memory:
    4x NO DIMM NO DIMM
    12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)
```

### Compiler Version Notes

```
==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
    525.x264_r(base, peak) 557.xz_r(base, peak)
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

(Continued on next page)
Huawei
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Compiler Version Notes (Continued)

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
541.leela_r(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 548.exchange2_r(base, peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64

(Continued on next page)
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Base Portability Flags (Continued)

505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Peak Compiler Invocation

C benchmarks:
icc

(Continued on next page)
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### Peak Compiler Invocation (Continued)

C++ benchmarks:
- icpc

Fortran benchmarks:
- ifort

### Peak Portability Flags

<table>
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<td>-DSPEC_LP64 -DSPEC_LINUX_X64</td>
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<tr>
<td>502.gcc_r</td>
<td>-D_FILE_OFFSET_BITS=64</td>
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### Peak Optimization Flags

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<td>-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3 -fno-strict-overflow -L/usr/local/je5.0.1-64/lib -ljemalloc</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-32/lib -ljemalloc</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>basepeak = yes</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -fno-alias -L/usr/local/je5.0.1-64/lib -ljemalloc</td>
</tr>
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### Peak Optimization Flags (Continued)

**C++ benchmarks:**

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: `-L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32`
`-W1,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo`
`-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3`
`-L/usr/local/je5.0.1-32/lib -ljemalloc`

531.deepsjeng_r: basepeak = yes

541.leela_r: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo`
`-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3`
`-L/usr/local/je5.0.1-64/lib -ljemalloc`

**Fortran benchmarks:**

`-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`
`-L/usr/local/je5.0.1-64/lib -ljemalloc`

### Peak Other Flags

**C benchmarks (except as noted below):**

`-m64 -std=c11`

502.gcc_r: `-m32 -std=c11`

**C++ benchmarks (except as noted below):**

`-m64`

523.xalancbmk_r: `-m32`

**Fortran benchmarks:**

`-m64`

---

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)
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