## Huawei

**Huawei XH321 V5 (Intel Xeon Platinum 8164)**

### SPECrate2017_fp_base = 213

### SPECrate2017_fp_peak = 217

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU Name:</strong> Intel Xeon Platinum 8164</td>
<td><strong>OS:</strong> Red Hat Enterprise Linux Server release 7.3 (Maipo)</td>
</tr>
<tr>
<td><strong>Max MHz.:</strong> 3700</td>
<td><strong>Compiler:</strong> C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td><strong>Nominal:</strong> 2000</td>
<td><strong>Parallel:</strong> No</td>
</tr>
<tr>
<td><strong>Enabled:</strong> 52 cores, 2 chips, 2 threads/core</td>
<td><strong>Firmware:</strong> Version 0.59 Released Feb-2018</td>
</tr>
<tr>
<td><strong>Orderable:</strong> 1.2 chips</td>
<td><strong>File System:</strong> xfs</td>
</tr>
<tr>
<td><strong>Cache L1:</strong> 32 KB I + 32 KB D on chip per core</td>
<td><strong>System State:</strong> Run level 3 (multi-user)</td>
</tr>
<tr>
<td><strong>L2:</strong> 1 MB I+D on chip per core</td>
<td><strong>Base Pointers:</strong> 64-bit</td>
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<tr>
<td><strong>L3:</strong> 35.75 MB I+D on chip per chip</td>
<td><strong>Peak Pointers:</strong> 64-bit</td>
</tr>
<tr>
<td><strong>Other:</strong> None</td>
<td><strong>Other:</strong> None</td>
</tr>
<tr>
<td><strong>Memory:</strong> 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)</td>
<td></td>
</tr>
<tr>
<td><strong>Storage:</strong> 1 x 1200 GB SAS, 10000 RPM</td>
<td></td>
</tr>
<tr>
<td><strong>Other:</strong> None</td>
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Huawei

Huawei XH321 V5 (Intel Xeon Platinum 8164)

Results Table

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<td>251</td>
<td>631</td>
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<td>527.cam4_r</td>
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<td>544.nab_r</td>
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<td>332</td>
<td>528</td>
<td>331</td>
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<td>549.fotonik3d_r</td>
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<td>554.roms_r</td>
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<td>1727</td>
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</tr>
</tbody>
</table>

SPECrate2017_fp_base = 213
SPECrate2017_fp_peak = 217

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/spec/lib/ia32;/spec/lib/intel64;/spec/je5.0.1-32;/spec/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
Huawei

Huawei XH321 V5 (Intel Xeon Platinum 8164)

Huawei

SPECrate2017_fp_base = 213
SPECrate2017_fp_peak = 217

CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Apr-2018
Tested by: Huawei
Hardware Availability: Jul-2017
Software Availability: Jan-2018

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Power Policy Set to Performance
SNC Set to Enabled
IMC Interleaving Set to 1-way Interleave
XPT Prefetch Set to Enabled
ADDDC Sparing Set to Disabled
Sysinfo program /spec/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on localhost.localdomain Sat Apr 21 06:21:08 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Platinum 8164 CPU @ 2.00GHz
  2 "physical id"s (chips)
  104 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 26
  siblings : 52
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28 29
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28 29

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  CPU(s): 104
  On-line CPU(s) list: 0-103
  Thread(s) per core: 2
  Core(s) per socket: 26
  Socket(s): 2
  NUMA node(s): 4
  Vendor ID: GenuineIntel
  CPU family: 6

(Continued on next page)
Huawei

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Test Sponsor: Huawei
Hardware Availability: Jul-2017
Test Date: Apr-2018
Tested by: Huawei
Software Availability: Jan-2018

Platform Notes (Continued)

Model: 85
Model name: Intel(R) Xeon(R) Platinum 8164 CPU @ 2.00GHz
Stepping: 4
CPU MHz: 2000.000
BogoMIPS: 4004.56
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-3,7-9,13-15,20-22,52-55,59-61,65-67,72-74
NUMA node1 CPU(s): 4-6,10-12,16-19,23-25,56-58,62-64,68-71,75-77
NUMA node2 CPU(s): 26-29,33-35,39-41,46-48,78-81,85-87,91-93,98-100
NUMA node3 CPU(s): 30-32,36-38,42-45,49-51,82-84,88-90,94-97,101-103

/proc/cpuinfo cache data

cache size : 36608 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
a available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 7 8 9 13 14 15 20 21 22 52 53 54 55 59 60 61 65 66 67 72 73 74
node 0 size: 96437 MB
node 0 free: 93233 MB
node 1 cpus: 4 5 6 10 11 12 16 17 18 19 23 24 25 56 57 58 62 63 64 68 69 70 71 75 76 77
node 1 size: 98304 MB
node 1 free: 95364 MB
node 2 cpus: 26 27 28 29 33 34 35 39 40 41 46 47 48 78 79 80 81 85 86 87 91 92 93 98 99
node 2 size: 98304 MB
node 2 free: 94659 MB
node 3 cpus: 30 31 32 36 37 38 42 43 44 45 49 50 51 82 83 84 88 89 90 94 95 96 97 101
node 3 size: 98304 MB
node 3 free: 95347 MB
node distances:
node 0 1 2 3
0: 10 11 21 21
1: 11 10 21 21
2: 21 21 10 11
3: 21 21 11 10

From /proc/meminfo

MemTotal: 394174484 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

(Continued on next page)
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Platform Notes (Continued)

From /etc/*release* /etc/*version*

```bash
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
```

```bash
uname -a:
Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Apr 20 18:53
```

SPEC is set to: /spec

```
Filesystem   Type  Size  Used  Avail  Use% Mounted on
/dev/sda8     xfs   325G   27G  299G   9%  /
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.59 02/24/2018

Memory:
4x NO DIMM NO DIMM
12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

```
==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
```

```
CC  519.lbm_r(peak) 544.nab_r(peak)
```

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Huawei
Huawei XH321 V5 (Intel Xeon Platinum 8164)

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Tested by: Huawei

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Compiler Version Notes (Continued)

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 508.namd_r(base) 510.parest_r(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 508.namd_r(peak) 510.parest_r(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CC 511.povray_r(base) 526.blender_r(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
iccc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CC 511.povray_r(peak) 526.blender_r(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
iccc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 507.cactuBSSN_r(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
iccc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Huawei

Huawei XH321 V5 (Intel Xeon Platinum 8164)

SPECrate2017_fp_base = 213
SPECrate2017_fp_peak = 217

CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Apr-2018
Tested by: Huawei
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Compiler Version Notes (Continued)

==============================================================================
FC  507.cactuBSSN_r(peak)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================

FC  503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================

FC  554.roms_r(peak)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================

CC  521.wrf_r(base) 527.cam4_r(base)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================

CC  521.wrf_r(peak) 527.cam4_r(peak)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
### SPEC CPU2017 Floating Point Rate Result

**Huawei**

**Huawei XH321 V5 (Intel Xeon Platinum 8164)**

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<thead>
<tr>
<th>SPECrate2017_fp_base</th>
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</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak</td>
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</table>

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei

**Test Date:** Apr-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Jan-2018

#### Base Compiler Invocation

- **C benchmarks:**  
  `icc`

- **C++ benchmarks:**  
  `icpc`

- **Fortran benchmarks:**  
  `ifort`

- **Benchmarks using both Fortran and C:**  
  `ifort icc`

- **Benchmarks using both C and C++:**  
  `icpc icc`

- **Benchmarks using Fortran, C, and C++:**  
  `icpc icc ifort`

#### Base Portability Flags

- `503.bwaves_r: -DSPEC_LP64`
- `507.cactuBSSN_r: -DSPEC_LP64`
- `508.namd_r: -DSPEC_LP64`
- `510.parest_r: -DSPEC_LP64`
- `511.povray_r: -DSPEC_LP64`
- `519.lbm_r: -DSPEC_LP64`
- `521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
- `526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char`
- `527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG`
- `538.imagick_r: -DSPEC_LP64`
- `544.nab_r: -DSPEC_LP64`
- `549.fotonik3d_r: -DSPEC_LP64`
- `554.roms_r: -DSPEC_LP64`

#### Base Optimization Flags

- **C benchmarks:**  
  `–xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3`

- **C++ benchmarks:**  
  `–xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only`

(Continued on next page)
Huawei

Huawei XH321 V5 (Intel Xeon Platinum 8164)

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Base Optimization Flags (Continued)

C++ benchmarks (continued):
- -qopt-mem-layout-trans=3

Fortran benchmarks:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
- -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
- -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
- -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
- -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
- -m64 -std=c11

C++ benchmarks:
- -m64

Fortran benchmarks:
- -m64

Benchmarks using both Fortran and C:
- -m64 -std=c11

Benchmarks using both C and C++:
- -m64 -std=c11

Benchmarks using Fortran, C, and C++:
- -m64 -std=c11
Huawei
Huawei XH321 V5 (Intel Xeon Platinum 8164)

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Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:

(Continued on next page)
Huawei

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SPEC CPU2017 Floating Point Rate Result

SPECrate2017_fp_base = 213
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CPU2017 License: 3175
Test Sponsor: Huawei
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Test Date: Apr-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Peak Optimization Flags (Continued)

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
507.cactuBSSN_r: basepeak = yes

Peak Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11
Huawei

Huawei XH321 V5 (Intel Xeon Platinum 8164)

<table>
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<th>SPECrate2017_FP_peak</th>
<th>SPECrate2017_fp_base</th>
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<tbody>
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<td>217</td>
<td>213</td>
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</table>

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Hardware Availability: Jul-2017
Software Availability: Jan-2018

Test Date: Apr-2018

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml

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Tested with SPEC CPU2017 v1.0.2 on 2018-04-21 06:21:07-0400.
Originally published on 2018-06-12.