**Lenovo Global Technology**

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Hardware**
- **CPU Name:** Intel Xeon Gold 5115  
- **Max MHz.:** 3200  
- **Nominal:** 2400  
- **Enabled:** 40 cores, 4 chips  
- **Orderable:** 2,4 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 13.75 MB I+D on chip per core  
- **Other:** None  
- **Memory:** 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)  
- **Storage:** 1 x 800 GB SAS SSD  
- **Other:** None

**Software**
- **OS:** SUSE Linux Enterprise Server 12 SP2 (x86_64)  
- **Kernel:** 4.4.114-92.64-default  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++; Fortran: Version 18.0.0.128 of Intel Fortran  
- **Parallel:** Yes  
- **Firmware:** Lenovo BIOS Version IVE113W 1.12 released Feb-2018  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc: jemalloc memory allocator library V5.0.1

**CPU2017 Speed Results**

<table>
<thead>
<tr>
<th>Test Name</th>
<th>Threads</th>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>40</td>
<td>7.49</td>
<td>7.69</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>40</td>
<td>7.96</td>
<td>8.18</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>40</td>
<td>4.57</td>
<td>4.65</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>40</td>
<td>10.0</td>
<td>10.0</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>40</td>
<td>11.6</td>
<td>11.6</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>40</td>
<td>19.7</td>
<td>19.7</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>40</td>
<td>3.75</td>
<td>3.76</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>40</td>
<td>3.75</td>
<td>3.76</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>40</td>
<td>18.0</td>
<td>18.0</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>40</td>
<td>20.0</td>
<td>20.0</td>
</tr>
</tbody>
</table>

**Specifications**
- **CPU Name:** Intel Xeon Gold 5115  
- **Max MHz.:** 3200  
- **Nominal:** 2400  
- **Enabled:** 40 cores, 4 chips  
- **Orderable:** 2,4 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 13.75 MB I+D on chip per core  
- **Other:** None  
- **Memory:** 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)  
- **Storage:** 1 x 800 GB SAS SSD  
- **Other:** None

**Test Information**
- **Test Date:** May-2018  
- **Hardware Availability:** Aug-2017  
- **Software Availability:** Feb-2018

---

Note: This document contains performance benchmarks and specifications for a Lenovo Global Technology machine, including details on the CPU, memory, storage, and software configurations used. The results show the machine's performance on various SPECbenchmarks, with specific scores for integer speed. The benchmarks were run on an Intel Xeon Gold 5115 CPU at 2.40 GHz, with Lenovo Global Technology as the test sponsor. The data was collected in May 2018, with software availability in February 2018.
## Lenovo Global Technology

ThinkSystem SN850  
(2.40 GHz, Intel Xeon Gold 5115)

### SPEC CPU2017 Integer Speed Result

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>40</td>
<td>337</td>
<td>5.26</td>
<td>339</td>
<td>5.23</td>
<td>337</td>
<td>5.26</td>
<td>40</td>
<td>283</td>
<td>6.28</td>
<td>283</td>
<td>6.26</td>
<td>284</td>
<td>6.26</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>40</td>
<td>513</td>
<td>7.76</td>
<td>516</td>
<td>7.72</td>
<td>515</td>
<td>7.73</td>
<td>40</td>
<td>499</td>
<td>7.98</td>
<td>500</td>
<td>7.96</td>
<td>500</td>
<td>7.96</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>40</td>
<td>349</td>
<td>4.67</td>
<td>369</td>
<td>4.42</td>
<td>348</td>
<td>4.69</td>
<td>40</td>
<td>348</td>
<td>4.69</td>
<td>350</td>
<td>4.65</td>
<td>363</td>
<td>4.49</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>40</td>
<td>173</td>
<td>8.18</td>
<td>174</td>
<td>8.15</td>
<td>173</td>
<td>8.18</td>
<td>40</td>
<td>164</td>
<td>8.64</td>
<td>164</td>
<td>8.63</td>
<td>163</td>
<td>8.67</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>40</td>
<td>178</td>
<td>9.93</td>
<td>176</td>
<td>10.0</td>
<td>176</td>
<td>10.0</td>
<td>40</td>
<td>177</td>
<td>9.99</td>
<td>176</td>
<td>10.0</td>
<td>176</td>
<td>10.0</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>40</td>
<td>329</td>
<td>4.36</td>
<td>329</td>
<td>4.35</td>
<td>329</td>
<td>4.35</td>
<td>40</td>
<td>331</td>
<td>4.33</td>
<td>334</td>
<td>4.29</td>
<td>331</td>
<td>4.33</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>40</td>
<td>455</td>
<td>3.75</td>
<td>455</td>
<td>3.75</td>
<td>455</td>
<td>3.75</td>
<td>40</td>
<td>453</td>
<td>3.76</td>
<td>454</td>
<td>3.76</td>
<td>453</td>
<td>3.76</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>40</td>
<td>254</td>
<td>11.6</td>
<td>255</td>
<td>11.6</td>
<td>256</td>
<td>11.5</td>
<td>40</td>
<td>254</td>
<td>11.6</td>
<td>254</td>
<td>11.6</td>
<td>255</td>
<td>11.5</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>40</td>
<td>314</td>
<td>19.7</td>
<td>311</td>
<td>19.9</td>
<td>314</td>
<td>19.7</td>
<td>40</td>
<td>313</td>
<td>19.7</td>
<td>314</td>
<td>19.7</td>
<td>314</td>
<td>19.7</td>
</tr>
</tbody>
</table>

**SPECspeed2017_int_base = 7.49**  
**SPECspeed2017_int_peak = 7.69**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

---

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

---

### General Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"

LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"

OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;

jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;


Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
SPEC CPU2017 Integer Speed Result

Lenovo Global Technology
ThinkSystem SN850
(2.40 GHz, Intel Xeon Gold 5115)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

**SPECspeed2017_int_base = 7.49**
**SPECspeed2017_int_peak = 7.69**

**Test Date:** May-2018
**Hardware Availability:** Aug-2017
**Software Availability:** Feb-2018

**Platform Notes**

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disable
DCU Streamer Prefetcher set to Disable
MONITORMWAIT set to Enable
DCA set to Enable
Stale AtoS set to Enable
LLC dead line alloc set to Disable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on SN850 Thu May 17 16:32:11 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) Gold 5115 CPU @ 2.40GHz
- 4 "physical id"s (chips)
- 40 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following
  excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 10
  - siblings : 10
  - physical 0: cores 0 1 2 3 4 8 9 10 11 12
  - physical 1: cores 0 1 2 3 4 8 9 10 11 12
  - physical 2: cores 0 1 2 3 4 8 9 10 11 12
  - physical 3: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 40
- On-line CPU(s) list: 0-39
- Thread(s) per core: 1
- Core(s) per socket: 10
- Socket(s): 4
- NUMA node(s): 4
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Gold 5115 CPU @ 2.40GHz
- Stepping: 4
- CPU MHz: 2394.389
- BogoMIPS: 4788.77
- Virtualization: VT-x

(Continued on next page)
Lenovo Global Technology
ThinkSystem SN850
(2.40 GHz, Intel Xeon Gold 5115)

SPEC CPU2017 Integer Speed Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

SPECspeed2017_int_base = 7.49
SPECspeed2017_int_peak = 7.69

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: May-2018
Hardware Availability: Aug-2017
Software Availability: Feb-2018

Platform Notes (Continued)

L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 14080K
NUMA node0 CPU(s): 0-9
NUMA node1 CPU(s): 10-19
NUMA node2 CPU(s): 20-29
NUMA node3 CPU(s): 30-39
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmrperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xempt pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnoprefetch ida arat epb invpcid_single pln pts
dtherm intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow vnmi flexpriority
ept vpid fsgsbase tsc_adjust bni hle avx2 smep bmi2 erms invpcid rtm cqm mpx
avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt
xsavec xgetbv1 cqm_llc cqm_occup_llc

/proc/cpuinfo cache data
    cache size : 14080 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
    available: 4 nodes (0-3)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9
    node 0 size: 386660 MB
    node 0 free: 386317 MB
    node 1 cpus: 10 11 12 13 14 15 16 17 18 19
    node 1 size: 387057 MB
    node 1 free: 386790 MB
    node 2 cpus: 20 21 22 23 24 25 26 27 28 29
    node 2 size: 387057 MB
    node 2 free: 386843 MB
    node 3 cpus: 30 31 32 33 34 35 36 37 38 39
    node 3 size: 387054 MB
    node 3 free: 386789 MB
    node distances:
      node  0  1  2  3
      0:  10 21 21 31
      1:  21 10 31 21
      2:  21 31 10 21
      3:  31 21 21 10

From /proc/meminfo
    MemTotal: 1584976776 KB
    HugePages_Total: 0

(Continued on next page)
platform Notes (Continued)

Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
  Linux SN850 4.4.114-92.64-default #1 SMP Thu Feb 1 19:18:19 UTC 2018 (c6ce5db) x86_64
  x86_64 x86_64 GNU/Linux

run-level 3 May 17 16:30

SPEC is set to: /home/cpu2017.1.0.2.ic18.0
Filesystsem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 836G 168G 669G 21% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS Lenovo -[IVE113W-1.12]- 02/06/2018
  Memory:
    48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
  CC 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base,
peak) 657.xz_s(base)

------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

(Continued on next page)
## Lenovo Global Technology

**ThinkSystem SN850**  
(2.40 GHz, Intel Xeon Gold 5115)

<table>
<thead>
<tr>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>7.49</td>
<td>7.69</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>Lenovo Global Technology</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor</td>
<td>Lenovo Global Technology</td>
</tr>
<tr>
<td>Tested by</td>
<td>Lenovo Global Technology</td>
</tr>
<tr>
<td>Test Date</td>
<td>May-2018</td>
</tr>
<tr>
<td>Hardware Availability</td>
<td>Aug-2017</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Feb-2018</td>
</tr>
</tbody>
</table>

### Compiler Version Notes (Continued)

```
==============================================================================
CC  600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
       641.leela_s(base)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
CXXC 620.omnetpp_s(peak) 623.xalancbmk_s(peak) 631.deepsjeng_s(peak)
       641.leela_s(peak)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
FC  648.exchange2_s(base, peak)
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

### Base Compiler Invocation

**C benchmarks:**  
`icc`

**C++ benchmarks:**  
`icpc`

**Fortran benchmarks:**  
`ifort`
**Lenovo Global Technology**

ThinkSystem SN850  
(2.40 GHz, Intel Xeon Gold 5115)

<table>
<thead>
<tr>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>7.49</td>
<td>7.69</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** May-2018  
**Hardware Availability:** Aug-2017  
**Software Availability:** Feb-2018

### Base Portability Flags

- 600.perlbench_s: -DSPEC_LP64  -DSPEC_LINUX_X64
- 602.gcc_s: -DSPEC_LP64
- 605.mcf_s: -DSPEC_LP64
- 620.omnetpp_s: -DSPEC_LP64
- 623.xalancbmk_s: -DSPEC_LP64  -DSPEC_LINUX
- 625.x264_s: -DSPEC_LP64
- 631.deepsjeng_s: -DSPEC_LP64
- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
- -L/usr/local/je5.0.1-64/lib -ljemalloc

**C++ benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

**Fortran benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
- -L/usr/local/je5.0.1-64/lib -ljemalloc

### Base Other Flags

**C benchmarks:**
- -m64 -std=c11

**C++ benchmarks:**
- -m64

**Fortran benchmarks:**
- -m64
Lenovo Global Technology
ThinkSystem SN850
(2.40 GHz, Intel Xeon Gold 5115)

SPECspeed2017_int_base = 7.49
SPECspeed2017_int_peak = 7.69

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

(Continued on next page)
Lenovo Global Technology
ThinkSystem SN850
(2.40 GHz, Intel Xeon Gold 5115)

SPECspeed2017_int_base = 7.49
SPECspeed2017_int_peak = 7.69

CPU2017 License: 9017  Test Date: May-2018
Test Sponsor: Lenovo Global Technology  Hardware Availability: Aug-2017
Tested by: Lenovo Global Technology  Software Availability: Feb-2018

Peak Optimization Flags (Continued)

625.x264_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: Same as 602.gcc_s

C++ benchmarks:
620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

623.xalancbmk_s: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-32/lib -ljemalloc

631.deepsjeng_s: Same as 620.omnetpp_s

641.leela_s: Same as 620.omnetpp_s

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks (except as noted below):
-m64

623.xalancbmk_s: -m32

Fortran benchmarks:
-m64
Lenovo Global Technology
ThinkSystem SN850
(2.40 GHz, Intel Xeon Gold 5115)

| SPECspeed2017_int_base | 7.49 |
| SPECspeed2017_int_peak | 7.69 |

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: May-2018
Hardware Availability: Aug-2017
Software Availability: Feb-2018

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.xml

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-05-17 04:32:11-0400.
Report generated on 2018-10-31 18:04:02 by CPU2017 PDF formatter v6067.
Originally published on 2018-06-12.