## Lenovo Global Technology

ThinkSystem SR570  
(2.60 GHz, Intel Xeon Gold 6132)

<table>
<thead>
<tr>
<th>Software</th>
<th>SPECspeed2017_fp_base = 97.8</th>
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<tbody>
<tr>
<td></td>
<td>SPECspeed2017_fp_peak = 99.1</td>
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### CPU2017 License:
9017

#### Test Sponsor:
Lenovo Global Technology

#### Tested by:
Lenovo Global Technology

#### Test Date:
May-2018

#### Hardware Availability:
Nov-2017

#### Software Availability:
Feb-2018

### Threads

<table>
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<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed2017_fp_peak</th>
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<tr>
<td>603.bwaves_s</td>
<td>28</td>
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<td>127</td>
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<td>607.cactuBSSN_s</td>
<td>28</td>
<td>39.2</td>
<td>39.2</td>
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<tr>
<td>621.wrf_s</td>
<td>28</td>
<td>68.0</td>
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<td>627.cam4_s</td>
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<td>638.imagick_s</td>
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<tr>
<td>644.nab_s</td>
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<tr>
<td>654.roms_s</td>
<td>28</td>
<td>104</td>
<td>108</td>
</tr>
</tbody>
</table>

---

### Hardware

- **CPU Name:** Intel Xeon Gold 6132
- **Max MHz.:** 3700
- **Nominal:** 2600
- **Enabled:** 28 cores, 2 chips
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 19.25 MB I+D on chip per core
- **Other:** None
- **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R)
- **Storage:** 1 x 800 GB SAS SSD
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 12 SP3 (x86_64)  
  Kernel 4.4.114-94.11-default
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++  
  Compiler for Linux:  
  Fortran: Version 18.0.0.128 of Intel Fortran  
  Compiler for Linux
- **Parallel:** Yes
- **File System:** btrfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** None
SPEC CPU2017 Floating Point Speed Result

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SPECspeed2017_fp_base = 97.8
SPECspeed2017_fp_peak = 99.1

Results Table

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<tr>
<th>Benchmark</th>
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<th>Base Ratio</th>
<th>Base Seconds</th>
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<td>146</td>
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</tbody>
</table>

SPECspeed2017_fp_base = 97.8
SPECspeed2017_fp_peak = 99.1

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3> /proc/sys/vm/drop_caches
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disable

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Tested by: Lenovo Global Technology

Platform Notes (Continued)

MONITORMWAIT set to Enable
Trusted Execution Technology set to Enable
DCU Streamer Prefetcher set to Disable
LLC dead line alloc set to Enable
Stale AtoS set to Enable
DCA set to Enable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f
running on linux-jnol Mon May 14 16:30:24 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6132 CPU @ 2.60GHz
  2 "physical id"s (chips)
  28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 14
siblings : 14
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 28
On-line CPU(s) list: 0-27
Thread(s) per core: 1
Core(s) per socket: 14
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6132 CPU @ 2.60GHz
Stepping: 4
CPU MHz: 2593.900
BogoMIPS: 5187.80
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 19712K

(Continued on next page)
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SPECspeed2017_fp_base = 97.8
SPECspeed2017_fp_peak = 99.1

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Platform Notes (Continued)

NUMA node0 CPU(s): 0-13
NUMA node1 CPU(s): 14-27
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good ntopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtrr pdcm pccc dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3nowprefetch ida arat epb invpcid_single pln pts
dtherm intel_pt rsb_ctxtsw spec_ctrl retpoline kaiser tpr_shadow vmpfn flexpriority
ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 3msrs invpcid rtm cqm mpx
avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt
xsavec xgetbv1 cqm_llc cqm_occup_llc pknu ospke

/proc/cpuinfo cache data
  cache size: 19712 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
    available: 2 nodes (0-1)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13
    node 0 size: 96059 MB
    node 0 free: 95202 MB
    node 1 cpus: 14 15 16 17 18 19 20 21 22 23 24 25 26 27
    node 1 size: 96749 MB
    node 1 free: 95751 MB
    node distances:
      node   0   1
      0: 10 21
      1: 21 10

From /proc/meminfo
  MemTotal: 197435516 KB
  HugePages_Total: 0
  Hugepagesize: 2048 KB

From /etc/*release* /etc/*version*
  SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 3
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP3"
    VERSION_ID="12.3"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"

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<th>Test Date</th>
<th>Test Sponsor</th>
<th>Lenovo Global Technology</th>
<th>Hardware Availability</th>
<th>Software Availability</th>
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<td>May-2018</td>
<td>Lenovo Global Technology</td>
<td>Lenovo Global Technology</td>
<td>Nov-2017</td>
<td>Feb-2018</td>
</tr>
</tbody>
</table>

**Platform Notes (Continued)**

```
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```
uname -a:
    Linux linux-jnol 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux
run-level 3 May 14 11:15
SPEC is set to: /home/cpu2017.1.0.2.ic18.0
```

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda2</td>
<td>btrfs</td>
<td>744G</td>
<td>22G</td>
<td>723G</td>
<td>3%</td>
<td>/home</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[TEE119R-1.22]- 02/06/2018
Memory:
4x NO DIMM NO DIMM
12x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)

---

## Compiler Version Notes

```
Cc 619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
```

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

```
Cc 619.lbm_s(peak)
```

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

```
Ff 607.cactuBSSN_s(base)
```

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

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Lenovo Global Technology
ThinkSystem SR570
(2.60 GHz, Intel Xeon Gold 6132)

SPECspeed2017_fp_base = 97.8
SPECspeed2017_fp_peak = 99.1

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: May-2018
Hardware Availability: Nov-2017
Software Availability: Feb-2018

Compiler Version Notes (Continued)

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC 607.cactuBSSN_s(peak)
==============================================================================
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
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ifort (IFORT) 18.0.0 20170811
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==============================================================================
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icc (ICC) 18.0.0 20170811
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### SPEC CPU2017 Floating Point Speed Result

| SPECspeed2017_fp_base = 97.8 |
| SPECspeed2017_fp_peak = 99.1 |

### Compiler Version Notes (Continued)

#### Base Compiler Invocation

C benchmarks:  
`icc`

Fortran benchmarks:  
`ifort`

Benchmarks using both Fortran and C:  
`ifort icc`

Benchmarks using Fortran, C, and C++:  
`icpc icc ifort`

#### Base Portability Flags

603.bwaves_s: -DSPEC_LP64  
607.cactuBSSN_s: -DSPEC_LP64  
619.lbm_s: -DSPEC_LP64  
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG  
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl  
638.imagick_s: -DSPEC_LP64  
644.nab_s: -DSPEC_LP64  
649.fotonik3d_s: -DSPEC_LP64  
654.roms_s: -DSPEC_LP64

#### Base Optimization Flags

C benchmarks:  
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:  
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-nostandard-realloc-lhs -align array32byte
## Lenovo Global Technology

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(2.60 GHz, Intel Xeon Gold 6132)

| SPECspeed2017_fp_base | 97.8
| SPECspeed2017_fp_peak  | 99.1

| CPU2017 License: | 9017
| Test Sponsor: | Lenovo Global Technology
| Tested by: | Lenovo Global Technology

### Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

- `xCORE-AVX512`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-qopenmp`  
- `-DSPEC_OPENMP`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

Benchmarks using Fortran, C, and C++:

- `xCORE-AVX512`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-qopenmp`  
- `-DSPEC_OPENMP`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

### Base Other Flags

#### C benchmarks:

- `-m64`  
- `-std=c11`

#### Fortran benchmarks:

- `-m64`

#### Benchmarks using both Fortran and C:

- `-m64`  
- `-std=c11`

#### Benchmarks using Fortran, C, and C++:

- `-m64`  
- `-std=c11`

### Peak Compiler Invocation

#### C benchmarks:

- `icc`

#### Fortran benchmarks:

- `ifort`

#### Benchmarks using both Fortran and C:

- `ifort icc`

#### Benchmarks using Fortran, C, and C++:

- `icpc icc ifort`
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SPECspeed2017_fp_base = 97.8
SPECspeed2017_fp_peak = 99.1

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
-align array32byte
Lenovo Global Technology
ThinkSystem SR570
(2.60 GHz, Intel Xeon Gold 6132)

SPECspeed2017_fp_base = 97.8
SPECspeed2017_fp_peak = 99.1

Peak Other Flags

C benchmarks:
- `m64 -std=c11`

Fortran benchmarks:
- `m64`

Benchmarks using both Fortran and C:
- `m64 -std=c11`

Benchmarks using Fortran, C, and C++:
- `m64 -std=c11`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.xml