## Lenovo Global Technology

**ThinkSystem SR570**  
(2.10 GHz, Intel Xeon Gold 6152)

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>OS</strong></td>
<td>SUSE Linux Enterprise Server 12 SP3 (x86_64)</td>
</tr>
<tr>
<td><strong>Compiler</strong></td>
<td>C/C++: Version 18.0.0.128 of Intel C/C++</td>
</tr>
<tr>
<td><strong>Compiler for Linux</strong></td>
<td>Fortran: Version 18.0.0.128 of Intel Fortran</td>
</tr>
<tr>
<td><strong>Firmware</strong></td>
<td>Lenovo BIOS Version TEE119R 1.22 released Feb-2018</td>
</tr>
<tr>
<td><strong>File System</strong></td>
<td>btrfs</td>
</tr>
<tr>
<td><strong>System State</strong></td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td><strong>Base Pointers</strong></td>
<td>64-bit</td>
</tr>
<tr>
<td><strong>Peak Pointers</strong></td>
<td>64-bit</td>
</tr>
<tr>
<td><strong>Other</strong></td>
<td>None</td>
</tr>
</tbody>
</table>

### SPECs Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>44</td>
<td>106</td>
<td>109</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>44</td>
<td>150</td>
<td>133</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>44</td>
<td>41.0</td>
<td>79.2</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>44</td>
<td>79.2</td>
<td>85.8</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>44</td>
<td>85.4</td>
<td>85.7</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>44</td>
<td>55.8</td>
<td>55.8</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>44</td>
<td>106</td>
<td>105</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>44</td>
<td>211</td>
<td>211</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>44</td>
<td>77.6</td>
<td>76.6</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>44</td>
<td>118</td>
<td>121</td>
</tr>
</tbody>
</table>

**CPU Name:** Intel Xeon Gold 6152  
**Max MHz.:** 3700  
**Nominal:** 2100  
**Enabled:** 44 cores, 2 chips  
**Orderable:** 1.2 chips  
**Cache L1:** 32 KB I + 32 KB D on chip per core  
**L2:** 1 MB I+D on chip per core  
**L3:** 30.25 MB I+D on chip per chip  
**Other:** None  
**Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R)  
**Storage:** 1 x 800 GB SAS SSD  
**Other:** None
Lenovo Global Technology
ThinkSystem SR570
(2.10 GHz, Intel Xeon Gold 6152)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECspeed2017_fp_base = 108
SPECspeed2017_fp_peak = 109

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
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<tr>
<td>603.bwaves_s</td>
<td>44</td>
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<td>451</td>
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<td>452</td>
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<td>448</td>
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<tr>
<td>607.cactuBSSN_s</td>
<td>44</td>
<td>111</td>
<td>150</td>
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<td>111</td>
<td>150</td>
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</tr>
<tr>
<td>619.lbm_s</td>
<td>44</td>
<td>129</td>
<td>40.7</td>
<td>127</td>
<td>41.1</td>
<td>128</td>
<td>41.0</td>
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<tr>
<td>621.wrf_s</td>
<td>44</td>
<td>168</td>
<td>78.8</td>
<td>166</td>
<td>79.8</td>
<td>166</td>
<td>79.8</td>
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<td>627.cam4_s</td>
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<td>628.pop2_s</td>
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<td>211</td>
<td>56.1</td>
<td>215</td>
<td>55.2</td>
<td>213</td>
<td>55.8</td>
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<td>638.imagick_s</td>
<td>44</td>
<td>136</td>
<td>106</td>
<td>138</td>
<td>104</td>
<td>136</td>
<td>106</td>
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<tr>
<td>644.nab_s</td>
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<td>82.8</td>
<td>211</td>
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<td>211</td>
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<td>211</td>
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<td>119</td>
<td>133</td>
<td>118</td>
</tr>
</tbody>
</table>

SPECspeed2017_fp_base = 108
SPECspeed2017_fp_peak = 109

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3>/proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disable

(Continued on next page)
LENNOVO GLOBAL TECHNOLOGY

**ThinkSystem SR570**
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<tr>
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<td>109</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**MONITORMWait** set to Enable  
**Trusted Execution Technology** set to Enable  
**DCU Streamer Prefetcher** set to Disable  
**LLC dead line alloc** set to Enable  
**Stale AtoS** set to Enable  
**DCA** set to Enable

Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f  
running on linux-uru4 Thu May 17 15:35:02 2018

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```plaintext
model name : Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz
  2 "physical id"s (chips)
  44 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 22
  siblings : 22
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
```

From lscpu:

```plaintext
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                44
On-line CPU(s) list:   0-43
Thread(s) per core:    1
Core(s) per socket:    22
Socket(s):             2
NUMA node(s):          2
Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz
Stepping:              4
CPU MHz:               2095.076
BogoMIPS:              4190.15
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              30976K
```

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

Lenovo Global Technology
ThinkSystem SR570
(2.10 GHz, Intel Xeon Gold 6152)

SPECspeed2017_fp_base = 108
SPECspeed2017_fp_peak = 109

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: May-2018
Hardware Availability: Nov-2017
Software Availability: Feb-2018

Platform Notes (Continued)

NUMA node0 CPU(s): 0-21
NUMA node1 CPU(s): 22-43
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat ebp invpcid_single pln pts
dtherm intel_pt rsb_cxsw spec_ctrl retpoline kaiser tpr_shadow vmlinux flexpriority
egt vpid fsgsb base tsc_adjust bmi1 hle avx2 smep bmi2 3rns invpcid rtm cqm mpx
avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt
xsavevc xgetbv1 cqm_llc cqm_occup_llc pkp ospke

From /proc/cpuinfo

```
//cache data
  cache size : 30976 KB
```

From numactl --hardware

```
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21
node 0 size: 96056 MB
node 0 free: 94848 MB
node 1 cpus: 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43
node 1 size: 96748 MB
node 1 free: 96169 MB
node distances:
node 0 1
0: 10 21
1: 21 10
```

From /proc/meminfo

```
MemTotal:       197432344 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
```

From /etc/*release* /etc/*version*

```
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 3
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP3"
    VERSION_ID="12.3"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
```

(Continued on next page)
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SPECspeed2017_fp_base = 108
SPECspeed2017_fp_peak = 109

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Tested by: Lenovo Global Technology

Platform Notes (Continued)

ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
    Linux linux-uru4 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
    x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 17 10:32

SPEC is set to: /home/cpu2017.1.0.2.ic18.0

Filesystem Type Size  Used Avail Use% Mounted on
/dev/sdb2  btrfs  744G  96G  649G  13% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[TEE119R-1.22]- 02/06/2018

Memory:
    4x NO DIMM NO DIMM
    12x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  619.lbm_s(base)  638.imagick_s(base, peak)  644.nab_s(base, peak)
==============================================================================

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================

FC  607.cactuBSSN_s(base)
==============================================================================
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

(Continued on next page)
## Lenovo Global Technology

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| SPECspeed2017_fp_base | 108  
|-----------------------|-----  
| SPECspeed2017_fp_peak | 109  

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** May-2018  
**Hardware Availability:** Nov-2017  
**Software Availability:** Feb-2018

---

### Compiler Version Notes (Continued)

```plaintext
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  

==============================================================================
FC 607.cactuBSSN_s(peak)  
==============================================================================
```

```plaintext
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  

==============================================================================
FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)  
==============================================================================
```

```plaintext
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  

==============================================================================
```

```plaintext
-------------------  
FC 603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)  
-------------------
```

```plaintext
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  

==============================================================================
```

```plaintext
-------------------  
CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)  
-------------------
```

```plaintext
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  

==============================================================================
```

```plaintext
-------------------  
CC 621.wrf_s(peak) 628.pop2_s(peak)  
-------------------
```

```plaintext
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  

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Compiler Version Notes (Continued)

Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactusBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

(Continued on next page)
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**Software Availability:** Feb-2018

### Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-xCORE-AVX512  -ipo  -O3  -no-prec-div  -qopt-prefetch
-ffinite-math-only  -qopt-mem-layout-trans=3  -qopenmp  -DSPEC_OPENMP
-nostandard-realloc-lhs  -align array32byte

Benchmarks using Fortran, C, and C++:

-xCORE-AVX512  -ipo  -O3  -no-prec-div  -qopt-prefetch
-ffinite-math-only  -qopt-mem-layout-trans=3  -qopenmp  -DSPEC_OPENMP
-nostandard-realloc-lhs  -align array32byte

### Base Other Flags

**C benchmarks:**

- m64  -std=c11

**Fortran benchmarks:**

- m64

**Benchmarks using both Fortran and C:**

- m64  -std=c11

**Benchmarks using Fortran, C, and C++:**

- m64  -std=c11

### Peak Compiler Invocation

**C benchmarks:**

- icc

**Fortran benchmarks:**

- ifort

**Benchmarks using both Fortran and C:**

- ifort  icc

**Benchmarks using Fortran, C, and C++:**

- icpc  icc  ifort
Lenovo Global Technology
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SPECspeed2017_fp_base = 108
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CPU2017 License: 9017
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Tested by: Lenovo Global Technology
Test Date: May-2018
Hardware Availability: Nov-2017
Software Availability: Feb-2018

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -03 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: -xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -03
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -03 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch
-ipo -03 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
-align array32byte
Lenovo Global Technology
ThinkSystem SR570
(2.10 GHz, Intel Xeon Gold 6152)

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**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

---

**Peak Other Flags**

C benchmarks:
- `-m64` `-std=c11`

Fortran benchmarks:
- `-m64`

Benchmarks using both Fortran and C:
- `-m64` `-std=c11`

Benchmarks using Fortran, C, and C++:
- `-m64` `-std=c11`

The flags files that were used to format this result can be browsed at:
- http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.html

You can also download the XML flags sources by saving the following links:
- http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
- http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.xml

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