### NEC Corporation

**Express5800/D120h (Intel Xeon Bronze 3104)**

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>SPECrate2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>33.6</td>
<td>35.0</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation  
**Test Date:** May-2018  
**Hardware Availability:** Jan-2018  
**Software Availability:** Mar-2018

#### Hardware

<table>
<thead>
<tr>
<th>Name</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU Name:</strong></td>
<td>Intel Xeon Bronze 3104</td>
</tr>
<tr>
<td><strong>Max MHz.:</strong></td>
<td>1700</td>
</tr>
<tr>
<td><strong>Nominal:</strong></td>
<td>1700</td>
</tr>
<tr>
<td><strong>Enabled:</strong></td>
<td>12 cores, 2 chips</td>
</tr>
<tr>
<td><strong>Orderable:</strong></td>
<td>1,2 chips</td>
</tr>
<tr>
<td><strong>Cache L1:</strong></td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td><strong>L2:</strong></td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td><strong>L3:</strong></td>
<td>8.25 MB I+D on chip per chip</td>
</tr>
<tr>
<td><strong>Other:</strong></td>
<td>None</td>
</tr>
<tr>
<td><strong>Memory:</strong></td>
<td>384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2133)</td>
</tr>
<tr>
<td><strong>Storage:</strong></td>
<td>1 x 1 TB SATA, 7200 RPM</td>
</tr>
<tr>
<td><strong>Other:</strong></td>
<td>None</td>
</tr>
</tbody>
</table>

#### Software

<table>
<thead>
<tr>
<th>Name</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>OS:</strong></td>
<td>Red Hat Enterprise Linux Server release 7.4 (Maipo)</td>
</tr>
<tr>
<td></td>
<td>Kernel 3.10.0-693.21.1.el7.x86_64</td>
</tr>
<tr>
<td><strong>Compiler:</strong></td>
<td>C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td><strong>Parallel:</strong></td>
<td>No</td>
</tr>
<tr>
<td><strong>Firmware:</strong></td>
<td>Version F21 02/22/2018 released Apr-2018</td>
</tr>
<tr>
<td><strong>File System:</strong></td>
<td>ext4</td>
</tr>
<tr>
<td><strong>System State:</strong></td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td><strong>Base Pointers:</strong></td>
<td>64-bit</td>
</tr>
<tr>
<td><strong>Peak Pointers:</strong></td>
<td>32/64-bit</td>
</tr>
<tr>
<td><strong>Other:</strong></td>
<td>jemalloc memory allocator library V5.0.1</td>
</tr>
</tbody>
</table>
SPEC CPU2017 Integer Rate Result

NEC Corporation

Express5800/D120h (Intel Xeon Bronze 3104)

SPECrate2017_int_base = 33.6
SPECrate2017_int_peak = 35.0

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>12</td>
<td>680</td>
<td>28.1</td>
<td>680</td>
<td>28.1</td>
<td>678</td>
<td>28.2</td>
<td>12</td>
<td>580</td>
<td>33.0</td>
<td>579</td>
<td>33.0</td>
<td>579</td>
<td>33.0</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>12</td>
<td>533</td>
<td>31.9</td>
<td>534</td>
<td>31.8</td>
<td>533</td>
<td>31.9</td>
<td>12</td>
<td>471</td>
<td>36.1</td>
<td>469</td>
<td>36.2</td>
<td>470</td>
<td>36.1</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>12</td>
<td>505</td>
<td>38.4</td>
<td>505</td>
<td>38.4</td>
<td>505</td>
<td>38.4</td>
<td>12</td>
<td>505</td>
<td>38.4</td>
<td>505</td>
<td>38.4</td>
<td>505</td>
<td>38.4</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>12</td>
<td>660</td>
<td>23.9</td>
<td>666</td>
<td>23.6</td>
<td>664</td>
<td>23.7</td>
<td>12</td>
<td>652</td>
<td>24.1</td>
<td>650</td>
<td>24.2</td>
<td>655</td>
<td>24.0</td>
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<tr>
<td>523.xalanbmk_r</td>
<td>12</td>
<td>356</td>
<td>35.6</td>
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<td>35.6</td>
<td>357</td>
<td>35.5</td>
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<td>330</td>
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<td>38.5</td>
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<td>38.5</td>
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<tr>
<td>525.x264_r</td>
<td>12</td>
<td>308</td>
<td>68.3</td>
<td>307</td>
<td>68.4</td>
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<td>68.3</td>
<td>12</td>
<td>308</td>
<td>68.3</td>
<td>307</td>
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<td>68.3</td>
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<tr>
<td>531.deepsjeng_r</td>
<td>12</td>
<td>488</td>
<td>28.2</td>
<td>488</td>
<td>28.2</td>
<td>488</td>
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<td>541.leela_r</td>
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<td>863</td>
<td>23.0</td>
<td>864</td>
<td>23.0</td>
<td>12</td>
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<td>23.1</td>
<td>859</td>
<td>23.1</td>
<td>859</td>
<td>23.1</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>12</td>
<td>485</td>
<td>65.1</td>
<td>485</td>
<td>64.8</td>
<td>485</td>
<td>64.9</td>
<td>12</td>
<td>485</td>
<td>64.9</td>
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<td>64.9</td>
<td>486</td>
<td>64.7</td>
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<tr>
<td>557.xz_r</td>
<td>12</td>
<td>583</td>
<td>22.2</td>
<td>584</td>
<td>22.2</td>
<td>582</td>
<td>22.3</td>
<td>12</td>
<td>582</td>
<td>22.3</td>
<td>582</td>
<td>22.3</td>
<td>582</td>
<td>22.3</td>
</tr>
</tbody>
</table>

SPECrate2017_int_base = 33.6
SPECrate2017_int_peak = 35.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
SPEC CPU2017 Integer Rate Result

NEC Corporation
Express5800/D120h (Intel Xeon Bronze 3104)

SPECrate2017_int_base = 33.6
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CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation

Test Date: May-2018
Hardware Availability: Jan-2018
Software Availability: Mar-2018

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
ENERGY_PERF_BIAS_CFG mode: Performance
Stale AtoS: Enable
LLC dead line alloc: Disable
Patrol Scrub: Disable
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618b0e91c0f
running on d120h Tue May 29 09:55:45 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
  2 "physical id"s (chips)
  12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 6
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Thread(s) per core: 1
Core(s) per socket: 6
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6

(Continued on next page)
SPEC CPU2017 Integer Rate Result

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Tested by: NEC Corporation

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Platform Notes (Continued)

Model: 85
Model name: Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
Stepping: 4
CPU MHz: 1349.109
CPU max MHz: 1700.0000
CPU min MHz: 800.0000
BogoMIPS: 3400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 8448K
NUMA node0 CPU(s): 0-5
NUMA node1 CPU(s): 6-11
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 fma
cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 invpcid_single
intel_pt spec_ctrl ibpb_support tpr_shadow vnmi flexpriority ept vpid fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erts invpcid rtm cqm mpx rdt_a avx512f avx512dq
rdseed adx smap clflushopt clwb avx512cd avx512bw avx512v1 xsaveopt xsavec xgetbv1
cqm_llc cqm_occup_llc cqm_mbbm_total cqm_mbbm_local dtherm arat pln pts hwp
hwp_act_window hwp_epp hwp_pkg_req

/proc/cpuinfo cache data
  cache size : 8448 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
    available: 2 nodes (0-1)
    node 0 cpus: 0 1 2 3 4 5
    node 0 size: 195236 MB
    node 0 free: 190457 MB
    node 1 cpus: 6 7 8 9 10 11
    node 1 size: 196608 MB
    node 1 free: 192008 MB
    node distances:
      node 0 1
      0: 10 21
      1: 21 10

From /proc/meminfo
  MemTotal: 394644656 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

(Continued on next page)
NEC Corporation
Express5800/D120h (Intel Xeon Bronze 3104)

SPECraten2017_int_base = 33.6
SPECraten2017_int_peak = 35.0

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Test Date: May-2018
Tested by: NEC Corporation
Hardware Availability: Jan-2018
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Platform Notes (Continued)

From /etc/*release*/etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.4 (Maipo)"
    ID="rhel"
    ID LIKE="fedora"
    VARIANT="Server"
    VARIANT_ID="server"
    VERSION_ID="7.4"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
  redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
  system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:
  Linux d120h 3.10.0-693.21.1.el7.x86_64 #1 SMP Fri Feb 23 18:54:16 UTC 2018 x86_64
  x86_64 x86_64 GNU/Linux

run-level 3 May 29 09:50

SPEC is set to: /home/cpu2017

  Filesystem Type Size Used Avail Use% Mounted on
  /dev/sda3 ext4 909G 330G 533G 39% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS GIGABYTE F21 02/22/2018
  Memory:
    4x NO DIMM NO DIMM
    1x SK Hynix HMA84GR7AFR4N-VK 32 GB 2 rank 2666, configured at 2133
    11x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2133

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
 CC  500.perlbench_r(base)  502.gcc_r(base)  505.mcf_r(base, peak)
     525.x264_r(base, peak)  557.xz_r(base, peak)
==============================================================================

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

(Continued on next page)
NEC Corporation

Express5800/D120h (Intel Xeon Bronze 3104)

SPEC CPU2017 Integer Rate Result

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SPECrate2017_int_base = 33.6
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Compiler Version Notes (Continued)

==============================================================================
CC 500.perlbmk_r(peak) 502.gcc_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
      541.leela_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
      541.leela_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC 548.exchange2_r(base, peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort
## SPEC CPU2017 Integer Rate Result

**NEC Corporation**

**Express5800/D120h (Intel Xeon Bronze 3104)**

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<tr>
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### Base Portability Flags

- perlbench: `-DSPEC_LP64 -DSPEC_LINUX_X64`
- gcc_r: `-DSPEC_LP64`
- mcf_r: `-DSPEC_LP64`
- omnetpp_r: `-DSPEC_LP64`
- xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
- x264_r: `-DSPEC_LP64`
- deepsjeng_r: `-DSPEC_LP64`
- leela_r: `-DSPEC_LP64`
- exchange2_r: `-DSPEC_LP64`
- xz_r: `-DSPEC_LP64`

### Base Optimization Flags

**C benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

**C++ benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

**Fortran benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

### Base Other Flags

**C benchmarks:**
- `-m64 -std=c11`

**C++ benchmarks:**
- `-m64`

**Fortran benchmarks:**
- `-m64`
SPEC CPU2017 Integer Rate Result

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Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

500.perlbmk_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalanchmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
-ljemalloc

525.x264_r: basepeak = yes

(Continued on next page)
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Peak Optimization Flags (Continued)

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Other Flags

C benchmarks (except as noted below):

-m64 -std=c11

502.gcc_r: -m32 -std=c11

C++ benchmarks (except as noted below):

-m64

523.xalancbmk_r: -m32

Fortran benchmarks:

-m64

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-D120h-RevA.html
## SPEC CPU2017 Integer Rate Result

**NEC Corporation**

**Express5800/D120h (Intel Xeon Bronze 3104)**

<table>
<thead>
<tr>
<th>SPECrate2017_int_base = 33.6</th>
<th>SPECrate2017_int_peak = 35.0</th>
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</table>

| CPU2017 License: | 9006 |
| Test Sponsor: | NEC Corporation |
| Tested by: | NEC Corporation |
| Test Date: | May-2018 |
| Hardware Availability: | Jan-2018 |
| Software Availability: | Mar-2018 |

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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