**Lenovo Global Technology**  
ThinkSystem SN550  
(2.00 GHz, Intel Xeon Silver 4109T)

**SPECrate2017_int_base = 69.5**  
**SPECrate2017_int_peak = 74.1**

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**Hardware**

- **CPU Name:** Intel Xeon Silver 4109T  
- **Max MHz.:** 3000  
- **Nominal:** 2000  
- **Enabled:** 16 cores, 2 chips, 2 threads/core  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 11 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)  
- **Storage:** 1 x 800 GB SAS SSD  
- **Other:** None

**Software**

- **OS:** SUSE Linux Enterprise Server 12 SP2 (x86_64)  
- **Kernel:** 4.4.114-92.64-default  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++  
- **Fortran:** Version 18.0.0.128 of Intel Fortran  
- **Compiler for Linux:**  
- **Firmware:** Lenovo BIOS Version IVE113W 1.12 released Feb-2018  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc: jemalloc memory allocator library V5.0.1
Lenovo Global Technology
ThinkSystem SN550
(2.00 GHz, Intel Xeon Silver 4109T)

SPECrate2017_int_base = 69.5
SPECrate2017_int_peak = 74.1

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</table>

SPECrate2017_int_base = 69.5
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

General Notes
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/jre5.0.1-32:/home/cpu2017.1.0.2.ic18.0/jre5.0.1-64"
Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
umactl --interleave=all runcpu <etc>
jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;

(Continued on next page)
Lenovo Global Technology
ThinkSystem SN550
(2.00 GHz, Intel Xeon Silver 4109T)

SPECrate2017_int_base = 69.5
SPECrate2017_int_peak = 74.1

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jun-2018
Hardware Availability: Aug-2017
Software Availability: Feb-2018

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
DCU Streamer Prefetcher set to Disable
SNC set to Disable
MONITOR/MWAIT set to Enable
Stale AtoS set to Enable
LLC dead line alloc set to Disable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f
running on SN550 Sun Jun 3 00:10:15 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4109T CPU @ 2.00GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel

(Continued on next page)
### Lenovo Global Technology

**ThinkSystem SN550**  
(2.00 GHz, Intel Xeon Silver 4109T)

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<td>Software Availability: Feb-2018</td>
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</tbody>
</table>

**SPECrate2017_int_base = 69.5**  
**SPECrate2017_int_peak = 74.1**

---

#### Platform Notes (Continued)

- **CPU family:** 6
- **Model:** 85
- **Model name:** Intel(R) Xeon(R) Silver 4109T CPU @ 2.00GHz
- **Stepping:** 4
- **CPU MHz:** 1995.311
- **BogoMIPS:** 3990.62
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 11264K
- **NUMA node0 CPU(s):** 0-7,16-23
- **NUMA node1 CPU(s):** 8-15,24-31

---

From `numactl --hardware`

```
WARNING: a numactl 'node' might or might not correspond to a physical chip.
```

---

From `/proc/cpuinfo` cache data

```
cache size : 11264 KB
```

---

From `/proc/meminfo`

```
MemTotal:       792255352 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
```

From `/etc/*release* /etc/*version*` (Continued on next page)
### SPEC CPU2017 Integer Rate Result

**Lenovo Global Technology**  
ThinkSystem SN550  
(2.00 GHz, Intel Xeon Silver 4109T)

| CPU2017 License | Lenovo Global Technology  
|-----------------|--------------------------|
| Test Sponsor    | Lenovo Global Technology  
| Tested by       | Lenovo Global Technology  

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**Test Date:**  
Jun-2018  
**Hardware Availability:**  
Aug-2017  
**Software Availability:**  
Feb-2018

---

**Platform Notes (Continued)**

SuSE-release:  
SUSE Linux Enterprise Server 12 (x86_64)  
VERSION = 12  
PATCHLEVEL = 2  
# This file is deprecated and will be removed in a future service pack or release.  
# Please check /etc/os-release for details about this release.  
os-release:  
NAME="SLES"  
VERSION="12-SP2"  
VERSION_ID="12.2"  
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"  
ID="sles"  
ANSI_COLOR="0;32"  
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:  
Linux SN550 4.4.114-92.64-default #1 SMP Thu Feb 1 19:18:19 UTC 2018 (c6ce5db) x86_64  
x86_64 x86_64 GNU/Linux

run-level 3 Jun 3 00:06

SPEC is set to: /home/cpu2017.1.0.2.ic18.0  
Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda4      xfs   687G  170G  518G  25% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret  
this section. The 'dmidecode' program reads system data which is "intended to allow  
hardware to be accurately determined", but the intent may not be met, as there are  
frequent changes to hardware, firmware, and the "DMTF SMI BIOS" standard.  
BIOS Lenovo -[IVE113W-1.12]- 02/06/2018  
Memory:  
24x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

---

**Compiler Version Notes**

==============================================================================  
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)  
525.x264_r(base, peak) 557.xz_r(base, peak)  
==============================================================================  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================

(Continued on next page)
**SPEC CPU2017 Integer Rate Result**

Lenovo Global Technology
ThinkSystem SN550  
(2.00 GHz, Intel Xeon Silver 4109T)  

**SPECrates**

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**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
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**Test Date:** Jun-2018  
**Hardware Availability:** Aug-2017  
**Software Availability:** Feb-2018

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**Compiler Version Notes (Continued)**

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<td>(C) 1985-2017 Intel Corporation. All rights reserved.</td>
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<td>520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)</td>
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<td>ifort (IFORT)</td>
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<tr>
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**Base Compiler Invocation**

**C benchmarks:**
- icc

**C++ benchmarks:**
- icpc

**Fortran benchmarks:**
- ifort

**Base Portability Flags**

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

(Continued on next page)
# SPEC CPU2017 Integer Rate Result

## Lenovo Global Technology
ThinkSystem SN550  
(2.00 GHz, Intel Xeon Silver 4109T)

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## Base Portability Flags (Continued)

- 502.gcc_r: -DSPEC_LP64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

## Base Optimization Flags

### C benchmarks:

```bash
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

### C++ benchmarks:

```bash
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

### Fortran benchmarks:

```bash
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

## Base Other Flags

### C benchmarks:

```bash
-m64 -std=c11
```

### C++ benchmarks:

```bash
-m64
```

### Fortran benchmarks:

```bash
-m64
```

## Peak Compiler Invocation

C benchmarks:

```bash
icc
```

(Continued on next page)
Lenovo Global Technology
ThinkSystem SN550
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SPECrate2017_int_base = 69.5
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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jun-2018
Hardware Availability: Aug-2017
Software Availability: Feb-2018

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
-ljemalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -fno-alias
-L/usr/local/je5.0.1-64/lib -ljemalloc

(Continued on next page)
Lenovo Global Technology
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Software Availability: Feb-2018

Peak Optimization Flags (Continued)

557.xz_r: Same as 505.mcf_r

C++ benchmarks:
520.omnetpp_r: -Wl, -z, multidefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc
523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl, -z, multidefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc
531.deepsjeng_r: Same as 520.omnetpp_r
541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl, -z, multidefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Other Flags

C benchmarks (except as noted below):
-m64 -std=c11
502.gcc_r: -m32 -std=c11

C++ benchmarks (except as noted below):
-m64
523.xalancbmk_r: -m32

Fortran benchmarks:
-m64

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.html
# SPEC CPU2017 Integer Rate Result

**Lenovo Global Technology**  
ThinkSystem SN550  
(2.00 GHz, Intel Xeon Silver 4109T)

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</tbody>
</table>

You can also download the XML flags sources by saving the following links:

- [Intel icl18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-icl18.0-official-linux64.xml)
- [Lenovo Platform SPECcpu2017-Flags-V1.2-SKL-C.xml](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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