## SPEC® CPU2017 Integer Rate Result

### Lenovo Global Technology
ThinkSystem SR570
(2.10 GHz, Intel Xeon Silver 4116T)

<table>
<thead>
<tr>
<th></th>
<th>SPECrate2017_int_base = 106</th>
<th>SPECrate2017_int_peak = 112</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU2017 License:</td>
<td>9017</td>
<td></td>
</tr>
<tr>
<td>Test Sponsor:</td>
<td>Lenovo Global Technology</td>
<td></td>
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<tr>
<td>Tested by:</td>
<td>Lenovo Global Technology</td>
<td></td>
</tr>
<tr>
<td>Test Date:</td>
<td>Jun-2018</td>
<td></td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Nov-2017</td>
<td></td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Feb-2018</td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>Component</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Silver 4116T</td>
</tr>
<tr>
<td>Max MHz.</td>
<td>3000</td>
</tr>
<tr>
<td>Nominal</td>
<td>2100</td>
</tr>
<tr>
<td>Enabled</td>
<td>24 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3</td>
<td>16.5 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x 800 GB SAS SSD</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Component</th>
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</thead>
<tbody>
<tr>
<td>OS</td>
<td>SUSE Linux Enterprise Server 12 SP3 (x86_64)</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 18.0.0.128 of Intel C/C++</td>
</tr>
<tr>
<td>Compiler for Linux</td>
<td></td>
</tr>
<tr>
<td>Fortran</td>
<td>Version 18.0.0.128 of Intel Fortran</td>
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<tr>
<td>Compiler for Linux</td>
<td></td>
</tr>
<tr>
<td>Parallel</td>
<td>No</td>
</tr>
<tr>
<td>Firmware</td>
<td>Lenovo BIOS Version TEE119R 1.22 released Feb-2018</td>
</tr>
<tr>
<td>File System</td>
<td>btrfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>jemalloc: jemalloc memory allocator library V5.0.1</td>
</tr>
</tbody>
</table>
## Lenovo Global Technology

**ThinkSystem SR570**  
(2.10 GHz, Intel Xeon Silver 4116T)

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>48</td>
<td>942</td>
<td>81.1</td>
<td>947</td>
<td>80.7</td>
<td>944</td>
<td>80.9</td>
<td>48</td>
<td>767</td>
<td>99.6</td>
<td>774</td>
<td>98.7</td>
<td>770</td>
<td>99.2</td>
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<tr>
<td>502.gcc_r</td>
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<td>732</td>
<td>92.9</td>
<td>742</td>
<td>91.6</td>
<td>743</td>
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<td>48</td>
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<td>505.mcf_r</td>
<td>48</td>
<td>587</td>
<td>132</td>
<td>588</td>
<td>132</td>
<td>596</td>
<td>130</td>
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<td>594</td>
<td>131</td>
<td>593</td>
<td>131</td>
<td>596</td>
<td>130</td>
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<td>520.omnetpp_r</td>
<td>48</td>
<td>895</td>
<td>70.4</td>
<td>905</td>
<td>69.6</td>
<td>930</td>
<td>67.7</td>
<td>48</td>
<td>971</td>
<td>64.9</td>
<td>965</td>
<td>65.3</td>
<td>970</td>
<td>65.0</td>
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<tr>
<td>523.xmlancbmk_r</td>
<td>48</td>
<td>458</td>
<td>111</td>
<td>461</td>
<td>110</td>
<td>460</td>
<td>110</td>
<td>48</td>
<td>388</td>
<td>131</td>
<td>390</td>
<td>130</td>
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<td>525.x264_r</td>
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<td>217</td>
<td>389</td>
<td>216</td>
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<td>531.deepsjeng_r</td>
<td>48</td>
<td>601</td>
<td>91.5</td>
<td>606</td>
<td>90.8</td>
<td>607</td>
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<td>91.0</td>
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<tr>
<td>541.leela_r</td>
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<td>918</td>
<td>86.6</td>
<td>918</td>
<td>86.5</td>
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<td>892</td>
<td>87.2</td>
<td>908</td>
<td>87.6</td>
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<tr>
<td>548.exchange2_r</td>
<td>48</td>
<td>620</td>
<td>203</td>
<td>622</td>
<td>202</td>
<td>621</td>
<td>202</td>
<td>48</td>
<td>621</td>
<td>203</td>
<td>620</td>
<td>203</td>
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<td>203</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>48</td>
<td>701</td>
<td>73.9</td>
<td>705</td>
<td>73.5</td>
<td>704</td>
<td>73.7</td>
<td>48</td>
<td>707</td>
<td>73.3</td>
<td>707</td>
<td>73.3</td>
<td>709</td>
<td>73.1</td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base** = 106  
**SPECrate2017_int_peak** = 112

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop_caches

runcpu command invoked through numactl i.e.:

numactl --interleave=all runcpu <etc>

jemalloc: configured and built at default for

32bit (i686) and 64bit (x86_64) targets;

jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;

jemalloc: sources available from jemalloc.net or
Lenovo Global Technology

ThinkSystem SR570
(2.10 GHz, Intel Xeon Silver 4116T)

SPECrate2017_int_base = 106
SPECrate2017_int_peak = 112

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Jun-2018
Tested by: Lenovo Global Technology
Hardware Availability: Nov-2017
Software Availability: Feb-2018

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
DCU Streamer Prefetcher set to Disable
MONITOR/MWAIT set to Enable
Execute Disable Bit set to Disable
DCA set to Enable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bdc091c0f
running on linux-uru4 Fri Jun 8 09:44:45 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4116T CPU @ 2.10GHz
  2 "physical id"s (chips)
  48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 2
Core(s) per socket: 12
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6

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Lenovo Global Technology
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Tested by: Lenovo Global Technology

Platform Notes (Continued)

Model: 85
Model name: Intel(R) Xeon(R) Silver 4116T CPU @ 2.10GHz
Stepping: 4
CPU MHz: 2095.084
BogoMIPS: 4190.16
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 16896K
NUMA node0 CPU(s): 0-11,24-35
NUMA node1 CPU(s): 12-23,36-47
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpes1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
dtheorem intel_pt rsb_cts xsw spec_ctrl retpoline kaiser tpr_shadow vnmi flexpriority
epi etpt fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ibrms invpcid rtm cqm mx
avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt
xsavexc xgetbv1 cqm_llc cqm_occup_llc pkp ospke

/proc/cpuinfo cache data
  cache size : 16896 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 24 25 26 27 28 29 30 31 32 33 34 35
  node 0 size: 96057 MB
  node 0 free: 95619 MB
  node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23 36 37 38 39 40 41 42 43 44 45 46 47
  node 1 size: 96747 MB
  node 1 free: 96246 MB
node distances:
  node 0 1
  0: 10 21
  1: 21 10

From /proc/meminfo
  MemTotal: 197432860 KB
  HugePages_Total: 0
  Hugepagesize: 2048 KB

From /etc/*release* /etc/*version*
  SuSE-release:
Lenovo Global Technology
ThinkSystem SR570
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Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Platform Notes (Continued)

SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP3"
  VERSION_ID="12.3"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
Linux linux-uru4 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 8 09:42

SPEC is set to: /home/cpu2017.1.0.2.ic18.0
FileSystem Type Size Used Avail Use% Mounted on
/dev/sdb2 btrfs 744G 96G 649G 13% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS Lenovo -[TEE119R-1.22]- 02/06/2018
Memory:
  4x NO DIMM NO DIMM
  12x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes
==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
     525.x264_r(base, peak) 557.xz_r(base, peak)
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================

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Lenovo Global Technology
ThinkSystem SR570
(2.10 GHz, Intel Xeon Silver 4116T)

CPU2017 License: 9017
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SPECrate2017_int_base = 106
SPECrate2017_int_peak = 112

Compiler Version Notes (Continued)

CC  500.perlbench_r(peak) 502.gcc_r(peak)

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
      541.leela_r(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
      541.leela_r(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC  548.exchange2_r(base, peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

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**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  

**Base Portability Flags (Continued)**

- 502.gcc_r: -DSPEC_LP64  
- 505.mcf_r: -DSPEC_LP64  
- 520.omnetpp_r: -DSPEC_LP64  
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
- 525.x264_r: -DSPEC_LP64  
- 531.deepsjeng_r: -DSPEC_LP64  
- 541.leela_r: -DSPEC_LP64  
- 548.exchange2_r: -DSPEC_LP64  
- 557.xz_r: -DSPEC_LP64  

**Base Optimization Flags**

**C benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
- -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc  

**C++ benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
- -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc  

**Fortran benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
- -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
- -L/usr/local/je5.0.1-64/lib -ljemalloc

**Base Other Flags**

**C benchmarks:**
- -m64 -std=c11

**C++ benchmarks:**
- -m64

**Fortran benchmarks:**
- -m64

**Peak Compiler Invocation**

**C benchmarks:**
- icc

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**SPEC CPU2017 Integer Rate Result**

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</table>

**SPECrater2017_int_base = 106**  
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**Peak Compiler Invocation (Continued)**

C++ benchmarks:
- icpc

Fortran benchmarks:
- ifort

---

**Peak Portability Flags**

500/perl_r -DSPEC_LP64 -DSPEC_LINUX_X64  
502/gcc_r -D_FILE_OFFSET_BITS=64  
505/mcf_r -DSPEC_LP64  
520/omnetpp_r -DSPEC_LP64  
523/xalancbmk_r -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX  
525/x264_r -DSPEC_LP64  
531/deepsjeng_r -DSPEC_LP64  
541/leela_r -DSPEC_LP64  
548/exchange2_r -DSPEC_LP64  
557/xz_r -DSPEC_LP64

---

**Peak Optimization Flags**

C benchmarks:
- 500/perl_r -W1,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
  -xCORE-AVX512 -03 -no-prec-div -qopt-mem-layout-trans=3  
  -fno-strict-overflow -L/usr/local/je5.0.1-64/lib  
  -ljemalloc

- 502/gcc_r -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32  
  -W1,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
  -xCORE-AVX512 -03 -no-prec-div -qopt-mem-layout-trans=3  
  -L/usr/local/je5.0.1-32/lib -ljemalloc

- 505/mcf_r -W1,-z,muldefs -xCORE-AVX512 -ipo -03 -no-prec-div  
  -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib  
  -ljemalloc

- 525/x264_r -W1,-z,muldefs -xCORE-AVX512 -ipo -03 -no-prec-div  
  -qopt-mem-layout-trans=3 -fno-alias  
  -L/usr/local/je5.0.1-64/lib -ljemalloc

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### Peak Optimization Flags (Continued)

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-\text{-L/\text{usr/local/je5.0.1-64/lib -ljemalloc}}

523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-\text{-L/\text{usr/local/je5.0.1-32/lib -ljemalloc}}

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-\text{-L/\text{usr/local/je5.0.1-64/lib -ljemalloc}}

### Peak Other Flags

C benchmarks (except as noted below):
-m64 -std=c11
502.gcc_r: -m32 -std=c11

C++ benchmarks (except as noted below):
-m64
523.xalancbmk_r: -m32

Fortran benchmarks:
-m64

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.html
Lenovo Global Technology  
ThinkSystem SR570  
(2.10 GHz, Intel Xeon Silver 4116T)  

SPECratenet2017_int_base = 106  
SPECratenet2017_int_peak = 112  

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Test Sponsor: Lenovo Global Technology  
Tested by: Lenovo Global Technology  

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Hardware Availability: Nov-2017  
Software Availability: Feb-2018

You can also download the XML flags sources by saving the following links:  
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml  
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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