## NEC Corporation

**Express5800/R120h-2E (Intel Xeon Bronze 3104)**

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>SPECrate2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>33.0</td>
<td>34.5</td>
</tr>
</tbody>
</table>

### CPU2017 License: 9006
- **Test Sponsor:** NEC Corporation
- **Tested by:** NEC Corporation
- **Test Date:** Jun-2018
- **Hardware Availability:** Nov-2017
- **Software Availability:** Mar-2018

### Hardware

<table>
<thead>
<tr>
<th>Component</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU Name</strong></td>
<td>Intel Xeon Bronze 3104</td>
</tr>
<tr>
<td><strong>Max MHz.</strong></td>
<td>1700</td>
</tr>
<tr>
<td><strong>Nominal</strong></td>
<td>1700</td>
</tr>
<tr>
<td><strong>Enabled</strong></td>
<td>12 cores, 2 chips</td>
</tr>
<tr>
<td><strong>Orderable</strong></td>
<td>1.2 chips</td>
</tr>
<tr>
<td><strong>Cache L1</strong></td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td><strong>Cache L2</strong></td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td><strong>Cache L3</strong></td>
<td>8.25 MB I+D on chip per chip</td>
</tr>
<tr>
<td><strong>Other</strong></td>
<td>None</td>
</tr>
<tr>
<td><strong>Memory</strong></td>
<td>192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2133)</td>
</tr>
<tr>
<td><strong>Storage</strong></td>
<td>1 x 600 GB SAS, 15000 RPM, RAID 0</td>
</tr>
<tr>
<td><strong>Other</strong></td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Component</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>OS</strong></td>
<td>Red Hat Enterprise Linux Server release 7.4</td>
</tr>
<tr>
<td></td>
<td>(Maipo)</td>
</tr>
<tr>
<td><strong>Kernel</strong></td>
<td>3.10.0-693.21.1.el7.x86_64</td>
</tr>
<tr>
<td><strong>Compiler</strong></td>
<td>C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux;</td>
</tr>
<tr>
<td></td>
<td>Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td><strong>Parallel</strong></td>
<td>No</td>
</tr>
<tr>
<td><strong>Firmware</strong></td>
<td>NEC BIOS Version U31 02/14/2018 released Mar-2018</td>
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<tr>
<td><strong>File System</strong></td>
<td>ext4</td>
</tr>
<tr>
<td><strong>System State</strong></td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td><strong>Base Pointers</strong></td>
<td>64-bit</td>
</tr>
<tr>
<td><strong>Peak Pointers</strong></td>
<td>32/64-bit</td>
</tr>
<tr>
<td><strong>Other</strong></td>
<td>jemalloc memory allocator V5.0.1, see general notes</td>
</tr>
</tbody>
</table>
SPEC CPU2017 Integer Rate Result

NEC Corporation

Express5800/R120h-2E (Intel Xeon Bronze 3104)

SPECrate2017_int_base = 33.0
SPECrate2017_int_peak = 34.5

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>12</td>
<td>689</td>
<td>27.7</td>
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<td>27.7</td>
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<tr>
<td>502.gcc_r</td>
<td>12</td>
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<td>31.3</td>
<td>542</td>
<td>31.4</td>
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<td>505.mcf_r</td>
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<tr>
<td>520.omnetpp_r</td>
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<td>659</td>
<td>23.9</td>
<td>661</td>
<td>23.8</td>
<td>654</td>
<td>24.1</td>
<td>657</td>
<td>24.0</td>
<td>651</td>
<td>24.2</td>
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<td>523.xalanckmk_r</td>
<td>12</td>
<td>361</td>
<td>35.1</td>
<td>362</td>
<td>35.0</td>
<td>361</td>
<td>35.1</td>
<td>331</td>
<td>38.3</td>
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<td>38.5</td>
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<tr>
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<td>12</td>
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<td>61.1</td>
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<td>541.leela_r</td>
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<td>848</td>
<td>23.4</td>
<td>847</td>
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<td>548.exchange2_r</td>
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<td>64.6</td>
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<td>64.6</td>
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<tr>
<td>557.xz_r</td>
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<td>21.9</td>
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<td>21.9</td>
<td>592</td>
<td>21.9</td>
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<td>592</td>
<td>21.9</td>
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SPECrate2017_int_base = 33.0
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

(Continued on next page)
# SPEC CPU2017 Integer Rate Result

**NEC Corporation**

Express5800/R120h-2E (Intel Xeon Bronze 3104)

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**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Test Date:** Jun-2018  
**Hardware Availability:** Nov-2017  
**Tested by:** NEC Corporation  
**Software Availability:** Mar-2018

## General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  

## Platform Notes

**BIOS Settings:**  
Thermal Configuration: Maximum Cooling  
Workload Profile: General Throughput Compute  
Memory Patrol Scrubbing: Disabled  
LLC Dead Line Allocation: Disabled  
LLC Prefetch: Enabled  
Workload Profile: Custom  
Sub-NUMA Clustering: Disabled  
Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9  
running on r120h2e Mon Jun 18 20:02:45 2018

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz  
2 "physical id"s (chips)  
12 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 6  
siblings : 6  
physical 0: cores 0 1 2 3 4 5  
physical 1: cores 0 1 2 3 4 5

From lscpu:  
Architecture: x86_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 12  
On-line CPU(s) list: 0-11  
Thread(s) per core: 1  
Core(s) per socket: 6  
Socket(s): 2  
NUMA node(s): 2

(Continued on next page)
SPEC CPU2017 Integer Rate Result

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Express5800/R120h-2E (Intel Xeon Bronze 3104)

SPECrate2017_int_base = 33.0
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CPU2017 License: 9006
Test Sponsor: NEC Corporation
Test Date: Jun-2018
Tested by: NEC Corporation
Hardware Availability: Nov-2017
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Platform Notes (Continued)

Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
Stepping: 4
CPU MHz: 1700.000
BogoMIPS: 3400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 8448K
NUMA node0 CPU(s): 0-2,6-8
NUMA node1 CPU(s): 3-5,9-11
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma
cx16 xtpr pdcm pclid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3nowprefetch epb cat_l3 cdp_l3 invpcid_single
intel_pt spec_ctrl ibpb_support tpr_shadow vnmi flexpriority ept vpid fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq
rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1
cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm arat pln pts

/proc/cpuinfo cache data
  cache size : 8448 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 6 7 8
  node 0 size: 97964 MB
  node 0 free: 95485 MB
  node 1 cpus: 3 4 5 9 10 11
  node 1 size: 98303 MB
  node 1 free: 95972 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10

From /proc/meminfo
  MemTotal: 197752372 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

(Continued on next page)
SPEC CPU2017 Integer Rate Result

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Platform Notes (Continued)

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.4 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VARIANT="Server"
    VARIANT_ID="server"
    VERSION_ID="7.4"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:
  Linux r120h2e 3.10.0-693.21.1.el7.x86_64 #1 SMP Fri Feb 23 18:54:16 UTC 2018 x86_64
  x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)

run-level 3 Jun 18 19:57

SPEC is set to: /home/cpu2017
  Filesystem     Type  Size  Used Avail Use% Mounted on
  /dev/sda3      ext4  542G  260G  255G  51% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMIOS" standard.
  BIOS NEC U31 02/14/2018
  Memory:
    4x UNKNOWN NOT AVAILABLE
    12x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666, configured at 2133

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
  557.xz_r(base)

(Continued on next page)
NEC Corporation
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Compiler Version Notes (Continued)

icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

--------------------------------------------------------------------------------
CC 500.perlbench_r(peak) 502.gcc_r(peak) 505.mcf_r(peak) 525.x264_r(peak)
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--------------------------------------------------------------------------------
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)
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--------------------------------------------------------------------------------
FC 548.exchange2_r(base)
--------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
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FC 548.exchange2_r(peak)
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</tbody>
</table>

### Base Compiler Invocation

C benchmarks:
```bash
icc -m64 -std=c11
```

C++ benchmarks:
```bash
icpc -m64
```

Fortran benchmarks:
```bash
ifort -m64
```

### Base Portability Flags

- `500.perlbench_r`: `-DSPEC_LP64 -DSPEC_LINUX_X64`
- `502.gcc_r`: `-DSPEC_LP64`
- `505.mcf_r`: `-DSPEC_LP64`
- `520.omnetpp_r`: `-DSPEC_LP64`
- `523.xalancbmk_r`: `-DSPEC_LP64 -DSPEC_LINUX`
- `525.x264_r`: `-DSPEC_LP64`
- `531.deepsjeng_r`: `-DSPEC_LP64`
- `541.leela_r`: `-DSPEC_LP64`
- `548.exchange2_r`: `-DSPEC_LP64`
- `557.xz_r`: `-DSPEC_LP64`

### Base Optimization Flags

**C benchmarks:**
```
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

**C++ benchmarks:**
```
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

**Fortran benchmarks:**
```
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc
```
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#### Peak Compiler Invocation

C benchmarks (except as noted below):
```bash
icc -m64 -std=c11
```

502.gcc_r: `icc -m32 -std=c11 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin`

C++ benchmarks (except as noted below):
```bash
icpc -m64
```

523.xalancbmk_r: `icpc -m32 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin`

Fortran benchmarks:
```bash
ifort -m64
```

#### Peak Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-D_FILE_OFFSET_BITS=64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

#### Peak Optimization Flags

C benchmarks:
```bash
500.perlbench_r: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3 -fno-strict-overflow -L/usr/local/je5.0.1-64/lib -ljemalloc
```

502.gcc_r: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-32/lib -ljemalloc`

505.mcf_r: `basepeak = yes`

(Continued on next page)
NEC Corporation

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Peak Optimization Flags (Continued)

525.x264_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-fno-alias -L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-L/usr/local/je5.0.1-64/lib -ljemalloc

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: basepeak = yes

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at:

http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevB.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevB.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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