Lenovo Global Technology
ThinkSystem SD530
(2.60 GHz, Intel Xeon Silver 4112)

<table>
<thead>
<tr>
<th>SPECrate2017_int_base = 43.1</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_int_peak = 45.9</td>
</tr>
</tbody>
</table>

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: Jun-2018
Hardware Availability: Aug-2017
Software Availability: Feb-2018

<table>
<thead>
<tr>
<th>Copies</th>
<th>500.perlbench_r</th>
<th>502.gcc_r</th>
<th>505.mcf_r</th>
<th>520.omnetpp_r</th>
<th>523.xalancbmk_r</th>
<th>525.x264_r</th>
<th>531.deepsjeng_r</th>
<th>541.leela_r</th>
<th>548.exchange2_r</th>
<th>557.xz_r</th>
</tr>
</thead>
<tbody>
<tr>
<td>16</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SPECrate2017_int_base (43.1)</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>SPECrate2017_int_peak (45.9)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>39.7</td>
<td>37.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td>31.7</td>
<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>85.3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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</tr>
</tbody>
</table>

Hardware
CPU Name: Intel Xeon Silver 4112
Max MHz.: 3000
Nominal: 2600
Enabled: 8 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 8.25 MB I+D on chip per chip
Other: None
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)
Storage: 1 x 800 GB SAS SSD
Other: None

Software
OS: SUSE Linux Enterprise Server 12 SP2 (x86_64)
Kernel 4.4.114-92.64-default
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++
Compiler for Linux:
Fortran: Version 18.0.0.128 of Intel Fortran
Compiler for Linux
Parallel: No
Firmware: Lenovo BIOS Version TEE119R 1.22 released Feb-2018
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc: jemalloc memory allocator library V5.0.1
**SPEC CPU2017 Integer Rate Result**

**Lenovo Global Technology**  
ThinkSystem SD530  
(2.60 GHz, Intel Xeon Silver 4112)  

| SPECrate2017_int_base = 43.1 | SPECrate2017_int_peak = 45.9 |

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Jun-2018  
**Hardware Availability:** Aug-2017  
**Software Availability:** Feb-2018

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>16</td>
<td>791</td>
<td>32.2</td>
<td>809</td>
<td>31.5</td>
<td>803</td>
<td>31.7</td>
<td>16</td>
<td>641</td>
<td>39.7</td>
<td>640</td>
<td>39.8</td>
<td>642</td>
<td>39.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>16</td>
<td>611</td>
<td>37.1</td>
<td>612</td>
<td>37.0</td>
<td>612</td>
<td>37.0</td>
<td>16</td>
<td>519</td>
<td>43.6</td>
<td>518</td>
<td>43.7</td>
<td>516</td>
<td>43.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>16</td>
<td>488</td>
<td>52.9</td>
<td>486</td>
<td>53.2</td>
<td>480</td>
<td>53.9</td>
<td>16</td>
<td>469</td>
<td>55.2</td>
<td>487</td>
<td>53.1</td>
<td>469</td>
<td>55.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>16</td>
<td>784</td>
<td>26.8</td>
<td>787</td>
<td>26.7</td>
<td>789</td>
<td>26.6</td>
<td>16</td>
<td>779</td>
<td>26.9</td>
<td>778</td>
<td>27.0</td>
<td>780</td>
<td>26.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>16</td>
<td>361</td>
<td>46.8</td>
<td>359</td>
<td>47.0</td>
<td>361</td>
<td>46.8</td>
<td>16</td>
<td>310</td>
<td>54.4</td>
<td>310</td>
<td>54.6</td>
<td>309</td>
<td>54.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>525.x264_r</td>
<td>16</td>
<td>328</td>
<td>85.5</td>
<td>331</td>
<td>84.7</td>
<td>329</td>
<td>85.3</td>
<td>16</td>
<td>318</td>
<td>88.2</td>
<td>317</td>
<td>88.4</td>
<td>317</td>
<td>88.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>16</td>
<td>491</td>
<td>37.3</td>
<td>491</td>
<td>37.4</td>
<td>491</td>
<td>37.3</td>
<td>16</td>
<td>490</td>
<td>37.4</td>
<td>489</td>
<td>37.5</td>
<td>490</td>
<td>37.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>541.leela_r</td>
<td>16</td>
<td>767</td>
<td>34.6</td>
<td>763</td>
<td>34.7</td>
<td>764</td>
<td>34.7</td>
<td>16</td>
<td>756</td>
<td>35.1</td>
<td>755</td>
<td>35.1</td>
<td>755</td>
<td>35.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>16</td>
<td>513</td>
<td>81.7</td>
<td>513</td>
<td>81.7</td>
<td>514</td>
<td>81.6</td>
<td>16</td>
<td>513</td>
<td>81.7</td>
<td>513</td>
<td>81.7</td>
<td>513</td>
<td>81.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>557.xz_r</td>
<td>16</td>
<td>550</td>
<td>31.4</td>
<td>550</td>
<td>31.4</td>
<td>550</td>
<td>31.4</td>
<td>16</td>
<td>549</td>
<td>31.5</td>
<td>550</td>
<td>31.4</td>
<td>552</td>
<td>31.3</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base =** 43.1  
**SPECrate2017_int_peak =** 45.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop_caches  
runcpu command invoked through numactl i.e.:  
umactl --interleave=all runcpu <etc>

jemalloc: configured and built at default for  
32bit (i686) and 64bit (x86_64) targets;  
jemalloc: built with the RedHat Enterprise 7.4,  
and the system compiler gcc 4.8.5;  
jemalloc: sources available from jemalloc.net or

(Continued on next page)
Lenovo Global Technology
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---

**General Notes (Continued)**

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

---

**Platform Notes**

BIOS configuration:
Choose Operating Mode set to Maximum Performance
SNC set to Enable
DCU Streamer Prefetcher set to Disable
MONITOR/MWAIT set to Enable
Execute Disable Bit set to Disable
DCA set to Enable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618b9cc091c0f
running on Staek-04 Wed Jun 20 02:45:58 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
2  "physical id"s (chips)
16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 8
physical 0: cores 1 2 4 5
physical 1: cores 1 2 4 5

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Thread(s) per core: 2
Core(s) per socket: 4
Socket(s): 2
NUMA node(s): 2

(Continued on next page)
Lenovo Global Technology

ThinkSystem SD530
(2.60 GHz, Intel Xeon Silver 4112)

SPECrate2017_int_base = 43.1
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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
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Platform Notes (Continued)

Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
Stepping:              4
CPU MHz:               2593.905
BogoMIPS:              5187.81
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              8448K
NUMA node0 CPU(s):     0-3,8-11
NUMA node1 CPU(s):     4-7,12-15
Flags:                 fpu vme de pse tsc msr pae mce cmov
                       pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                       lm constant-tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
                       aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
                       fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
                       xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
                       dtherm intel_pt rsb_ctxtsw spec_ctrl retpoline kaiser tpr_shadow vnmi flexpriority
                       ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cmp mpx
                       avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt
                       xsavec xgetbv1 cqm_llc cqm_occup_llc

From /proc/cpuinfo cache data
  cache size : 8448 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 8 9 10 11
  node 0 size: 193110 MB
  node 0 free: 192699 MB
  node 1 cpus: 4 5 6 7 12 13 14 15
  node 1 size: 193504 MB
  node 1 free: 193100 MB
  node distances:
  node 0 1
  0: 10 21
  1: 21 10

From /proc/meminfo
  MemTotal:       395894048 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

(Continued on next page)
Lenovo Global Technology

ThinkSystem SD530 (2.60 GHz, Intel Xeon Silver 4112)

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</tr>
</thead>
<tbody>
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</tr>
</tbody>
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Platform Notes (Continued)

From /etc/*release* /etc/*version*

SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.

os-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
  Linux Staek-04 4.4.114-92.64-default #1 SMP Thu Feb 1 19:18:19 UTC 2018 (c6ce5db)
  x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 20 02:45

SPEC is set to: /home/cpu2017.1.0.2.ic18.0

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda4</td>
<td>xfs</td>
<td>689G</td>
<td>56G</td>
<td>634G</td>
<td>9%</td>
<td>/home</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS Lenovo -[TEE119R-1.22]- 02/06/2018
  Memory:
    4x NO DIMM NO DIMM
    12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) |
| 525.x264_r(base, peak) 557.xz_r(base, peak) |
==============================================================================

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

(Continued on next page)
**Compiler Version Notes (Continued)**

```
CC  500.perlibench_r(peak) 502.gcc_r(peak)
    icc (ICC) 18.0.0 20170811
    Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
      541.leela_r(base)
    icpc (ICC) 18.0.0 20170811
    Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
      541.leela_r(peak)
    icpc (ICC) 18.0.0 20170811
    Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC  548.exchange2_r(base, peak)
    ifort (IFORT) 18.0.0 20170811
    Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

**Base Compiler Invocation**

- C benchmarks: icc
- C++ benchmarks: icpc
- Fortran benchmarks: ifort
Lenovo Global Technology
ThinkSystem SD530
(2.60 GHz, Intel Xeon Silver 4112)

| SPECrate2017_int_base | 43.1 |
| SPECrate2017_int_peak | 45.9 |

CPU2017 License: 9017
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---

### Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

---

### Base Optimization Flags

**C benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
  -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

**C++ benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
  -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

**Fortran benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
  -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
  -L/usr/local/je5.0.1-64/lib -ljemalloc

---

### Base Other Flags

**C benchmarks:**
- -m64 -std=c11

**C++ benchmarks:**
- -m64

**Fortran benchmarks:**
- -m64
Lenovo Global Technology

ThinkSystem SD530
(2.60 GHz, Intel Xeon Silver 4112)

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CPU2017 License: 9017
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Tested by: Lenovo Global Technology

Test Date: Jun-2018
Hardware Availability: Aug-2017
Software Availability: Feb-2018

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

502.gcc_r: -L/opt/intel/compiler_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
-ljemalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -fno-alias

(Continued on next page)
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ThinkSystem SD530
(2.60 GHz, Intel Xeon Silver 4112)

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>43.1</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_int_peak</td>
<td>45.9</td>
</tr>
</tbody>
</table>

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Peak Optimization Flags (Continued)

525.x264_r (continued):
-L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Other Flags

C benchmarks (except as noted below):
-m64 -std=c11

502.gcc_r: -m32 -std=c11

C++ benchmarks (except as noted below):
-m64

523.xalancbmk_r: -m32

Fortran benchmarks:
-m64

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.html
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SPECrate2017_int_base = 43.1
SPECrate2017_int_peak = 45.9

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Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jun-2018
Hardware Availability: Aug-2017
Software Availability: Feb-2018

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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