New H3C Technologies Co., Ltd. | SPECspeed2017_fp_base = 159
---|---
H3C UniServer R6900 G3 (Intel Xeon Gold 6142) | SPECspeed2017_fp_peak = 161

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>0</td>
<td>193</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>0</td>
<td>197</td>
</tr>
<tr>
<td>619.ibm_s</td>
<td>0</td>
<td>78.9</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>0</td>
<td>75.4</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>0</td>
<td>132</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>0</td>
<td>132</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>0</td>
<td>177</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>0</td>
<td>180</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>0</td>
<td>318</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>0</td>
<td>212</td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Gold 6142  
- **Max MHz.:** 3700  
- **Nominal:** 2600  
- **Enabled:** 64 cores, 4 chips  
- **Orderable:** 1,2,3,4 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **Cache L2:** 1 MB I+D on chip per core  
- **Cache L3:** 22 MB I+D on chip per chip  
- **Memory:** 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)  
- **Storage:** 1 x 600 GB SAS HDD,10000 RPM  
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux Server release 7.5 (Maipo)  
  3.10.0-862.3.2.el7.x86_64  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++  
  Compiler for Linux: Fortran: Version 18.0.0.128 of Intel Fortran  
  Compiler for Linux  
- **Parallel:** Yes  
- **Firmware:** INSYDE Corp. BIOS Version 1.00.16 Released May-2018  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None
New H3C Technologies Co., Ltd.  
H3C UniServer R6900 G3 (Intel Xeon Gold 6142)

SPECspeed2017_fp_base = 159
SPECspeed2017_fp_peak = 161

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>64</td>
<td>893</td>
<td>65.8</td>
<td>896</td>
<td>66.0</td>
<td>894</td>
<td>66.0</td>
<td>894</td>
<td>66.0</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>64</td>
<td>193</td>
<td>87.4</td>
<td>191</td>
<td>86.3</td>
<td>193</td>
<td>88.4</td>
<td>197</td>
<td>84.8</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>64</td>
<td>78.9</td>
<td>67.9</td>
<td>77.1</td>
<td>66.2</td>
<td>79.2</td>
<td>66.7</td>
<td>78.6</td>
<td>69.1</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>64</td>
<td>178</td>
<td>74.5</td>
<td>175</td>
<td>75.4</td>
<td>175</td>
<td>78.8</td>
<td>174</td>
<td>76.0</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>64</td>
<td>132</td>
<td>67.1</td>
<td>132</td>
<td>66.1</td>
<td>134</td>
<td>67.0</td>
<td>133</td>
<td>67.0</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>64</td>
<td>54.6</td>
<td>215</td>
<td>55.3</td>
<td>209</td>
<td>56.8</td>
<td>210</td>
<td>56.6</td>
<td>211</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>64</td>
<td>171</td>
<td>81.5</td>
<td>177</td>
<td>80.2</td>
<td>180</td>
<td>80.1</td>
<td>180</td>
<td>80.3</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>64</td>
<td>318</td>
<td>54.9</td>
<td>318</td>
<td>54.8</td>
<td>319</td>
<td>54.7</td>
<td>320</td>
<td>55.0</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>64</td>
<td>117</td>
<td>79.0</td>
<td>115</td>
<td>77.9</td>
<td>117</td>
<td>79.0</td>
<td>115</td>
<td>76.1</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>64</td>
<td>216</td>
<td>75.7</td>
<td>208</td>
<td>74.4</td>
<td>212</td>
<td>70.1</td>
<td>225</td>
<td>71.0</td>
</tr>
</tbody>
</table>

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/speccpu/lib/ia32:/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-32:/home/speccpu/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Set Power Supply Mode to Performance
Set Hyper-Threading to Disabled

(Continued on next page)
New H3C Technologies Co., Ltd. | SPECspeed2017_fp_base = 159
---|---
H3C UniServer R6900 G3 (Intel Xeon Gold 6142) | SPECspeed2017_fp_peak = 161

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Test Date:** Jun-2018  
**Tested by:** New H3C Technologies Co., Ltd.  
**Hardware Availability:** Sep-2017  
**Software Availability:** May-2018

---

**Platform Notes (Continued)**

Set DCU Streamer Prefetcher to Disabled  
Set LLC dead line alloc to Disabled  
Set Trusted Execution Technology to Enabled  
Set Patrol Scrub to Disabled  
Sysinfo program /home/spec/cpu/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f running on localhost.localdomain Tue Jun 26 08:56:54 2018

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 6142 CPU @ 2.60GHz
  4 "physical id"s (chips)
  64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
  siblings : 16
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6142 CPU @ 2.60GHz
Stepping: 4
CPU MHz: 1831.933
CPU max MHz: 3700.0000
CPU min MHz: 1000.0000
BogoMIPS: 5200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
```

(Continued on next page)
New H3C Technologies Co., Ltd.

H3C UniServer R6900 G3 (Intel Xeon Gold 6142)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>159</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_fp_peak</td>
<td>161</td>
</tr>
</tbody>
</table>

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Platform Notes (Continued)

L2 cache: 1024K
L3 cache: 22528K
NUMA node0 CPU(s): 0-15
NUMA node1 CPU(s): 16-31
NUMA node2 CPU(s): 32-47
NUMA node3 CPU(s): 48-63
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 intel_pip
intel_pt mba tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2
smep bmi2 erms invpcid rdmsk rdtscp cpuid msr dtes64_64bitcap nuca pdcm pid intel_pxe
clflushopt clwb avx512cd avx512bw avx512vl xsaves xsavesrc xgetbv1 cmx16t cmx8064t
clwb avx512ase float128intel_pmca cmx128t cmx128 elevatedinterrupts 512 euclidean
/proc/cpuinfo

Cache data

Cache size: 22528 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 194957 MB
node 0 free: 183191 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
node 1 size: 196608 MB
node 1 free: 192033 MB
node 2 cpus: 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
node 2 size: 196608 MB
node 2 free: 185736 MB
node 3 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63
node 3 size: 196608 MB
node 3 free: 189721 MB
node distances:

node 0 1 2 3
0: 10 21 21 21
1: 21 10 21 21
2: 21 21 10 21
3: 21 21 21 10

From /proc/meminfo

MemTotal: 790695980 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

(Continued on next page)
New H3C Technologies Co., Ltd.
H3C UniServer R6900 G3 (Intel Xeon Gold 6142)

**SPEC** CPU2017 Floating Point Speed Result

**SPECspeed2017_fp_base** = 159
**SPECspeed2017_fp_peak** = 161

---

**Platform Notes (Continued)**

From /etc/*release* /etc/*version*

```lua
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.5 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VARIANT="Server"
    VARIANT_ID="server"
    VERSION_ID="7.5"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.5 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.5 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.5 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.5:ga:server
```

```bash
uname -a:
Linux localhost.localdomain 3.10.0-862.3.2.el7.x86_64 #1 SMP Mon May 21 23:36:36 UTC 2018 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Jun 26 01:36
```

SPEC is set to: /home/speccpu

```bash
Filesystem            Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   504G   62G  443G  13% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard. BIOS INSYDE Corp. 1.00.16P00 05/30/2018

Memory:
48x Hynix HMA82GR7AFR8N-VK 16 GB 2 rank 2666

(End of data from sysinfo program)

---

**Compiler Version Notes**

```
CC 619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
```

```
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```
CC 619.lbm_s(peak)
```

(Continued on next page)
New H3C Technologies Co., Ltd.
H3C UniServer R6900 G3 (Intel Xeon Gold 6142)

SPECspeed2017_fp_base = 159
SPECspeed2017_fp_peak = 161

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Test Date: Jun-2018
Hardware Availability: Sep-2017
Tested by: New H3C Technologies Co., Ltd.
Software Availability: May-2018

Compiler Version Notes (Continued)

----
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------------
FC 607.cactuBSSN_s(base)
-------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------------
FC 607.cactuBSSN_s(peak)
-------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------------
FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
-------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------------
FC 603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)
-------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-------------------
CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
-------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811

(Continued on next page)
New H3C Technologies Co., Ltd.  | SPECspeed2017_fp_base = 159
H3C UniServer R6900 G3 (Intel Xeon Gold 6142)  | SPECspeed2017_fp_peak = 161

<table>
<thead>
<tr>
<th>CPU2017 License: 9066</th>
<th>Test Date: Jun-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: New H3C Technologies Co., Ltd.</td>
<td>Hardware Availability: Sep-2017</td>
</tr>
<tr>
<td>Tested by: New H3C Technologies Co., Ltd.</td>
<td>Software Availability: May-2018</td>
</tr>
</tbody>
</table>

---

### Compiler Version Notes (Continued)

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

```
CC  621.wrf_s(peak) 628.pop2_s(peak)
```

```
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

---

### Base Compiler Invocation

**C benchmarks:**

```
icc
```

**Fortran benchmarks:**

```
ifort
```

**Benchmarks using both Fortran and C:**

```
ifort icc
```

**Benchmarks using Fortran, C, and C++:**

```
icpc icc ifort
```

---

### Base Portability Flags

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
   -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```
New H3C Technologies Co., Ltd. | SPECspeed2017_fp_base = 159
---|---
H3C UniServer R6900 G3 (Intel Xeon Gold 6142) | SPECspeed2017_fp_peak = 161

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9066</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>New H3C Technologies Co., Ltd.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>New H3C Technologies Co., Ltd.</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Jun-2018</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Sep-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>May-2018</td>
</tr>
</tbody>
</table>

### Base Optimization Flags

**C benchmarks:**
- -xCORE-AVX2
- -ipo
- -O3
- -no-prec-div
- -qopt-prefetch
- -ffinite-math-only
- -qopt-mem-layout-trans=3
- -qopenmp
- -DSPEC_OPENMP

**Fortran benchmarks:**
- -DSPEC_OPENMP
- -xCORE-AVX2
- -ipo
- -O3
- -no-prec-div
- -qopt-prefetch
- -ffinite-math-only
- -qopt-mem-layout-trans=3
- -qopenmp
- -nostandard-realloc-lhs
- -align array32byte

**Benchmarks using both Fortran and C:**
- -xCORE-AVX2
- -ipo
- -O3
- -no-prec-div
- -qopt-prefetch
- -ffinite-math-only
- -qopt-mem-layout-trans=3
- -qopenmp
- -DSPEC_OPENMP
- -nostandard-realloc-lhs
- -align array32byte

**Benchmarks using Fortran, C, and C++:**
- -xCORE-AVX2
- -ipo
- -O3
- -no-prec-div
- -qopt-prefetch
- -ffinite-math-only
- -qopt-mem-layout-trans=3
- -qopenmp
- -DSPEC_OPENMP
- -nostandard-realloc-lhs
- -align array32byte

### Base Other Flags

**C benchmarks:**
- -m64
- -std=c11

**Fortran benchmarks:**
- -m64

**Benchmarks using both Fortran and C:**
- -m64
- -std=c11

**Benchmarks using Fortran, C, and C++:**
- -m64
- -std=c11

### Peak Compiler Invocation

**C benchmarks:**
- icc

**Fortran benchmarks:**
- ifort

(Continued on next page)
New H3C Technologies Co., Ltd.
H3C UniServer R6900 G3 (Intel Xeon Gold 6142)

**Peak Compiler Invocation (Continued)**

Benchmarks using both Fortran and C:
```
ifort icc
```

Benchmarks using Fortran, C, and C++:
```
icpc icc ifort
```

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

C benchmarks:
```
619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s
```

Fortran benchmarks:
```
-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte
```

Benchmarks using both Fortran and C:
```
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte
```

(Continued on next page)
Peak Optimization Flags (Continued)

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
-align array32byte

Peak Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevB.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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