SPEC® CPU2017 Integer Rate Result

NEC Corporation

Express5800/R120h-2E (Intel Xeon Gold 5122) SPECrate2017_int_base = 55.4
SPECrate2017_int_peak = 58.6

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation

Test Date: Jun-2018
Hardware Availability: Nov-2017
Software Availability: Mar-2018

Hardware
CPU Name: Intel Xeon Gold 5122
Max MHz.: 3700
Nominal: 3600
Enabled: 8 cores, 2 chips, 2 threads/core
Orderable: 1.2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 16.5 MB I+D on chip per chip
Other: None
Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R)
Storage: 1 x 600 GB SAS, 15000 RPM, RAID 0
Other: None

Software
OS: Red Hat Enterprise Linux Server release 7.4 (Maipo)
Kernel 3.10.0-693.21.1.el7.x86_64
Compiler: C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux:
Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux
Parallel: No
Firmware: NEC BIOS Version U31 02/14/2018 released Mar-2018
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>16</td>
<td>620</td>
<td>41.1</td>
<td>611</td>
<td>41.7</td>
<td>619</td>
<td>41.2</td>
<td>16</td>
<td>500</td>
<td>50.9</td>
<td>499</td>
<td>51.0</td>
<td>502</td>
<td>50.7</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>16</td>
<td>473</td>
<td>47.9</td>
<td>472</td>
<td>48.0</td>
<td>470</td>
<td>48.2</td>
<td>16</td>
<td>400</td>
<td>56.6</td>
<td>400</td>
<td>56.6</td>
<td>401</td>
<td>56.4</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>16</td>
<td>376</td>
<td>68.7</td>
<td>375</td>
<td>69.0</td>
<td>375</td>
<td>68.9</td>
<td>16</td>
<td>376</td>
<td>68.7</td>
<td>375</td>
<td>69.0</td>
<td>375</td>
<td>68.9</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>16</td>
<td>638</td>
<td>32.9</td>
<td>633</td>
<td>33.2</td>
<td>632</td>
<td>33.2</td>
<td>16</td>
<td>638</td>
<td>32.9</td>
<td>633</td>
<td>33.2</td>
<td>632</td>
<td>33.2</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>16</td>
<td>272</td>
<td>62.1</td>
<td>277</td>
<td>60.9</td>
<td>278</td>
<td>60.9</td>
<td>16</td>
<td>233</td>
<td>72.6</td>
<td>233</td>
<td>72.4</td>
<td>233</td>
<td>72.5</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>16</td>
<td>248</td>
<td>113</td>
<td>249</td>
<td>113</td>
<td>249</td>
<td>113</td>
<td>16</td>
<td>248</td>
<td>113</td>
<td>246</td>
<td>114</td>
<td>247</td>
<td>113</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>16</td>
<td>382</td>
<td>48.0</td>
<td>383</td>
<td>47.8</td>
<td>382</td>
<td>47.9</td>
<td>16</td>
<td>382</td>
<td>48.0</td>
<td>382</td>
<td>48.0</td>
<td>385</td>
<td>47.6</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>16</td>
<td>604</td>
<td>43.8</td>
<td>592</td>
<td>44.7</td>
<td>595</td>
<td>44.5</td>
<td>16</td>
<td>588</td>
<td>45.1</td>
<td>588</td>
<td>45.0</td>
<td>587</td>
<td>45.1</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>16</td>
<td>403</td>
<td>104</td>
<td>402</td>
<td>104</td>
<td>396</td>
<td>106</td>
<td>16</td>
<td>403</td>
<td>104</td>
<td>402</td>
<td>104</td>
<td>396</td>
<td>106</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>16</td>
<td>440</td>
<td>39.3</td>
<td>439</td>
<td>39.4</td>
<td>440</td>
<td>39.3</td>
<td>16</td>
<td>440</td>
<td>39.3</td>
<td>439</td>
<td>39.4</td>
<td>440</td>
<td>39.3</td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base = 55.4**

**SPECrate2017_int_peak = 58.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM

Memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
General Notes (Continued)

is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
Thermal Configuration: Maximum Cooling
Workload Profile: General Throughput Compute
Memory Patrol Scrubbing: Disabled
LLC Dead Line Allocation: Disabled
LLC Prefetch: Enabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on r120h2e Thu Jun 28 11:58:49 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5122 CPU @ 3.60GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 4
  siblings : 8
  physical 0: cores 2 3 4 10
  physical 1: cores 0 5 9 13

From lscpu:
  Architecture:         x86_64
  CPU op-mode(s):       32-bit, 64-bit
  Byte Order:           LittleEndian
  CPU(s):               16
  On-line CPU(s) list:  0-15
  Thread(s) per core:   2
  Core(s) per socket:   4
  Socket(s):            2
  NUMA node(s):         4
  Vendor ID:            GenuineIntel
  CPU family:           6
  Model:                85
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Platform Notes (Continued)

Model name: Intel(R) Xeon(R) Gold 5122 CPU @ 3.60GHz
Stepping: 4
CPU MHz: 3600.000
BogoMIPS: 7200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 16896K
NUMA node0 CPU(s): 0,1,8,9
NUMA node1 CPU(s): 2,3,10,11
NUMA node2 CPU(s): 4,5,12,13
NUMA node3 CPU(s): 6,7,14,15
Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsvavx f16c rdrand lahf_lm abm 3dnowprefetch epb cat _l3 cdp _l3 invpcid_single intel_pt spec_ctrl ibpb_support tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local dtmmer ida arat pln pts

/proc/cpuinfo cache data
  cache size: 16896 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0-3)
  node 0 cpus: 0 1 8 9
  node 0 size: 48812 MB
  node 0 free: 47496 MB
  node 1 cpus: 2 3 10 11
  node 1 size: 49152 MB
  node 1 free: 47911 MB
  node 2 cpus: 4 5 12 13
  node 2 size: 49152 MB
  node 2 free: 47987 MB
  node 3 cpus: 6 7 14 15
  node 3 size: 49151 MB
  node 3 free: 48020 MB
  node distances:
    node 0 1 2 3
    0: 10 21 31 31
    1: 21 10 31 31
    2: 31 31 10 21

(Continued on next page)
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Platform Notes (Continued)

3: 31 31 21 10

From /proc/meminfo
MemTotal: 197750724 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.4 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.4"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:
Linux r120h2e 3.10.0-693.21.1.el7.x86_64 #1 SMP Fri Feb 23 18:54:16 UTC 2018 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)

run-level 3 Jun 28 11:53

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 ext4 542G 298G 217G 58% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS NEC U31 02/14/2018
Memory:
4x UNKNOWN NOT AVAILABLE
12x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666

(End of data from sysinfo program)
# SPEC CPU2017 Integer Rate Result

**NEC Corporation**

**Express5800/R120h-2E (Intel Xeon Gold 5122)**

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## Compiler Version Notes

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<th>Compiler</th>
<th>Version</th>
<th>Date</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>CC</td>
<td>500.perlbench_r(base)</td>
<td>502.gcc_r(base)</td>
<td>505.mcf_r(base)</td>
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<tr>
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<td>icc (ICC)</td>
<td>18.0.2</td>
<td>20180210</td>
</tr>
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<td>531.deepsjeng_r(base)</td>
</tr>
<tr>
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<tr>
<td></td>
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</tr>
<tr>
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<td></td>
<td></td>
</tr>
<tr>
<td></td>
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</tr>
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<td></td>
<td></td>
</tr>
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**NEC Corporation**

**Express5800/R120h-2E (Intel Xeon Gold 5122)**

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</tr>
</tbody>
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</thead>
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</tr>
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<td>NEC Corporation</td>
</tr>
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<td>Test Date</td>
<td>Jun-2018</td>
</tr>
<tr>
<td>Hardware Availability</td>
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</tr>
</tbody>
</table>

### Base Compiler Invocation

**C benchmarks:**

\[
\text{icc} \ -m64 \ -std=c11
\]

**C++ benchmarks:**

\[
\text{icpc} \ -m64
\]

**Fortran benchmarks:**

\[
\text{ifort} \ -m64
\]

### Base Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
- 502.gcc_r: -DSPEC_LP64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**

\[
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
\]

**C++ benchmarks:**

\[
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
\]

**Fortran benchmarks:**

\[
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc
\]
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Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64 -std=c11

502.gcc_r: icc -m32 -std=c11 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):
icpc -m64

523.xalancbmk_r: icpc -m32 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leepa_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: basepeak = yes

(Continued on next page)
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Peak Optimization Flags (Continued)

```
525.x264_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-alias -L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

541.leela_r: Same as 531.deepsjeng_r

Fortran benchmarks:

548.exchange2_r: basepeak = yes
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevB.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2018-06-27 22:58:49-0400.