**Huawei CH121 V5 (Intel Xeon Silver 4114)**

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei  
**Test Date:** Jun-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Jan-2018

### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>20</td>
<td>89.1</td>
<td>90.9</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>20</td>
<td>35.0</td>
<td>36.1</td>
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<tr>
<td>619.ibm_s</td>
<td>20</td>
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<td>44.5</td>
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<tr>
<td>621.wrf_s</td>
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<td>627.cam4_s</td>
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<td>54.8</td>
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<td>628.pop2_s</td>
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<td>53.4</td>
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<td>638.imagick_s</td>
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<tr>
<td>644.nab_s</td>
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<tr>
<td>649.fotonik3d_s</td>
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<td>71.5</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>20</td>
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</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Silver 4114  
- **Max MHz:** 3000  
- **Nominal:** 2200  
- **Enabled:** 20 cores, 2 chips  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 13.75 MB I+D on chip per core  
- **Other:** None  
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
- **Storage:** 1 x 1200 GB SAS, 10000 RPM  
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux Server release 7.4 (Maipo)  
  3.10.0-693.11.6.el7.x86_64  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
- **Parallel:** Yes  
- **Firmware:** Version 0.62 Released Mar-2018  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None
## SPEC CPU2017 Floating Point Speed Result

**Huawei**

**Huawei CH121 V5 (Intel Xeon Silver 4114)**

**SPECspeed2017_fp_base = 70.7**

**SPECspeed2017_fp_peak = 72.2**

**CPU2017 License:** 3175  
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### Results Table

<table>
<thead>
<tr>
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<th>Base Ratio</th>
<th>Base Seconds</th>
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<td>52.7</td>
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<td>644.nab_s</td>
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<td>211</td>
<td>74.6</td>
<td>210</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

- Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

- Environment variables set by runcpu before the start of the run:
  - KMP_AFFINITY = "granularity=fine,compact"
  - OMP_STACKSIZE = "192M"

- Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
- Transparent Huge Pages enabled by default
- Prior to runcpu invocation
- Filesystem page cache synced and cleared with:
  - sync; echo 3 > /proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

### Platform Notes

- BIOS configuration:
  - Power Policy Set to Load Balance
  - Hyper-Threading Set to Disable

(Continued on next page)
Huawei CH121 V5 (Intel Xeon Silver 4114)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_peak</th>
<th>72.2</th>
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<tbody>
<tr>
<td>SPECspeed2017_fp_base</td>
<td>70.7</td>
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CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

<table>
<thead>
<tr>
<th>Test Date</th>
<th>Jun-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Jul-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Jan-2018</td>
</tr>
</tbody>
</table>

Platform Notes (Continued)

XPT Prefetch Set to Enabled
Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618b091c0f
running on localhost.localdomain Sat Jun 30 14:33:59 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz
  2 "physical id"s (chips)
  20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 10
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                20
On-line CPU(s) list:   0-19
Thread(s) per core:    1
Core(s) per socket:    10
Socket(s):             2
NUMA node(s):          2
Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz
Stepping:              4
CPU MHz:               2201.000
CPU max MHz:           2201.0000
CPU min MHz:           800.0000
BogoMIPS:              4400.00
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              14080K
NUMA node0 CPU(s):     0-9
NUMA node1 CPU(s):     10-19
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov

(Continued on next page)
Huawei CH121 V5 (Intel Xeon Silver 4114)

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Platform Notes (Continued)

```
 pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
 lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
 aperfmperf eagerfpu pni pclmulqdq dtes64 msr tm cmov stp matri x87 svm fms xsave
 avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_13 cdp_13 invpcid_single intel_pt
 spec_ctrl ibpb_support tpr_shadow vnumi flexpriority ept vpid fsgsbase tsc_adjust
 bmi1 hle avx2 smep bmi2 erts invpcid rtm cqm mpx rd disables adx
 smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsaveopt xsaves opt xgetbv1 cqm_llc
 cqm_occup_llc cqm_mbm_total cqm_mbm_local dtm ida arat pln pts
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.

```
 available: 2 nodes (0-1)
 node 0 cpus: 0 1 2 3 4 5 6 7 8 9
 node 0 size: 194741 MB
 node 0 free: 189456 MB
 node 1 cpus: 10 11 12 13 14 15 16 17 18 19
 node 1 size: 196608 MB
 node 1 free: 191554 MB
 node distances:
 node 0 1
 0: 10 21
 1: 21 10
```

From /proc/meminfo

```
 MemTotal: 394174584 kB
 HugePages_Total: 0
 Hugepagesize: 2048 kB
```

From /etc/*release*/etc/*version*

```
 os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.4 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VARIANT="Server"
  VARIANT_ID="server"
  VERSION_ID="7.4"
  PRETTY_NAME="Red HatEnterprise Linux Server 7.4 (Maipo)"
 redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
 system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
 system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server
```

uname -a:

(Continued on next page)
Huawei

Huawei CH121 V5 (Intel Xeon Silver 4114)

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Platform Notes (Continued)

Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 30 07:13

SPEC is set to: /spec2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 700G 34G 666G 5% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.62 03/26/2018
Memory:
24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

----------------------------------------------------------------------
CC 619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)

ICC (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

----------------------------------------------------------------------
CC 619.lbm_s(peak)

ICC (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

----------------------------------------------------------------------
FC 607.cactuBSSN_s(base)

ICC (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

ICPC (ICPC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

(Continued on next page)
Huawei CH121 V5 (Intel Xeon Silver 4114)

SPECspeed2017_fp_base = 70.7
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CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Jun-2018
Tested by: Huawei
Hardware Availability: Jul-2017
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Compiler Version Notes (Continued)

==============================================================================
FC  607.cactusSSN_s(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC  603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC  603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC  621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC  621.wrf_s(peak) 628.pop2_s(peak)
------------------------------------------------------------------------------
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Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
## SPEC CPU2017 Floating Point Speed Result

### Huawei

**Huawei CH121 V5 (Intel Xeon Silver 4114)**

- **SPECspeed2017_fp_base =** 70.7
- **SPECspeed2017_fp_peak =** 72.2

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
** Tested by:** Huawei  
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### Base Compiler Invocation

C benchmarks:
- icc

Fortran benchmarks:
- ifort

Benchmarks using both Fortran and C:
- ifort icc

Benchmarks using Fortran, C, and C++:
- icpc icc ifort

### Base Portability Flags

- `603.bwaves_s`: `-DSPEC_LP64`  
- `607.cactuBSSN_s`: `-DSPEC_LP64`  
- `619.ibm_s`: `-DSPEC_LP64`  
- `621.wrf_s`: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`  
- `627.cam4_s`: `-DSPEC_LP64 -DSPEC_CASE_FLAG`  
- `628.pop2_s`: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byte/recl`  
- `638.imagick_s`: `-DSPEC_LP64`  
- `644.nab_s`: `-DSPEC_LP64`  
- `649.fotonik3d_s`: `-DSPEC_LP64`  
- `654.roms_s`: `-DSPEC_LP64`

### Base Optimization Flags

C benchmarks:
- `-CORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP`

Fortran benchmarks:
- `-DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp`  
- `nostandard-realloc-lhs -align array32byte`

Benchmarks using both Fortran and C:
- `-CORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP`  
- `nostandard-realloc-lhs -align array32byte`

(Continued on next page)
Huawei CH121 V5 (Intel Xeon Silver 4114)

Table:

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags
Huawei CH121 V5 (Intel Xeon Silver 4114)

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<th>SPECspeed2017_fp_base</th>
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<tbody>
<tr>
<td>70.7</td>
<td>72.2</td>
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</tbody>
</table>

**Peak Optimization Flags**

**C benchmarks:**

- 619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
- -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
- -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
- -DSPEC_OPENMP

- 638.imagick_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
- -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
- -DSPEC_OPENMP

- 644.nab_s: Same as 638.imagick_s

**Fortran benchmarks:**

- -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
- -DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3
- -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
- -nostandard-realloc-lhs -align array32byte

**Benchmarks using both Fortran and C:**

- 621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
- -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
- -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
- -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

- 627.cam4_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
- -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
- -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

- 628.pop2_s: Same as 621.wrf_s

**Peak Other Flags**

**C benchmarks:**

- -m64 -std=c11

(Continued on next page)
Huawei

Huawei CH121 V5 (Intel Xeon Silver 4114)

SPECspeed2017_fp_peak = 72.2
SPECspeed2017_fp_base = 70.7

CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Jun-2018
Tested by: Huawei
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Peak Other Flags (Continued)

Fortran benchmarks:
- m64

Benchmarks using both Fortran and C:
- m64 -std=c11

Benchmarks using Fortran, C, and C++:
- m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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