**Fujitsu**

**PRIMERGY TX2550 M4, Intel Xeon Silver 4114, 2.20GHz**

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<th>SPECrate2017_int_base</th>
<th>SPECrate2017_int_peak</th>
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<td>93.0</td>
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</tr>
<tr>
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<tr>
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<td>40</td>
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</table>

**Hardware**

- **CPU Name:** Intel Xeon Silver 4114
- **Max MHz.:** 3000
- **Nominal:** 2200
- **Enabled:** 20 cores, 2 chips, 2 threads/core
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 13.75 MB I+D on chip per chip
- **Other:** None
- **Memory:** 192 GB (12 x 16 GB 2Rx4 PC4-2666V-R, running at 2400)
- **Storage:** 96 GB tmpfs
- **Other:** 1 x SATA HDD, 1000 GB, 7200 RPM, used for swap

**Software**

- **OS:** SUSE Linux Enterprise Server 12 SP2 4.4.114-92.64-default
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
- **Parallel:** No
- **Firmware:** Fujitsu BIOS Version R1.22.0 for D3386-A1x released Jun-2018
- **File System:** tmpfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** jemalloc memory allocator library V5.0.1
SPEC CPU2017 Integer Rate Result

Fujitsu
PRIMERGY TX2550 M4, Intel Xeon Silver 4114, 2.20GHz

SPECrate2017_int_base = 93.0
SPECrate2017_int_peak = Not Run

Results Table

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<td>672</td>
<td>64.3</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Set Kernel Boot Parameter: nohz_full=1-39
Set CPU frequency governor to maximum performance with:
cpupower -c all frequency-set -g performance
Set tmpfs filesystem with:
mkdir /home/memory
mount -t tmpfs -o size=96g,rw tmpfs /home/memory
cpu idle state set with:
cpupower idle-set -d 1
Process tuning settings:
echo 0 > /proc/sys/kernel/numa_balancing

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/memory/speccpu/lib/ia32"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/memory/speccpu/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/memory/speccpu/je5.0.1-32"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/memory/speccpu/je5.0.1-64"

(Continued on next page)
SPEC CPU2017 Integer Rate Result
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PRIMERGTYX2550 M4, Intel Xeon Silver 4114, 2.20GHz

SPECrate2017_int_base = 93.0
SPECrate2017_int_peak = Not Run

General Notes (Continued)

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
    numactl --interleave=all runcpu <etc>

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
jemalloc: sources available via jemalloc.net

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
  DCU Streamer Prefetcher = Disabled
  Override OS Energy Performance = Enabled
  Energy Performance = Performance
  Utilization Profile = Unbalanced
  Package C State limit = C0
  Stale AtoS = Enabled
  IMC Interleaving = 2-way
  Sub NUMA Clustering = Disabled
  Fan Control = Full
Sysinfo program /home/memory/speccpu/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcb091c0f
running on TX2550M4 Thu Jun 28 12:59:50 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz
  2 "physical ids (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

(Continued on next page)
Fujitsu
PRIMERGY TX2550 M4, Intel Xeon Silver 4114, 2.20GHz

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Test Sponsor: Fujitsu
Tested by: Fujitsu

Platform Notes (Continued)

cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 40
On-line CPU(s) list: 0-39
Thread(s) per core: 2
Core(s) per socket: 10
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz
Stepping: 4
CPU MHz: 2653.809
CPU max MHz: 3000.0000
CPU min MHz: 800.0000
BogoMIPS: 4389.69
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 14080K
NUMA node0 CPU(s): 0-9,20-29
NUMA node1 CPU(s): 10-19,30-39
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmperf eagerfpdu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe ropcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
dtherm hwp hwp_act_window hwp_epp hwp_pkg_req intel_pt rsb_ctxs w spec_ctrl rtpoline
kaiser tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmis1 hle avx2 smep
bmi2 erm invpcid rtm cqm mpx avx512f avx512d avx512dq rdseed adx smap clflushopt clwb
avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.

(Continued on next page)
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Tested by: Fujitsu

Platform Notes ( Continued )

available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 20 21 22 23 24 25 26 27 28 29
node 0 size: 94874 MB
node 0 free: 85870 MB
node 1 cpus: 10 11 12 13 14 15 16 17 18 19 30 31 32 33 34 35 36 37 38 39
node 1 size: 96616 MB
node 1 free: 95644 MB
node distances:
node 0 1
0: 10 21
1: 21 10

From /proc/meminfo
MemTotal: 196087396 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux TX2550M4 4.4.114-92.64-default #1 SMP Thu Feb 1 19:18:19 UTC 2018 (c6ce5db)
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 28 08:13

SPEC is set to: /home/memory/speccpu
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 96G 8.9G 88G 10% /home/memory

Additional information from dmidecode follows. WARNING: Use caution when you interpret

(Continued on next page)
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<tr>
<td>Software Availability</td>
<td>Feb-2018</td>
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Platform Notes (Continued)
this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS FUJITSU // American Megatrends Inc. V5.0.0.12 R1.22.0 for D3386-A1x
06/04/2018
Memory:
12x Samsung M393A2G40EB2-CTD 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

```
==============================================================================
 CC  500.perlibench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base)
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)
==============================================================================
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
PC  548.exchange2_r(base)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort
SPEC CPU2017 Integer Rate Result

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Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Jun-2018
Hardware Availability: Nov-2017
Software Availability: Feb-2018

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

The flags files that were used to format this result can be browsed at
## SPEC CPU2017 Integer Rate Result

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**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test Date:** Jun-2018  
**Hardware Availability:** Nov-2017  
**Software Availability:** Feb-2018

You can also download the XML flags sources by saving the following links:


http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevE.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-06-28 12:59:49-0400.  