Supermicro
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Gold 6130)

**SPECrate2017_int_base = 77.2**

**SPECrate2017_int_peak = 82.1**

<table>
<thead>
<tr>
<th>Test Sponsor:</th>
<th>Supermicro</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tested by:</td>
<td>Supermicro</td>
</tr>
<tr>
<td>CPU2017 License:</td>
<td>001176</td>
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<tr>
<td>Test Date:</td>
<td>Jun-2018</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Jul-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Mar-2018</td>
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</table>

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name: Intel Xeon Gold 6130</td>
<td></td>
</tr>
<tr>
<td>Max MHz.: 3700</td>
<td></td>
</tr>
<tr>
<td>Nominal: 2100</td>
<td></td>
</tr>
<tr>
<td>Enabled: 16 cores, 1 chip, 2 threads/core</td>
<td></td>
</tr>
<tr>
<td>Orderable: 1 chip</td>
<td></td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
<td></td>
</tr>
<tr>
<td>L2: 1 MB I+D on chip per core</td>
<td></td>
</tr>
<tr>
<td>L3: 22 MB I+D on chip per chip</td>
<td></td>
</tr>
<tr>
<td>Other: None</td>
<td></td>
</tr>
<tr>
<td>Memory: 192 GB (6 x 32 GB 2Rx4 PC4-2666V-R)</td>
<td></td>
</tr>
<tr>
<td>Storage: 1 x 2 TB SATA III, 7200 RPM</td>
<td></td>
</tr>
<tr>
<td>Other: None</td>
<td></td>
</tr>
<tr>
<td>OS: SUSE Linux Enterprise Server 12 SP3</td>
<td></td>
</tr>
<tr>
<td>Compiler: C/C++: Version 18.0.2.199 of Intel C/C++</td>
<td></td>
</tr>
<tr>
<td>Compiler for Linux:</td>
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<tr>
<td>Fortran: Version 18.0.2.199 of Intel Fortran</td>
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<tr>
<td>Compiler for Linux:</td>
<td></td>
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<tr>
<td>Parallel: No</td>
<td></td>
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<tr>
<td>Firmware: Supermicro BIOS version 2.0b released Feb-2018</td>
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<tr>
<td>File System: xfs</td>
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</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
<td></td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
<td></td>
</tr>
<tr>
<td>Peak Pointers: 32/64-bit</td>
<td></td>
</tr>
<tr>
<td>Other: jemalloc memory allocator library V5.0.1</td>
<td></td>
</tr>
</tbody>
</table>
SPEC CPU2017 Integer Rate Result

Supermicro
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Gold 6130)

SPECrate2017_int_base = 77.2
SPECrate2017_int_peak = 82.1

Results Table

<table>
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<tr>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>32</td>
<td>828</td>
<td>61.5</td>
<td>831</td>
<td>61.3</td>
<td>830</td>
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<td>32</td>
<td>695</td>
<td>73.3</td>
<td>698</td>
<td>73.0</td>
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<td>72.9</td>
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<tr>
<td>502.gcc_r</td>
<td>32</td>
<td>690</td>
<td>65.7</td>
<td>694</td>
<td>65.3</td>
<td>698</td>
<td>65.0</td>
<td>32</td>
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<td>79.6</td>
<td>569</td>
<td>79.6</td>
<td>569</td>
<td>79.6</td>
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<tr>
<td>505.mcf_r</td>
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<td>550</td>
<td>94.0</td>
<td>565</td>
<td>91.5</td>
<td>566</td>
<td>91.3</td>
<td>32</td>
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<td>94.0</td>
<td>565</td>
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<td>91.3</td>
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<tr>
<td>520.omnetpp_r</td>
<td>32</td>
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<td>47.7</td>
<td>882</td>
<td>47.6</td>
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<td>47.7</td>
<td>880</td>
<td>47.7</td>
<td>882</td>
<td>47.6</td>
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<tr>
<td>523.xalancbmk_r</td>
<td>32</td>
<td>477</td>
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<td>478</td>
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<td>32</td>
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<td>384</td>
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<td>346</td>
<td>162</td>
<td>344</td>
<td>163</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
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<td>519</td>
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<td>528</td>
<td>69.4</td>
<td>530</td>
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<td>528</td>
<td>69.4</td>
<td>530</td>
<td>69.2</td>
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<tr>
<td>541.leela_r</td>
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<td>777</td>
<td>68.2</td>
<td>778</td>
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<td>69.7</td>
<td>762</td>
<td>69.6</td>
<td>765</td>
<td>69.3</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>32</td>
<td>543</td>
<td>153</td>
<td>547</td>
<td>153</td>
<td>548</td>
<td>153</td>
<td>32</td>
<td>548</td>
<td>153</td>
<td>547</td>
<td>153</td>
<td>548</td>
<td>153</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>32</td>
<td>620</td>
<td>55.8</td>
<td>669</td>
<td>51.7</td>
<td>669</td>
<td>51.7</td>
<td>32</td>
<td>620</td>
<td>55.8</td>
<td>669</td>
<td>51.7</td>
<td>669</td>
<td>51.7</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

jemalloc: jemalloc, a general purpose malloc implementation;
jemalloc: built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5;

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
SPEC CPU2017 Integer Rate Result

Supermicro
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Gold 6130)

SPECrate2017_int_base = 77.2
SPECrate2017_int_peak = 82.1

CPU2017 License: 001176
Test Sponsor: Supermicro
Test Date: Jun-2018
Hardware Availability: Jul-2017
Tested by: Supermicro
Software Availability: Mar-2018

General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
LLC prefetch = Enable
Power Technology = Custom
Power Performance Tuning = BIOS Controls EPB
ENERGY_PERF_BIAS_CFG mode = Maximum Performance
Hardware P-state = Out of Band Mode
SNC = Enable
XPT Prefetch = Enable
Stale AtoS = Enable
LLC dead line alloc = Disable
IMC Interleaving = 1-way Interleave
SDDC Plus One = Disable
ADDDC Sparing = Disable
Patrol Scrub = Disable
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-9m9c Fri Jun 29 10:06:34 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
  1 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31

(Continued on next page)
## SPEC CPU2017 Integer Rate Result

**Supermicro**

SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Gold 6130)

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
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<tbody>
<tr>
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**CPU2017 License:** 001176

**Test Date:** Jun-2018

**Test Sponsor:** Supermicro

**Hardware Availability:** Jul-2017

**Tested by:** Supermicro

**Software Availability:** Mar-2018

---

**Platform Notes (Continued)**

- Thread(s) per core: 2
- Core(s) per socket: 16
- Socket(s): 1
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
- Stepping: 4
- CPU MHz: 2099.988
- BogoMIPS: 4199.97
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 22528K
- NUMA node0 CPU(s): 0-3,8-11,16-19,24-27
- NUMA node1 CPU(s): 4-7,12-15,20-23,28-31
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pni pt dtherm hwp_epp intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow vmmi flexpriority ept fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 3dnowprefetch ida arat epb invpcid_single pni pti msr_adjust cpb pmull cbrx mepmclock cmov stibp cpsee

/proc/cpuinfo cache data
- cache size: 22528 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

- available: 2 nodes (0-1)
- node 0 cpus: 0 1 2 3 4 8 9 10 11 16 17 18 19 24 25 26 27
- node 0 size: 95256 MB
- node 0 free: 94884 MB
- node 1 cpus: 4 5 6 7 12 13 14 15 20 21 22 23 28 29 30 31
- node 1 size: 96626 MB
- node 1 free: 96227 MB
- node distances:
  - node 0: 1
  - 0: 10 11
  - 1: 11 10

From /proc/meminfo

---

(Continued on next page)
**SPEC CPU2017 Integer Rate Result**

**Supermicro**
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Gold 6130)

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>001176</th>
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<tbody>
<tr>
<td>Test Sponsor</td>
<td>Supermicro</td>
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<tr>
<td>Tested by</td>
<td>Supermicro</td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base** = 77.2
**SPECrate2017_int_peak** = 82.1

---

**Platform Notes (Continued)**

```markdown
MemTotal: 196489056 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP3

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 3
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP3"
  VERSION_ID="12.3"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
  Linux linux-9m9c 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux

runc-level 3 Jun 29 10:05

SPEC is set to: /home/cpu2017

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda4</td>
<td>xfs</td>
<td>1.8T</td>
<td>45G</td>
<td>1.8T</td>
<td>3%</td>
<td>/home</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- BIOS American Megatrends Inc. 2.0b 02/26/2018
- Memory:
  - 2x NO DIMM NO DIMM
  - 6x Samsung M393A4K40CB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)
Supermicro
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Gold 6130)

SPEC CPU2017 Integer Rate Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

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CPU2017 License: 001176
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Tested by: Supermicro
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Compiler Version Notes

==============================================================================
 CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
      557.xz_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 CC  500.perlbench_r(peak) 502.gcc_r(peak) 505.mcf_r(peak) 525.x264_r(peak)
      557.xz_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
      541.leela_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
      541.leela_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 FC  548.exchange2_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 FC  548.exchange2_r(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
### Base Compiler Invocation

- **C benchmarks:**
  - `icc -m64 -std=c11`

- **C++ benchmarks:**
  - `icpc -m64`

- **Fortran benchmarks:**
  - `ifort -m64`

### Base Portability Flags

- **C benchmarks:**
  - `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

- **C++ benchmarks:**
  - `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

- **Fortran benchmarks:**
  - `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc`

### Base Optimization Flags

- **C benchmarks:**
  - `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

- **C++ benchmarks:**
  - `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

- **Fortran benchmarks:**
  - `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc`
Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64 -std=c11
```

```
502.gcc_r: icc -m32 -std=c11 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
523.xalancbmk_r: icpc -m32 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin
```

Fortran benchmarks:

```
ifort -m64
```

Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc
```

```
502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc
```

```
505.mcf_r: basepeak = yes
```

(Continued on next page)
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CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Peak Optimization Flags (Continued)

525.x264_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-alias -L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: basepeak = yes

541.leela_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revD.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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