## SPEC® CPU2017 Integer Speed Result

**Huawei**

**Huawei 1288H V5 (Intel Xeon Gold 6130T)**

<table>
<thead>
<tr>
<th>Thread</th>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>Huawei</td>
<td>8.89</td>
<td>9.16</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 3175

**Test Sponsor:** Huawei

**Test Date:** Jul-2018

**Hardware Availability:** Jul-2017

**Tested by:** Huawei

**Software Availability:** Feb-2018

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**Threads**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>6.21</td>
<td>7.41</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>9.69</td>
<td>10.2</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>9.57</td>
<td>11.5</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>6.15</td>
<td>11.5</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>9.57</td>
<td>10.2</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>5.19</td>
<td>13.4</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>4.31</td>
<td>13.4</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>21.9</td>
<td>22.6</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>32</td>
<td>32</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>32</td>
<td>32</td>
</tr>
</tbody>
</table>

---

**Hardware**

<table>
<thead>
<tr>
<th>Feature</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Gold 6130T</td>
</tr>
<tr>
<td>Max MHz.</td>
<td>3700</td>
</tr>
<tr>
<td>Nominal</td>
<td>2100</td>
</tr>
<tr>
<td>Enabled</td>
<td>32 cores, 2 chips</td>
</tr>
<tr>
<td>Orderable</td>
<td>1,2 chips</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3</td>
<td>22 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x 1200 GB SAS, 10000 RPM</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

**Software**

<table>
<thead>
<tr>
<th>Feature</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS</td>
<td>SUSE Linux Enterprise Server 12 SP2 (x86_64) 4.4.114-92.64-default</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Parallel</td>
<td>Yes</td>
</tr>
<tr>
<td>Firmware</td>
<td>Version 0.62 Released Mar-2018</td>
</tr>
<tr>
<td>File System</td>
<td>xfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>jemalloc: jemalloc memory allocator library V5.0.1</td>
</tr>
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</table>
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>32</td>
<td>286</td>
<td>6.21</td>
<td>288</td>
<td>6.15</td>
<td>284</td>
<td>6.24</td>
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<tr>
<td>602.gcc_s</td>
<td>32</td>
<td>409</td>
<td>9.74</td>
<td>416</td>
<td>9.56</td>
<td><strong>411</strong></td>
<td><strong>9.69</strong></td>
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<tr>
<td>605.mcf_s</td>
<td>32</td>
<td>408</td>
<td>11.6</td>
<td><strong>409</strong></td>
<td><strong>11.5</strong></td>
<td>414</td>
<td>11.4</td>
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<tr>
<td>620.omnetpp_s</td>
<td>32</td>
<td>258</td>
<td>6.33</td>
<td><strong>265</strong></td>
<td><strong>6.15</strong></td>
<td>274</td>
<td>5.96</td>
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<tr>
<td>623.xalanghk_s</td>
<td>32</td>
<td><strong>148</strong></td>
<td><strong>9.57</strong></td>
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<td>9.59</td>
<td>149</td>
<td>9.50</td>
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<tr>
<td>625.x264_s</td>
<td>32</td>
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<td>11.5</td>
<td>153</td>
<td><strong>11.5</strong></td>
<td>154</td>
<td>11.5</td>
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</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>32</td>
<td>276</td>
<td>5.19</td>
<td><strong>276</strong></td>
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<td>277</td>
<td>5.18</td>
<td></td>
<td></td>
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<td>641.leela_s</td>
<td>32</td>
<td><strong>396</strong></td>
<td><strong>4.31</strong></td>
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<td>4.31</td>
<td>396</td>
<td>4.30</td>
<td></td>
<td></td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>32</td>
<td><strong>219</strong></td>
<td><strong>13.4</strong></td>
<td>219</td>
<td>13.4</td>
<td>218</td>
<td>13.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>657.xz_s</td>
<td>32</td>
<td>283</td>
<td>21.9</td>
<td>283</td>
<td><strong>21.9</strong></td>
<td>283</td>
<td><strong>21.9</strong></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,scatter"
- OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
    sync; echo 3> /proc/sys/vm/drop_caches
```

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;

jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;


Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Huawei

Huawei 1288H V5 (Intel Xeon Gold 6130T)

| SPECspeed2017_int_base = 8.89 |
| SPECspeed2017_int_peak = 9.16 |

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Platform Notes

BIOS configuration:
Power Policy Set to Load Balance
Hyper-Threading Set to Disable
XPT Prefetch Set to Enabled

Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-2gz1 Mon Jul 9 12:57:18 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6130T CPU @ 2.10GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6130T CPU @ 2.10GHz
Stepping: 4
CPU MHz: 1000.000
CPU max MHz: 2101.0000
CPU min MHz: 1000.0000
BogoMIPS: 4200.03
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 22528K

(Continued on next page)
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**CPU2017 License:** 3175  
**Test Date:** Jul-2018  
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**Hardware Availability:** Jul-2017  
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**Software Availability:** Feb-2018

#### Platform Notes (Continued)

<table>
<thead>
<tr>
<th>Metric</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>NUMA node0 CPU(s):</td>
<td>0-15</td>
</tr>
<tr>
<td>NUMA node1 CPU(s):</td>
<td>16-31</td>
</tr>
<tr>
<td>Flags:</td>
<td>fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp lm constant_tsc art arch_perfmon pebs bts rep_good no pcl Mueller xtopology nonstop_tsc aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtr pdcid pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat eplt evpm crude single pln pts dtherm intel_pt rsb cts xw spec_ctrl retpoline kaiser tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmlh hle avx2 smep bmi2 3ms incv ctm qmx avx512f avx512dq rdseed adx clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup llc</td>
</tr>
</tbody>
</table>

/proc/cpuinfo cache data  
```
    cache size : 22528 KB
```

From numactl --hardware  
```
    WARNING: a numactl 'node' might or might not correspond to a physical chip.
    available: 2 nodes (0-1)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
    node 0 size: 191528 MB
    node 0 free: 185414 MB
    node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
    node 1 size: 193382 MB
    node 1 free: 191578 MB
    node distances:
    node 0 1
    0: 10 21
    1: 21 10
```

From /proc/meminfo  
```
    MemTotal: 394149468 kB
    HugePages_Total: 0
    Hugepagesize: 2048 kB
```

From /etc/*release*/etc/*version*  
```
    SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 2
        # This file is deprecated and will be removed in a future service pack or release.
        # Please check /etc/os-release for details about this release.
    os-release:
        NAME="SLES"
        VERSION="12-SP2"
        VERSION_ID="12.2"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
```

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SPEC CPU2017 Integer Speed Result

Huawei

Huawei 1288H V5 (Intel Xeon Gold 6130T)

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</tbody>
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CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Platform Notes (Continued)

ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux linux-2gz1 4.4.114-92.64-default #1 SMP Thu Feb 1 19:18:19 UTC 2018 (c6ce5db)
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 9 07:55
SPEC is set to: /spec2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 269G 30G 240G 11% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.62 03/26/2018
Memory:
24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
CC  600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
CXXC 620.omnetpp_s(base) 623.xalanchbmk_s(base) 631.deepsjeng_s(base) 641.leela_s(base)
==============================================================================
icpc (ICC) 18.0.0 20170811

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Huawei

Huawei 1288H V5 (Intel Xeon Gold 6130T)

SPECspeed2017_int_base = 8.89
SPECspeed2017_int_peak = 9.16

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Jul-2018
Hardware Availability: Jul-2017
Software Availability: Feb-2018

Compiler Version Notes (Continued)

CXXC 620.omnetpp_s(peak) 623.xalancbmk_s(peak) 631.deepsjeng_s(peak)
641.leela_s(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 648.exchange2_s(base, peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
Huawei
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**Base Optimization Flags**

C benchmarks:
- -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
- -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
- -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
- -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
- -L/usr/local/je5.0.1-64/lib -ljemalloc

**Base Other Flags**

C benchmarks:
- -m64 -std=c11

C++ benchmarks:
- -m64

Fortran benchmarks:
- -m64

**Peak Compiler Invocation**

C benchmarks:
- icc

C++ benchmarks:
- icpc

Fortran benchmarks:
- ifort

**Peak Portability Flags**

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64

(Continued on next page)
Huawei

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<thead>
<tr>
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Peak Portability Flags (Continued)

602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2  
-xCORE-AVX2 -qopt-mem-layout-trans=3 -ipo -O3  
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP -fno-strict-overflow  
-1/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2  
-xCORE-AVX2 -qopt-mem-layout-trans=3 -ipo -O3  
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP  
-1/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP  
-1/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: Same as 602.gcc_s

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: -L/opt/intel/compiler_and_libraries_2018/linux/lib/ia32  
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP

(Continued on next page)
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<tr>
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</table>

### Peak Optimization Flags (Continued)

- **623.xalancbmk_s (continued):**
  - `-L/usr/local/je5.0.1-32/lib -ljemalloc`

- **631.deepsjeng_s:** `basepeak = yes`

- **641.leela_s:** `basepeak = yes`

**Fortran benchmarks:**

- `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte -L/usr/local/je5.0.1-64/lib -ljemalloc`

### Peak Other Flags

**C benchmarks:**

- `-m64 -std=c11`

**C++ benchmarks (except as noted below):**

- `-m64`

- **623.xalancbmk_s:** `-m32`

**Fortran benchmarks:**

- `-m64`

The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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