Huawei CH242 V5 (Intel Xeon Gold 5118)

**SPECspeed2017_fp_base = 119**

**SPECspeed2017_fp_peak = 121**

**Threads**

<table>
<thead>
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<th>Benchmark</th>
<th>Results</th>
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<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>48</td>
<td>679</td>
<td>679</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>48</td>
<td>149</td>
<td>144</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>48</td>
<td>149</td>
<td>149</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>48</td>
<td>66.1</td>
<td>66.1</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>48</td>
<td>66.7</td>
<td>66.7</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>48</td>
<td>90.2</td>
<td>90.5</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>48</td>
<td>46.8</td>
<td>46.8</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>48</td>
<td>114</td>
<td>114</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>48</td>
<td>207</td>
<td>208</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>48</td>
<td>102</td>
<td>102</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>48</td>
<td>142</td>
<td>143</td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Gold 5118
- **Max MHz.:** 3200
- **Nominal:** 2300
- **Enabled:** 48 cores, 4 chips
- **Orderable:** 2,4 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **Cache L2:** 1 MB I+D on chip per core
- **Cache L3:** 16.5 MB I+D on chip per chip
- **Memory:** 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)
- **Storage:** 1 x 1200 GB SAS, 10000 RPM
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux Server release 7.3 (Maipo)
  - 3.10.0-693.11.6.el7.x86_64
- **Compiler:**
  - C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux:
  - Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
- **Parallel:** Yes
- **Firmware:** Version 0.84 Released Mar-2018
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** None
SPEC CPU2017 Floating Point Speed Result

Huawei CH242 V5 (Intel Xeon Gold 5118)

SPECspeed2017_fp_base = 119
SPECspeed2017_fp_peak = 121

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>48</td>
<td>87.2</td>
<td>677</td>
<td>86.5</td>
<td>682</td>
<td>87.1</td>
<td>678</td>
<td>48</td>
<td>87.8</td>
<td>672</td>
<td>86.9</td>
<td>679</td>
<td>86.2</td>
<td>684</td>
</tr>
<tr>
<td>607.cactubssn_s</td>
<td>48</td>
<td>117</td>
<td>143</td>
<td>116</td>
<td>144</td>
<td>116</td>
<td>144</td>
<td>48</td>
<td>113</td>
<td>147</td>
<td>111</td>
<td>150</td>
<td>112</td>
<td>149</td>
</tr>
<tr>
<td>619.ibm_s</td>
<td>48</td>
<td>79.3</td>
<td>66.0</td>
<td>79.2</td>
<td>66.1</td>
<td>79.3</td>
<td>66.1</td>
<td>48</td>
<td>79.3</td>
<td>66.0</td>
<td>79.2</td>
<td>66.1</td>
<td>79.3</td>
<td>66.1</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>48</td>
<td>225</td>
<td>58.8</td>
<td>216</td>
<td>61.1</td>
<td>216</td>
<td>61.3</td>
<td>48</td>
<td>211</td>
<td>62.7</td>
<td>196</td>
<td>67.5</td>
<td>198</td>
<td>66.7</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>48</td>
<td>97.2</td>
<td>91.2</td>
<td>98.3</td>
<td>90.2</td>
<td>98.9</td>
<td>89.7</td>
<td>48</td>
<td>98.1</td>
<td>90.4</td>
<td>97.3</td>
<td>91.1</td>
<td>98.0</td>
<td>90.5</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>48</td>
<td>251</td>
<td>47.2</td>
<td>257</td>
<td>46.2</td>
<td>253</td>
<td>46.8</td>
<td>48</td>
<td>251</td>
<td>47.2</td>
<td>257</td>
<td>46.2</td>
<td>253</td>
<td>46.8</td>
</tr>
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<td>48</td>
<td>126</td>
<td>114</td>
<td>129</td>
<td>112</td>
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<td>114</td>
<td>48</td>
<td>126</td>
<td>114</td>
<td>126</td>
<td>114</td>
<td>126</td>
<td>115</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>48</td>
<td>84.1</td>
<td>208</td>
<td>84.6</td>
<td>207</td>
<td>84.2</td>
<td>207</td>
<td>48</td>
<td>84.5</td>
<td>207</td>
<td>84.0</td>
<td>208</td>
<td>84.1</td>
<td>208</td>
</tr>
<tr>
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<td>48</td>
<td>89.2</td>
<td>102</td>
<td>88.8</td>
<td>103</td>
<td>89.1</td>
<td>102</td>
<td>48</td>
<td>89.2</td>
<td>102</td>
<td>88.8</td>
<td>103</td>
<td>89.1</td>
<td>102</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>48</td>
<td>107</td>
<td>148</td>
<td>111</td>
<td>142</td>
<td>115</td>
<td>136</td>
<td>48</td>
<td>112</td>
<td>140</td>
<td>107</td>
<td>147</td>
<td>110</td>
<td>143</td>
</tr>
</tbody>
</table>

SPECspeed2017_fp_base = 119
SPECspeed2017_fp_peak = 121

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/spec/lib/ia32:/spec/lib/intel64:/spec/je5.0.1-32:/spec/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Power Policy Set to Load Balance
Hyper-Threading Set to Disable

(Continued on next page)
Huawei CH242 V5 (Intel Xeon Gold 5118)

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</table>

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Platform Notes (Continued)

XPT Prefetch Set to Enabled
Sysinfo program /spec/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f
running on localhost.localdomain Tue Jul 10 11:20:15 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5118 CPU @ 2.30GHz
  4 "physical id"s (chips)
  48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 12
  siblings : 12
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 1
Core(s) per socket: 12
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5118 CPU @ 2.30GHz
Stepping: 4
CPU MHz: 2301.000
BogoMIPS: 4606.30
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 16896K
NUMA node0 CPU(s): 0-11
NUMA node1 CPU(s): 12-23
NUMA node2 CPU(s): 24-35

(Continued on next page)
Huawei

Huawei CH242 V5 (Intel Xeon Gold 5118)

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CPU2017 License: 3175
Test Sponsor: Huawei
Hardware Availability: Jul-2017
Test Date: Jul-2018
Tested by: Huawei
Software Availability: Jan-2018

NUMA node3 CPU(s): 36-47

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.

/proc/cpuinfo cache data
  cache size: 16896 KB

From /proc/meminfo
  MemTotal: 1583346560 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.3 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.3"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
    ANSI_COLOR=\"0;31\"
    CPE_NAME=cpe:/o:redhat:enterprise_linux:7.3:GA:server
    redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
    system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

uname -a:

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

Huawei

Huawei CH242 V5 (Intel Xeon Gold 5118)

SPECspeed2017_fp_base = 119
SPECspeed2017_fp_peak = 121

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Jul-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Platform Notes (Continued)

Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 10 05:14

SPEC is set to: /spec
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 400G 24G 377G 6% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.84 03/26/2018
Memory:
40x Hynix HMA84GR7AFR4N-VK 32 GB 2 rank 2666, configured at 2400
8x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC   619.lbm_s(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC  607.cactuBSSN_s(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

(Continued on next page)
# SPEC CPU2017 Floating Point Speed Result

## Huawei

### Huawei CH242 V5 (Intel Xeon Gold 5118)

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<td>121</td>
</tr>
</tbody>
</table>

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<th>CPU2017 License:</th>
<th>3175</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Huawei</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Huawei</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Jul-2018</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Jul-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Jan-2018</td>
</tr>
</tbody>
</table>

## Compiler Version Notes (Continued)

---

**FC  607.cactuBSSN_s(peak)**

---

**icpc (ICC) 18.0.0 20170811**

*Copyright (C) 1985-2017 Intel Corporation. All rights reserved.*

**icc (ICC) 18.0.0 20170811**

*Copyright (C) 1985-2017 Intel Corporation. All rights reserved.*

**ifort (IFORT) 18.0.0 20170811**

*Copyright (C) 1985-2017 Intel Corporation. All rights reserved.*

---

**FC  603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)**

---

**ifort (IFORT) 18.0.0 20170811**

*Copyright (C) 1985-2017 Intel Corporation. All rights reserved.*

---

**FC  603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)**

---

**ifort (IFORT) 18.0.0 20170811**

*Copyright (C) 1985-2017 Intel Corporation. All rights reserved.*

---

**CC  621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)**

---

**ifort (IFORT) 18.0.0 20170811**

*Copyright (C) 1985-2017 Intel Corporation. All rights reserved.*

**icc (ICC) 18.0.0 20170811**

*Copyright (C) 1985-2017 Intel Corporation. All rights reserved.*

---

**CC  621.wrf_s(peak) 628.pop2_s(peak)**

---

**ifort (IFORT) 18.0.0 20170811**

*Copyright (C) 1985-2017 Intel Corporation. All rights reserved.*

**icc (ICC) 18.0.0 20170811**

*Copyright (C) 1985-2017 Intel Corporation. All rights reserved.*
## SPEC CPU2017 Floating Point Speed Result

**Huawei**

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<th>Huawei CH242 V5 (Intel Xeon Gold 5118)</th>
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<th>SPECspeed2017_fp_peak = 121</th>
</tr>
</thead>
</table>

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei

---

### Base Compiler Invocation

C benchmarks:  
```
icc
```

Fortran benchmarks:  
```
ifort
```

Benchmarks using both Fortran and C:  
```
ifort icc
```

Benchmarks using Fortran, C, and C++:  
```
icpc icc ifort
```

---

### Base Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td><code>-DSPEC_LP64</code></td>
</tr>
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<td><code>-DSPEC_LP64</code></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td><code>-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian</code></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td><code>-DSPEC_LP64 -DSPEC_CASE_FLAG</code></td>
</tr>
<tr>
<td>628.pop2_s</td>
<td><code>-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl</code></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td><code>-DSPEC_LP64</code></td>
</tr>
<tr>
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</tr>
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</tr>
<tr>
<td>654.roms_s</td>
<td><code>-DSPEC_LP64</code></td>
</tr>
</tbody>
</table>

---

### Base Optimization Flags

C benchmarks:  
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
```

Fortran benchmarks:  
```
-DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -nostandard-realloc-lhs -align array32byte
```

Benchmarks using both Fortran and C:  
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte
```

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Huawei

Huawei CH242 V5 (Intel Xeon Gold 5118)

SPECspeed2017_fp_base = 119
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</thead>
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</tr>
<tr>
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</tr>
</tbody>
</table>

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags
# SPEC CPU2017 Floating Point Speed Result

## Huawei

**Huawei CH242 V5 (Intel Xeon Gold 5118)**

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- **CPU2017 License:** 3175
- **Test Sponsor:** Huawei
- **Tested by:** Huawei
- **Test Date:** Jul-2018
- **Hardware Availability:** Jul-2017
- **Software Availability:** Jan-2018

### Peak Optimization Flags

#### C benchmarks:

- `619.lbm_s`: basepeak = yes
- `638.imagick_s`: `-xCORE-AVX2` `-ipo` `-O3` `-no-prec-div` `-qopt-prefetch` `-ffinite-math-only` `-qopt-mem-layout-trans=3` `-qopenmp` `-DSPEC_OPENMP`
- `644.nab_s`: Same as `638.imagick_s`

#### Fortran benchmarks:

- `603.bwaves_s`: `-prof-gen(pass 1)` `-prof-use(pass 2)` `-DSPEC_SUPPRESS_OPENMP` `-DSPEC_OPENMP` `-O2` `-xCORE-AVX2` `-qopt-prefetch` `-ipo` `-O3` `-ffinite-math-only` `-no-prec-div` `-qopt-mem-layout-trans=3` `-qopenmp` `-nostandard-realloc-lhs` `-align array32byte`
- `649.fotonik3d_s`: basepeak = yes
- `654.roms_s`: Same as `603.bwaves_s`

#### Benchmarks using both Fortran and C:

- `621.wrf_s`: `-prof-gen(pass 1)` `-prof-use(pass 2)` `-O2` `-xCORE-AVX2` `-qopt-prefetch` `-ipo` `-O3` `-ffinite-math-only` `-no-prec-div` `-qopt-mem-layout-trans=3` `-DSPEC_SUPPRESS_OPENMP` `-qopenmp` `-DSPEC_OPENMP` `-nostandard-realloc-lhs` `-align array32byte`
- `627.cam4_s`: `-xCORE-AVX2` `-ipo` `-O3` `-no-prec-div` `-qopt-prefetch` `-ffinite-math-only` `-qopt-mem-layout-trans=3` `-qopenmp` `-DSPEC_OPENMP` `-nostandard-realloc-lhs` `-align array32byte`
- `628.pop2_s`: basepeak = yes

#### Benchmarks using Fortran, C, and C++:

- `-prof-gen(pass 1)` `-prof-use(pass 2)` `-O2` `-xCORE-AVX2` `-qopt-prefetch` `-ipo` `-O3` `-ffinite-math-only` `-no-prec-div` `-qopt-mem-layout-trans=3` `-DSPEC_SUPPRESS_OPENMP` `-qopenmp` `-DSPEC_OPENMP` `-nostandard-realloc-lhs` `-align array32byte`

### Peak Other Flags

#### C benchmarks:

- `-m64` `-std=c11`
Huawei CH242 V5 (Intel Xeon Gold 5118)

SPECspeed2017_fp_base = 119
SPECspeed2017_fp_peak = 121

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei
Test Date: Jul-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Peak Other Flags (Continued)

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml

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