Huawei

Huawei 1288H V5 (Intel Xeon Silver 4112)

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<tbody>
<tr>
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CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

<table>
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</tr>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>48.2</td>
<td>49.9</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>28.6</td>
<td>29.9</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>28.6</td>
<td>33.3</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>37.2</td>
<td></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>20.6</td>
<td></td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>34.3</td>
<td>37.1</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>25.8</td>
<td>25.9</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>45.0</td>
<td>45.0</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>50.5</td>
<td></td>
</tr>
<tr>
<td>654.roms_s</td>
<td>40.7</td>
<td>43.2</td>
</tr>
</tbody>
</table>

Hardware

CPU Name: Intel Xeon Silver 4112
Max MHz.: 3000
Nominal: 2600
Enabled: 8 cores, 2 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 8.25 MB I+D on chip per chip
Other: None
Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)
Storage: 1 x 1200 GB SAS, 10000 RPM
Other: None

Software

OS: Red Hat Enterprise Linux Server release 7.4 (Maipo)
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;
Compiler for Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
Parallel: Yes
Firmware: Version 0.62 Released Mar-2018
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
SPEC CPU2017 Floating Point Speed Result

Huawei
Huawei 1288H V5 (Intel Xeon Silver 4112)

SPECspeed2017_fp_base = 42.2
SPECspeed2017_fp_peak = 43.4

CPU2017 License: 3175
Test Sponsor: Huawei
Hardware Availability: Jul-2017
Tested by: Huawei
Software Availability: Jan-2018

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>8</td>
<td>255</td>
<td>232</td>
<td>255</td>
<td>231</td>
<td>255</td>
<td>232</td>
<td>8</td>
<td>255</td>
<td>232</td>
<td>255</td>
<td>232</td>
<td>255</td>
<td>232</td>
<td></td>
<td></td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>8</td>
<td>346</td>
<td>48.2</td>
<td>345</td>
<td>48.3</td>
<td>346</td>
<td>48.2</td>
<td>8</td>
<td>334</td>
<td>49.9</td>
<td>334</td>
<td>49.9</td>
<td>334</td>
<td>49.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>619.ibm_s</td>
<td>8</td>
<td>183</td>
<td>28.6</td>
<td>184</td>
<td>28.5</td>
<td>183</td>
<td>28.6</td>
<td>8</td>
<td>183</td>
<td>28.7</td>
<td>183</td>
<td>28.6</td>
<td>184</td>
<td>28.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>8</td>
<td>398</td>
<td>33.2</td>
<td>397</td>
<td>33.3</td>
<td>394</td>
<td>33.5</td>
<td>8</td>
<td>355</td>
<td>37.2</td>
<td>356</td>
<td>37.1</td>
<td>355</td>
<td>37.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>8</td>
<td>430</td>
<td>20.6</td>
<td>430</td>
<td>20.6</td>
<td>432</td>
<td>20.5</td>
<td>8</td>
<td>430</td>
<td>20.6</td>
<td>430</td>
<td>20.6</td>
<td>432</td>
<td>20.5</td>
<td></td>
<td></td>
</tr>
<tr>
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<td>8</td>
<td>346</td>
<td>34.3</td>
<td>344</td>
<td>34.5</td>
<td>350</td>
<td>34.0</td>
<td>8</td>
<td>321</td>
<td>37.0</td>
<td>319</td>
<td>37.2</td>
<td>320</td>
<td>37.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>8</td>
<td>558</td>
<td>25.9</td>
<td>559</td>
<td>25.8</td>
<td>559</td>
<td>25.8</td>
<td>8</td>
<td>556</td>
<td>25.9</td>
<td>558</td>
<td>25.9</td>
<td>560</td>
<td>25.8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>644.nab_s</td>
<td>8</td>
<td>388</td>
<td>45.0</td>
<td>388</td>
<td>45.0</td>
<td>388</td>
<td>45.1</td>
<td>8</td>
<td>388</td>
<td>45.0</td>
<td>388</td>
<td>45.0</td>
<td>388</td>
<td>45.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>8</td>
<td>181</td>
<td>50.4</td>
<td>180</td>
<td>50.5</td>
<td>179</td>
<td>50.8</td>
<td>8</td>
<td>181</td>
<td>50.4</td>
<td>180</td>
<td>50.5</td>
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<td></td>
<td></td>
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<tr>
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<td>8</td>
<td>386</td>
<td>40.8</td>
<td>386</td>
<td>40.7</td>
<td>387</td>
<td>40.7</td>
<td>8</td>
<td>365</td>
<td>43.2</td>
<td>365</td>
<td>43.1</td>
<td>364</td>
<td>43.2</td>
<td></td>
<td></td>
</tr>
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</table>

SPECspeed2017_fp_base = 42.2
SPECspeed2017_fp_peak = 43.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Power Policy Set to Load Balance
Hyper-Threading Set to Disable

(Continued on next page)
Huawei

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CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Jul-2018

Hardware Availability: Jul-2017
Software Availability: Jan-2018

Platform Notes (Continued)

XPT Prefetch Set to Enabled
Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on localhost.localdomain Sat Jul 14 01:12:29 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
  2 "physical id"s (chips)
  8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 4
physical 0: cores 0 1 4 5
physical 1: cores 0 2 3 4

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 8
On-line CPU(s) list: 0-7
Thread(s) per core: 1
Core(s) per socket: 4
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
Stepping: 4
CPU MHz: 2601.000
CPU max MHz: 2601.0000
CPU min MHz: 800.0000
BogoMIPS: 5200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 8448K
NUMA node0 CPU(s): 0-3
NUMA node1 CPU(s): 4-7
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov

(Continued on next page)
Huawei

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**Test Date:** Jul-2018  
**Test Sponsor:** Huawei  
**Hardware Availability:** Jul-2017  
**Tested by:** Huawei  
**Software Availability:** Jan-2018

**Platform Notes (Continued)**

pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 dts cpl vmx smx est tm2 ssse3 fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat l3 cdp cpg_1 invpcid_single intel_pt spec_ctrl ibpb_support tpr_shadow vni flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts

/proc/cpuinfo cache data  

cache size : 8448 KB

From numactl --hardware  

WARNING: a numactl 'node' might or might not correspond to a physical chip.  

available: 2 nodes (0-1)  
node 0 cpus: 0 1 2 3  
node 0 size: 194741 MB  
node 0 free: 189006 MB  
node 1 cpus: 4 5 6 7  
node 1 size: 196608 MB  
node 1 free: 190843 MB  
node distances:  
node 0 1  
0: 10  21  
1: 21  10

From /proc/meminfo  

MemTotal: 394174584 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*  

os-release:  
NAME="Red Hat Enterprise Linux Server"  
VERSION="7.4 (Maipo)"  
ID="rhel"  
ID_LIKE="fedora"  
VARIANT="Server"  
VARIANT_ID="server"  
VERSION_ID="7.4"  
PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"  
redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)  
system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)  
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:

(Continued on next page)
**Huawei**

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**Software Availability:** Jan-2018

**Platform Notes (Continued)**

Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux  
run-level 3 Jul 13 05:39  
SPEC is set to: /spec2017  
Filesystem  Type  Size  Used Avail Use% Mounted on  
/dev/sda4  xfs  700G  35G  666G  5%  /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.62 03/26/2018  
Memory:  
24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

**Compiler Version Notes**

==============================================================================  
CC  619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)  
------------------------------------------------------------------------------  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
==============================================================================  
FC  607.cactuBSSN_s(base)  
------------------------------------------------------------------------------  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
------------------------------------------------------------------------------

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Huawei

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Compiler Version Notes (Continued)

FC 607.cactuBSSN_s(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

---

FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

---

FC 603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

---

CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)

ifort (IFORT) 18.0.0 20170811
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icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

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#### Base Compiler Invocation

- **C benchmarks:** icc
- **Fortran benchmarks:** ifort
- **Benchmarks using both Fortran and C:** ifort icc
- **Benchmarks using Fortran, C, and C++:** icpc icc ifort

#### Base Portability Flags

- `603.bwaves_s`: `-DSPEC_LP64`
- `607.cactuBSSN_s`: `-DSPEC_LP64`
- `619.lbm_s`: `-DSPEC_LP64`
- `621.wrf_s`: `-DSPEC_LP64 `-DSPEC_CASE_FLAG` `-convert big_endian`
- `627.cam4_s`: `-DSPEC_LP64 `-DSPEC_CASE_FLAG`
- `628.pop2_s`: `-DSPEC_LP64 `-DSPEC_CASE_FLAG` `-convert big_endian` `-assume byterecl`
- `638.imagick_s`: `-DSPEC_LP64`
- `644.nab_s`: `-DSPEC_LP64`
- `649.fotonik3d_s`: `-DSPEC_LP64`
- `654.roms_s`: `-DSPEC_LP64`

#### Base Optimization Flags

- **C benchmarks:**
  - `-xCORE-AVX2` `-ipo` `-O3` `-no-prec-div` `-qopt-prefetch` `-ffinite-math-only` `-qopt-mem-layout-trans=3` `-qopenmp` `-DSPEC_OPENMP`
- **Fortran benchmarks:**
  - `-DSPEC_OPENMP` `-xCORE-AVX2` `-ipo` `-O3` `-no-prec-div` `-qopt-prefetch` `-ffinite-math-only` `-qopt-mem-layout-trans=3` `-qopenmp` `-nostandard-realloc-lhs` `-align array32byte`
- **Benchmarks using both Fortran and C:**
  - `-xCORE-AVX2` `-ipo` `-O3` `-no-prec-div` `-qopt-prefetch` `-ffinite-math-only` `-qopt-mem-layout-trans=3` `-qopenmp` `-DSPEC_OPENMP` `-nostandard-realloc-lhs` `-align array32byte`
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</table>

**Base Optimization Flags (Continued)**

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

**Base Other Flags**

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

**Peak Compiler Invocation**

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

**Peak Portability Flags**

Same as Base Portability Flags
Huawei 1288H V5 (Intel Xeon Silver 4112)  

SPECspeed2017_fp_base = 42.2  
SPECspeed2017_fp_peak = 43.4

CPU2017 License: 3175  
Test Sponsor: Huawei  
Tested by: Huawei  
Test Date: Jul-2018  
Hardware Availability: Jul-2017  
Software Availability: Jan-2018

Peak Optimization Flags

C benchmarks:

619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -02 -xCORE-AVX2  
-qopt-prefetch -ipo -03 -ffinite-math-only -no-prec-div  
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP

638.imagick_s: -xCORE-AVX2 -ipo -03 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:

603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP  
-DSPEC_OPENMP -02 -xCORE-AVX2 -qopt-prefetch -ipo -03  
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3  
-qopenmp -nostandard-realloc-lhs -align array32byte

649.fotonik3d_s: basepeak = yes

654.roms_s: Same as 603.bwaves_s

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -02 -xCORE-AVX2  
-qopt-prefetch -ipo -03 -ffinite-math-only -no-prec-div  
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: basepeak = yes

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:

- prof-gen(pass 1) - prof-use(pass 2) -02 -xCORE-AVX2 -qopt-prefetch  
-ipo -03 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3  
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs  
-align array32byte
SPEC CPU2017 Floating Point Speed Result

Huawei

Huawei 1288H V5 (Intel Xeon Silver 4112)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base = 42.2</th>
<th>SPECspeed2017_fp_peak = 43.4</th>
</tr>
</thead>
<tbody>
<tr>
<td>Huawei 1288H V5 (Intel Xeon Silver 4112)</td>
<td>Huawei 1288H V5 (Intel Xeon Silver 4112)</td>
</tr>
<tr>
<td>CPU2017 License: 3175</td>
<td>Test Date: Jul-2018</td>
</tr>
<tr>
<td>Test Sponsor: Huawei</td>
<td>Hardware Availability: Jul-2017</td>
</tr>
<tr>
<td>Tested by: Huawei</td>
<td>Software Availability: Jan-2018</td>
</tr>
<tr>
<td>SPECspeed2017_fp_peak = 43.4</td>
<td></td>
</tr>
</tbody>
</table>

Peak Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml