**SPEC® CPU2017 Floating Point Speed Result**

**Huawei**

Huawei 1288H V5 (Intel Xeon Gold 5120T)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
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<tbody>
<tr>
<td>87.5</td>
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**CPU2017 License:** 3175

**Test Sponsor:** Huawei

**Tested by:** Huawei

**Test Date:** Aug-2018

**Hardware Availability:** Jul-2017

**Software Availability:** Jan-2018

<table>
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<tr>
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<td>638.imagick_s</td>
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<td>649.fotonik3d_s</td>
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</tr>
<tr>
<td>654.roms_s</td>
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**Threads:**
- 0, 20, 40, 60, 80, 100, 120, 140, 160, 180, 200, 220, 240, 260, 280, 300, 320, 340, 360, 380, 400, 420

**Software**

**CPU Name:** Intel Xeon Gold 5120T

- **Max MHz.:** 3200
- **Nominal:** 2200
- **Enabled:** 28 cores, 2 chips
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 19.25 MB I+D on chip per chip
- **Other:** None
- **Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)
- **Storage:** 1 x 1200 GB SAS, 10000 RPM
- **Other:** None

**OS:** Red Hat Enterprise Linux Server release 7.4 (Maipo)

**Compiler:**
- C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;
- Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux

**Parallel:** Yes

**Firmware:** Version 0.81 Released Jul-2018

**File System:** xfs

**System State:** Run level 3 (multi-user)

**Base Pointers:** 64-bit

**Peak Pointers:** 64-bit

**Other:** None
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#### Results Table

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</table>

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#### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

---

#### General Notes

Environment variables set by runcpu before the start of the run:

- KMP_AFFINITY = "granularity=fine,compact"
- OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4  
Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3>/proc/sys/vm/drop_caches
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

---

#### Platform Notes

BIOS configuration:

- Power Policy Set to Load Balance
- Hyper-Threading Set to Disable

(Continued on next page)
Platform Notes (Continued)

XPT Prefetch Set to Enabled
Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on localhost.localdomain Fri Aug 10 13:19:25 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5120T CPU @ 2.20GHz
  2 "physical id"s (chips)
  28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 14
siblings : 14
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

From lscpu:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                28
On-line CPU(s) list:   0-27
Thread(s) per core:    1
Core(s) per socket:    14
Socket(s):             2
NUMA node(s):          2
Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Gold 5120T CPU @ 2.20GHz
Stepping:              4
CPU MHz:               2201.000
CPU max MHz:           2201.0000
CPU min MHz:           1000.0000
BogoMIPS:              4400.00
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              19712K
NUMA node0 CPU(s):     0-13
NUMA node1 CPU(s):     14-27
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov

(Continued on next page)
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Platform Notes (Continued)

pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpcelgb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfu pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 fma cx16 xtpcr
pdcn pdci dca ssse4_1 ssse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx
f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 invpcid_single intel_pt
spec_ctrl ibpb_support tpr_shadow vnumi flexpriority ept vpid fsgsbase tsc_adjust
bm1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx
smad clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xsaveopt xgetbv1 cqm_llc
cqm_occupa llc cqm_mbb_total cqm_mbb_local dtherm ida arat p1n pts

/platform/cpuinfo cache data
    cache size : 19712 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
    physical chip.
    available: 2 nodes (0-1)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13
    node 0 size: 391349 MB
    node 0 free: 371872 MB
    node 1 cpus: 14 15 16 17 18 19 20 21 22 23 24 25 26 27
    node 1 size: 393216 MB
    node 1 free: 380369 MB
    node distances:
        node   0   1
    0:  10  21
    1:  21  10

From /proc/meminfo
    MemTotal: 790512260 kB
    HugePages_Total: 0
    Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
    os-release:
        NAME="Red Hat Enterprise Linux Server"
        VERSION="7.4 (Maipo)"
        ID="rhel"
        ID_LIKE="fedora"
        VARIANT="Server"
        VARIANT_ID="server"
        VERSION_ID="7.4"
        PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
        redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
        system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
        system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

    uname -a:

(Continued on next page)
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---

**Platform Notes (Continued)**

Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 8 23:39

SPEC is set to: /spec2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 781G 117G 665G 15% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.81 07/02/2018
Memory:
24x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

---

**Compiler Version Notes**

==============================================================================
CC 619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
FC 607.cactuBSSN_s(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
(Continued on next page)
Compiler Version Notes (Continued)

-----------------------------
FC 607.cactuBSSN_s(peak)
-----------------------------
icpc (ICC) 18.0.0 20170811
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icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

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FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
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ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

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FC 603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)
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ifort (IFORT) 18.0.0 20170811
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CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
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icc (ICC) 18.0.0 20170811
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Copyright 2017-2018 Standard Performance Evaluation Corporation

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Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
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</table>

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags
Huawei 1288H V5 (Intel Xeon Gold 5120T) | SPECspeed2017_fp_base = 87.5
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### SPEC CPU2017 Floating Point Speed Result

#### Peak Optimization Flags

**C benchmarks:**

- `619.lbm_s`: `-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
  -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
  -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
  -DSPEC_OPENMP`

- `638.imagick_s`: `basepeak = yes`

- `644.nab_s`: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
  -DSPEC_OPENMP`

**Fortran benchmarks:**

- `-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP`
- `-DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3
  -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
  -nstandard-realloc-lhs -align array32byte`

**Benchmarks using both Fortran and C:**

- `621.wrf_s`: `-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
  -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
  -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
  -DSPEC_OPENMP -nstandard-realloc-lhs -align array32byte`

- `627.cam4_s`: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
  -DSPEC_OPENMP -nstandard-realloc-lhs -align array32byte`

- `628.pop2_s`: `Same as 621.wrf_s`

**Benchmarks using Fortran, C, and C++:**

- `-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch
  -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
  -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nstandard-realloc-lhs
  -align array32byte`

### Peak Other Flags

**C benchmarks:**

- `-m64 -std=c11`
Huawei

Huawei 1288H V5 (Intel Xeon Gold 5120T)

| SPECspeed2017_fp_peak = 89.0 |
| SPECspeed2017_fp_base = 87.5 |

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Peak Other Flags (Continued)

Fortran benchmarks:
- "m64"

Benchmarks using both Fortran and C:
- "m64 -std=c11"

Benchmarks using Fortran, C, and C++:
- "m64 -std=c11"

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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