## SPEC® CPU2017 Integer Speed Result

**Dell Inc.**  
PowerEdge MX740c (Intel Xeon Gold 5117 CPU, 2.00GHz)

<table>
<thead>
<tr>
<th>Thread</th>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench_s</td>
<td>6.73</td>
<td>6.95</td>
</tr>
<tr>
<td>gcc_s</td>
<td></td>
<td></td>
</tr>
<tr>
<td>mcf_s</td>
<td></td>
<td></td>
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<tr>
<td>omnetpp_s</td>
<td></td>
<td></td>
</tr>
<tr>
<td>xalancbmk_s</td>
<td></td>
<td></td>
</tr>
<tr>
<td>x264_s</td>
<td></td>
<td></td>
</tr>
<tr>
<td>deepsjeng_s</td>
<td></td>
<td></td>
</tr>
<tr>
<td>leela_s</td>
<td></td>
<td></td>
</tr>
<tr>
<td>exchange2_s</td>
<td></td>
<td></td>
</tr>
<tr>
<td>xz_s</td>
<td></td>
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</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Date:** Aug-2018  
**Test Sponsor:** Dell Inc.  
**Hardware Availability:** Sep-2017  
**Tested by:** Dell Inc.  
**Software Availability:** Jun-2018

### Software

- **OS:** SUSE Linux Enterprise Server 12 SP3  
  - 4.138-8.g8686768-default
- **Compiler:**  
  - C/C++: Version 18.0.0.128 of Intel C/C++  
  - Compiler for Linux: Fortran: Version 18.0.0.128 of Intel Fortran
- **Firmware:**  
  - Version 1.6.0 released Jul-2018
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 32/64-bit
- **Other:** jemalloc memory allocator library V5.0.1

### Hardware

- **CPU Name:** Intel Xeon Gold 5117  
- **Max MHz.:** 2800  
- **Nominal:** 2000
- **Enabled:** 28 cores, 2 chips  
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 19.25 MB I+D on chip per chip  
- **Other:** None
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
- **Storage:** 1 TB SATA SSD  
- **Other:** None
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SPECspeed2017_int_base = 6.73
SPECspeed2017_int_peak = 6.95

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>28</td>
<td>377</td>
<td>4.71</td>
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<td>4.68</td>
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<td>602.gcc_s</td>
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<td>605.mcf_s</td>
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<tr>
<td>610.omnetpp_s</td>
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<td>347</td>
<td>4.70</td>
<td>357</td>
<td>4.57</td>
<td>353</td>
<td>4.63</td>
<td>28</td>
<td>347</td>
<td>4.69</td>
<td>342</td>
<td>4.77</td>
<td>341</td>
<td>4.79</td>
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<td>623.xalancmk_s</td>
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<td>7.22</td>
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<td>194</td>
<td>7.32</td>
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<td>7.76</td>
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<td>625.x264_s</td>
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<td>8.90</td>
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<td>631.deepsjeng_s</td>
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<td>3.95</td>
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<td>3.97</td>
<td>361</td>
<td>3.97</td>
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<tr>
<td>648.exchange2_s</td>
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<td>10.1</td>
<td>290</td>
<td>10.1</td>
<td>289</td>
<td>10.2</td>
<td>28</td>
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<td>10.1</td>
<td>289</td>
<td>10.2</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>28</td>
<td>375</td>
<td>16.5</td>
<td>376</td>
<td>16.4</td>
<td>374</td>
<td>16.5</td>
<td>28</td>
<td>364</td>
<td>17.0</td>
<td>363</td>
<td>17.0</td>
<td>364</td>
<td>17.0</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
jemalloc: sources available via jemalloc.net
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
**Platform Notes**

BIOS settings:
- Sub NUMA Cluster Disabled
- Virtualization Technology Disabled
- System Profile set to Custom
- CPU Performance set to Maximum Performance
- C States set to Autonomous
- C1E Disabled
- Uncore Frequency set to Dynamic
- Energy Efficiency Policy set to Performance
- Memory Patrol Scrub Disabled
- Logical Processor Disabled
- CPU Interconnect Bus Link Power Management Disabled
- PCI ASPM L1 Link Power Management Disabled

Sysinfo program /root/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcccc091c0f
running on linux-wlx Tue Aug 7 01:40:29 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) Gold 5117 CPU @ 2.00GHz
  2 "physical id"s (chips)
  28 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 14
  siblings : 14
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 28
- On-line CPU(s) list: 0-27
- Thread(s) per core: 1
- Core(s) per socket: 14
- Socket(s): 2
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Gold 5117 CPU @ 2.00GHz
- Stepping: 4

(Continued on next page)
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<tr>
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<th>SPECspeed2017_int_peak</th>
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<tbody>
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<td>6.73</td>
<td>6.95</td>
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### CPU2017 License:
- Dell Inc.

### Test Sponsor:
- Dell Inc.

### Tested by:
- Dell Inc.

### Hardware Availability:
- Sep-2017

### Software Availability:
- Jun-2018

---

### Platform Notes (Continued)

<table>
<thead>
<tr>
<th>Field</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU MHz:</td>
<td>1995.330</td>
</tr>
<tr>
<td>BogoMIPS:</td>
<td>3990.66</td>
</tr>
<tr>
<td>Virtualization:</td>
<td>VT-x</td>
</tr>
<tr>
<td>L1d cache:</td>
<td>32K</td>
</tr>
<tr>
<td>L1i cache:</td>
<td>32K</td>
</tr>
<tr>
<td>L2 cache:</td>
<td>1024K</td>
</tr>
<tr>
<td>L3 cache:</td>
<td>19712K</td>
</tr>
<tr>
<td>NUMA node0 CPU(s):</td>
<td>0,2,4,6,8,10,12,14,16,18,20,22,24,26</td>
</tr>
<tr>
<td>NUMA node1 CPU(s):</td>
<td>1,3,5,7,9,11,13,15,17,19,21,23,25,27</td>
</tr>
<tr>
<td>Flags:</td>
<td>fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperf perf_event pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtop pdcib pcid dca sse4_1 sse4_2 x2apic movbe popcnt pcpn tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts dtherm intel_pt rsb_ctxsw spec_ctrl ssbd ibpb stibp retpoline kaiser tpr_shadow vmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 3dnow invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc pku ospke</td>
</tr>
</tbody>
</table>

From `/proc/cpuinfo`

- cache size : 19712 KB

From `numactl --hardware`

```
WARNING: a numactl 'node' might or might not correspond to a physical chip.
```

```
available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26
node 0 size: 192116 MB
node 0 free: 191748 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27
node 1 size: 193515 MB
node 1 free: 193172 MB
node distances:
    node   0   1
    0:  10  21
    1:  21  10
```

From `/proc/meminfo`

- MemTotal: 394886400 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From `/etc/*release*`

```
SUSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 3
```

(Continued on next page)
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Platform Notes (Continued)

# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
  NAME="SLES"
  VERSION="12-SP3"
  VERSION_ID="12.3"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
Linux linux-wlwx 4.4.138-8.g8686768-default #1 SMP Mon Jun 25 17:25:25 UTC 2018 (8686768) x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Aug 6 23:58

SPEC is set to: /root/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 604G 15G 590G 3% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS Dell Inc. 1.6.0 07/19/2018
  Memory:
    24x 00CE063200CE M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC   600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
CC   600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 625.x264_s(peak)
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

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---

### Compiler Version Notes (Continued)

---

```bash
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)  
641.leela_s(base)
```

icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

---

```bash
CXXC 620.omnetpp_s(peak) 623.xalancbmk_s(peak) 631.deepsjeng_s(peak)  
641.leela_s(peak)
```

icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

---

```bash
FC 648.exchange2_s(base, peak)
```

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

---

### Base Compiler Invocation

**C benchmarks:**

icc

**C++ benchmarks:**

icpc

**Fortran benchmarks:**

ifort

---

### Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64  
602.gcc_s: -DSPEC_LP64  
605.mcf_s: -DSPEC_LP64  
620.omnetpp_s: -DSPEC_LP64  
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX

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### Base Portability Flags (Continued)

- 625.x264_s: -DSPEC_LP64
- 631.deepsjeng_s: -DSPEC_LP64
- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
- -L/usr/local/je5.0.1-64/lib -ljemalloc

**C++ benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

**Fortran benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
- -L/usr/local/je5.0.1-64/lib -ljemalloc

### Base Other Flags

**C benchmarks:**
- -m64 -std=c11

**C++ benchmarks:**
- -m64

**Fortran benchmarks:**
- -m64

### Peak Compiler Invocation

**C benchmarks:**
- icc

**C++ benchmarks:**
- icpc

(Continued on next page)
**Peak Compiler Invocation (Continued)**

Fortran benchmarks:

```
ifort
```

**Peak Portability Flags**

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmks: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

**Peak Optimization Flags**

C benchmarks:

```
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -fno-strict-overflow -L/usr/local/je5.0.1-64/lib -ljemalloc
602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc
605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc
625.x264_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc
```

(Continued on next page)
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Copyright 2017-2018 Standard Performance Evaluation Corporation

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Peak Optimization Flags (Continued)

657.xz_s: Same as 602.gcc_s

C++ benchmarks:
620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

623.xalancbmk_s: -L/opt/intel-compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-32/lib -ljemalloc

631.deepsjeng_s: Same as 620.omnetpp_s

641.leela_s: Same as 620.omnetpp_s

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks (except as noted below):
-m64

623.xalancbmk_s: -m32

Fortran benchmarks:
-m64

The flags files that were used to format this result can be browsed at
## SPEC CPU2017 Integer Speed Result

### Dell Inc.
PowerEdge MX740c (Intel Xeon Gold 5117 CPU, 2.00GHz)

<table>
<thead>
<tr>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>6.73</td>
<td>6.95</td>
</tr>
</tbody>
</table>

- CPU2017 License: 55
- Test Sponsor: Dell Inc.
- Tested by: Dell Inc.
- Test Date: Aug-2018
- Hardware Availability: Sep-2017
- Software Availability: Jun-2018

You can also download the XML flags sources by saving the following links:

- [Dell-Platform-Flags-PowerEdge14G-revC.xml](http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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