Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4112 CPU, 2.60GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: May-2018
Hardware Availability: Sep-2017
Software Availability: Sep-2017

SPECrate2017_fp_base = 49.3
SPECrate2017_fp_peak = 50.5

Hardware

CPU Name: Intel Xeon Silver 4112
Max MHz.: 3000
Nominal: 2600
Enabled: 8 cores, 2 chips, 2 threads/core
Orderable: 1.2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 8.25 MB I+D on chip per core
Other: None
Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)
Storage: 960 GB SAS SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 12 SP3
4.4.114-94.11-default
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++
Compiler for Linux;
Fortran: Version 18.0.0.128 of Intel Fortran
Compiler for Linux
Parallel: No
Firmware: Version 0.4.3 released May-2018
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None

503.bwaves_r 16
507.cactuBSSN_r 16
508.namd_r 16
510.parest_r 16
511.povray_r 16
519.lbm_r 16
521.wrf_r 16
526.blender_r 16
527.cam4_r 16
538.imagick_r 16
544.nab_r 16
549.fotonik3d_r 16
554.roms_r 16

SPECrater2017_fp_base (49.3) SPECrate2017_fp_peak (50.5)
### SPEC CPU2017 Floating Point Rate Result

**Dell Inc.**

PowerEdge MX740c (Intel Xeon Silver 4112 CPU, 2.60GHz)

<table>
<thead>
<tr>
<th>CPU2017 License: 55</th>
<th>Test Sponsor: Dell Inc.</th>
<th>Tested by: Dell Inc.</th>
</tr>
</thead>
</table>

**SPECrate2017_fp_base = 49.3**

**SPECrate2017_fp_peak = 50.5**

---

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base</th>
<th>Peak</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Copies</td>
<td>Seconds</td>
</tr>
<tr>
<td>503.bwaves_r</td>
<td>16</td>
<td>871</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>16</td>
<td>521</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>16</td>
<td>449</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>16</td>
<td>1087</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>16</td>
<td>684</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>16</td>
<td>467</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>16</td>
<td>678</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>16</td>
<td>534</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>16</td>
<td>717</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>16</td>
<td>593</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>16</td>
<td>461</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>16</td>
<td>1253</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>16</td>
<td>760</td>
</tr>
</tbody>
</table>

**SPECrate2017_fp_base = 49.3**

**SPECrate2017_fp_peak = 50.5**

---

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

---

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

---

### General Notes

Environment variables set by runcpu before the start of the run:


Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

 Prior to runcpu invocation

(Continued on next page)
General Notes (Continued)

Filesystem page cache synced and cleared with:
`sync; echo 3>/proc/sys/vm/drop_caches`
runcpu command invoked through numacltl i.e.:
`numactl --interleave=all runcpu <etc>`

Platform Notes

BIOS settings:
Sub NUMA Cluster Disabled
Virtualization Technology Disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E Disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub Disabled
Logical Processor Enabled
CPU Interconnect Bus Link Power Management Disabled
PCI ASPM L1 Link Power Management Disabled
Sysinfo program /root/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bdc091c0f
running on linux-kuth Tue May 29 17:37:58 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 8
physical 0: cores 0 1 3 4
physical 1: cores 1 2 4 5

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Thread(s) per core: 2
# SPEC CPU2017 Floating Point Rate Result

## Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4112 CPU, 2.60GHz)

### SPECrate2017_fp_base = 49.3
### SPECrate2017_fp_peak = 50.5

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>55</th>
<th>Test Date:</th>
<th>May-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Dell Inc.</td>
<td>Hardware Availability:</td>
<td>Sep-2017</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
<td>Software Availability:</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

### Platform Notes (Continued)

- **Core(s) per socket:** 4
- **Socket(s):** 2
- **NUMA node(s):** 2
- **Vendor ID:** GenuineIntel
- **CPU family:** 6
- **Model:** 85
- **Model name:** Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
- **Stepping:** 4
- **CPU MHz:** 2594.024
- **BogoMIPS:** 5188.04
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 8448K
- **NUMA node0 CPU(s):** 0,2,4,6,8,10,12,14
- **NUMA node1 CPU(s):** 1,3,5,7,9,11,13,15
- **Flags:** fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pni pclmulqdq dts mwait msr_drbudt msr_cblend m Mull lbmi hlav2 smep bmi2 erva invoptd ltm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsaveprec xgetbv1 cqm_11c cqm_occup_llc pku ospke

```
From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a physical chip.

<table>
<thead>
<tr>
<th>available: 2 nodes (0-1)</th>
<th>node 0 cpus: 0 2 4 6 8 10 12 14</th>
</tr>
</thead>
<tbody>
<tr>
<td>node 0 size: 96298 MB</td>
<td>node 0 free: 95727 MB</td>
</tr>
<tr>
<td>node 1 cpus: 1 3 5 7 9 11 13 15</td>
<td>node 1 size: 96749 MB</td>
</tr>
<tr>
<td>node 1 free: 96218 MB</td>
<td>node distances:</td>
</tr>
<tr>
<td></td>
<td>node 0 1</td>
</tr>
<tr>
<td></td>
<td>0: 10 21</td>
</tr>
<tr>
<td></td>
<td>1: 21 10</td>
</tr>
</tbody>
</table>

From /proc/meminfo

| MemTotal: 197681980 kB |
```

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4112 CPU, 2.60GHz)

SPECrate2017_fp_base = 49.3
SPECrate2017_fp_peak = 50.5

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: May-2018
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Platform Notes (Continued)

HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP3

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 3
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP3"
    VERSION_ID="12.3"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
  Linux linux-kuth 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
  x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 29 08:11

SPEC is set to: /root/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 890G 17G 873G 2% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.4.3 05/15/2018
Memory:
  12x 00AD063200AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666, configured at 2400
  12x Not Specified Not Specified

(End of data from sysinfo program)
Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4112 CPU, 2.60GHz)

SPEC CPU2017 Floating Point Rate Result

SPECrater2017_fp_base = 49.3
SPECrater2017_fp_peak = 50.5

CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: May-2018
Tested by: Dell Inc.
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Compiler Version Notes

==============================================================================
CC  519.lbm_r(base)  538.imagick_r(base, peak)  544.nab_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
CC  519.lbm_r(peak)  544.nab_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
CXXC  508.namd_r(base)  510.parest_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
CXXC  508.namd_r(peak)  510.parest_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
CC  511.povray_r(base)  526.blender_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
CC  511.povray_r(peak)  526.blender_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4112 CPU, 2.60GHz)

SPECrate2017_fp_base = 49.3
SPECrate2017_fp_peak = 50.5

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Compiler Version Notes (Continued)

FC 507.cactuBSSN_r(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 507.cactuBSSN_r(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 554.roms_r(peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CC 521.wrf_r(base) 527.cam4_r(base)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CC 521.wrf_r(peak) 527.cam4_r(peak)

(Continued on next page)
**SPEC CPU2017 Floating Point Rate Result**

**Dell Inc.**
PowerEdge MX740c (Intel Xeon Silver 4112 CPU, 2.60GHz)  

<table>
<thead>
<tr>
<th>Spec CPU2017 License: 55</th>
<th>Test Date: May-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Dell Inc.</td>
<td>Hardware Availability: Sep-2017</td>
</tr>
<tr>
<td>Tested by: Dell Inc.</td>
<td>Software Availability: Sep-2017</td>
</tr>
</tbody>
</table>

**SPECrate2017_fp_base = 49.3**  
**SPECrate2017_fp_peak = 50.5**

**Compiler Version Notes (Continued)**

`ifort (IFORT) 18.0.0 20170811`  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
`icc (ICC) 18.0.0 20170811`  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------

**Base Compiler Invocation**

C benchmarks:  
`icc`

C++ benchmarks:  
`icpc`

Fortran benchmarks:  
`ifort`

Benchmarks using both Fortran and C:  
`ifort icc`

Benchmarks using both C and C++:  
`icpc icc`

Benchmarks using Fortran, C, and C++:  
`icpc icc ifort`

**Base Portability Flags**

503.bwaves_r: -DSPEC_LP64  
507.cactuBSSN_r: -DSPEC_LP64  
508.namd_r: -DSPEC_LP64  
510.parest_r: -DSPEC_LP64  
511.povray_r: -DSPEC_LP64  
519.lbm_r: -DSPEC_LP64  
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char  
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG  
538.imagick_r: -DSPEC_LP64  
544.nab_r: -DSPEC_LP64  
549.fotonik3d_r: -DSPEC_LP64  
554.roms_r: -DSPEC_LP64
**SPEC CPU2017 Floating Point Rate Result**

**Dell Inc.**

PowerEdge MX740c (Intel Xeon Silver 4112 CPU, 2.60GHz)  

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>55</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by</td>
<td>Dell Inc.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>49.3</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak</td>
<td>50.5</td>
</tr>
</tbody>
</table>

**Base Optimization Flags**

**C benchmarks:**
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`

**C++ benchmarks:**
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`

**Fortran benchmarks:**
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

**Benchmarks using both Fortran and C:**
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

**Benchmarks using both C and C++:**
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`

**Benchmarks using Fortran, C, and C++:**
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

**Base Other Flags**

**C benchmarks:**
- `-m64`  
- `-std=c11`

**C++ benchmarks:**
- `-m64`

**Fortran benchmarks:**
- `-m64`

**Benchmarks using both Fortran and C:**
- `-m64`  
- `-std=c11`

**Benchmarks using both C and C++:**
- `-m64`  
- `-std=c11`

**Benchmarks using Fortran, C, and C++:**
- `-m64`  
- `-std=c11`
Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4112 CPU, 2.60GHz)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>49.3</td>
<td>50.5</td>
</tr>
</tbody>
</table>

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

**Peak Compiler Invocation**

C benchmarks:
ncc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

C benchmarks:
519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: Same as 519.lbm_r

C++ benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:

(Continued on next page)
Peak Optimization Flags (Continued)

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -nstandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -nstandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -nstandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -nstandard-realloc-lhs -align array32byte

Peak Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11
SPEC CPU2017 Floating Point Rate Result

Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4112 CPU, 2.60GHz)  SPECrate2017_fp_base = 49.3
SPECrate2017_fp_peak = 50.5

CPU2017 License: 55  Test Date: May-2018
Test Sponsor: Dell Inc.  Hardware Availability: Sep-2017
Tested by: Dell Inc.  Software Availability: Sep-2017

Peak Other Flags (Continued)

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-05-29 05:37:58-0400.
Originally published on 2018-09-04.