# Dell Inc.

PowerEdge MX740c (Intel Xeon Silver 4108 CPU, 1.80GHz)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>74.6</td>
<td>76.4</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Test Date:** May-2018  
**Hardware Availability:** Sep-2017

## Hardware

- **CPU Name:** Intel Xeon Silver 4108  
- **Max MHz.:** 3000  
- **Nominal:** 1800  
- **Enabled:** 16 cores, 2 chips, 2 threads/core  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 11 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
- **Storage:** 960 GB SAS SSD  
- **Other:** None

## Software

- **OS:** SUSE Linux Enterprise Server 12 SP3  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
- **Parallel:** No  
- **Firmware:** Version 0.4.3 released May-2018  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None
## Dell Inc. 
PowerEdge MX740c (Intel Xeon Silver 4108 CPU, 1.80GHz)

### SPECrate2017_fp_base = 74.6

### SPECrate2017_fp_peak = 76.4

#### CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

#### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>32</td>
<td>1199</td>
<td>268</td>
<td>1180</td>
<td>272</td>
<td>1194</td>
<td>269</td>
<td>32</td>
<td>1187</td>
<td>270</td>
<td>1192</td>
<td>269</td>
<td>1190</td>
<td>270</td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>32</td>
<td>654</td>
<td>62.0</td>
<td>654</td>
<td>62.0</td>
<td>654</td>
<td>61.9</td>
<td>32</td>
<td>668</td>
<td>60.6</td>
<td>670</td>
<td>60.4</td>
<td>669</td>
<td>60.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>32</td>
<td>623</td>
<td>48.8</td>
<td>621</td>
<td>48.9</td>
<td>622</td>
<td>48.8</td>
<td>32</td>
<td>622</td>
<td>48.9</td>
<td>622</td>
<td>48.9</td>
<td>617</td>
<td>49.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>32</td>
<td>1512</td>
<td>55.4</td>
<td>1526</td>
<td>54.9</td>
<td>1529</td>
<td>54.8</td>
<td>32</td>
<td>1521</td>
<td>55.0</td>
<td>1521</td>
<td>55.0</td>
<td>1524</td>
<td>54.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>32</td>
<td>947</td>
<td>78.9</td>
<td>947</td>
<td>78.9</td>
<td>949</td>
<td>78.7</td>
<td>32</td>
<td>811</td>
<td>92.1</td>
<td>817</td>
<td>91.5</td>
<td>816</td>
<td>91.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>32</td>
<td>576</td>
<td>58.6</td>
<td>592</td>
<td>56.9</td>
<td>591</td>
<td>57.1</td>
<td>32</td>
<td>533</td>
<td>63.3</td>
<td>538</td>
<td>62.7</td>
<td>535</td>
<td>63.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>32</td>
<td>820</td>
<td>87.4</td>
<td>824</td>
<td>87.0</td>
<td>824</td>
<td>87.0</td>
<td>32</td>
<td>802</td>
<td>89.4</td>
<td>799</td>
<td>89.7</td>
<td>804</td>
<td>89.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>32</td>
<td>720</td>
<td>67.7</td>
<td>719</td>
<td>67.8</td>
<td>721</td>
<td>67.6</td>
<td>32</td>
<td>718</td>
<td>67.9</td>
<td>717</td>
<td>67.9</td>
<td>722</td>
<td>67.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>32</td>
<td>850</td>
<td>65.9</td>
<td>848</td>
<td>66.0</td>
<td>850</td>
<td>65.8</td>
<td>32</td>
<td>837</td>
<td>66.9</td>
<td>835</td>
<td>67.0</td>
<td>836</td>
<td>67.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>32</td>
<td>832</td>
<td>95.7</td>
<td>832</td>
<td>95.7</td>
<td>833</td>
<td>95.6</td>
<td>32</td>
<td>832</td>
<td>95.6</td>
<td>832</td>
<td>95.6</td>
<td>834</td>
<td>95.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>32</td>
<td>641</td>
<td>84.0</td>
<td>636</td>
<td>84.6</td>
<td>638</td>
<td>84.4</td>
<td>32</td>
<td>632</td>
<td>85.2</td>
<td>630</td>
<td>85.5</td>
<td>628</td>
<td>85.8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>32</td>
<td>1649</td>
<td>75.6</td>
<td>1629</td>
<td>76.6</td>
<td>1628</td>
<td>76.6</td>
<td>32</td>
<td>1624</td>
<td>76.8</td>
<td>1628</td>
<td>76.6</td>
<td>1632</td>
<td>76.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>32</td>
<td>1102</td>
<td>46.1</td>
<td>1123</td>
<td>45.3</td>
<td>1114</td>
<td>45.6</td>
<td>32</td>
<td>1080</td>
<td>47.1</td>
<td>1077</td>
<td>47.2</td>
<td>1076</td>
<td>47.3</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

```
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation
General Notes (Continued)

Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
umactl --interleave=all runcpu <etc>

Platform Notes

BIOS settings:
Sub NUMA Cluster Disabled
Virtualization Technology Disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E Disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub Disabled
Logical Processor Enabled
CPU Interconnect Bus Link Power Management Disabled
PCI ASPM L1 Link Power Management Disabled
Sysinfo program /root/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f
running on linux-kuth Sat May 26 08:15:55 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4108 CPU @ 1.80GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
Dell Inc.  
PowerEdge MX740c (Intel Xeon Silver 4108 CPU, 1.80GHz)  

**SPEC CPU2017 Floating Point Rate Result**

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### Test Details

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>55</th>
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<tbody>
<tr>
<td>Test Sponsor:</td>
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<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Test Date:</td>
<td>May-2018</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Sep-2017</td>
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</tbody>
</table>

**SPECrate2017_fp_base = 74.6**  
**SPECrate2017_fp_peak = 76.4**

### Platform Notes (Continued)

- **Core(s) per socket:** 8
- **Socket(s):** 2
- **NUMA node(s):** 2
- **Vendor ID:** GenuineIntel
- **CPU family:** 6
- **Model:** 85
- **Model name:** Intel(R) Xeon(R) Silver 4108 CPU @ 1.80GHz
- **Stepping:** 4
- **CPU MHZ:** 1795.866
- **BogoMIPS:** 3591.73
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 11264K

**NUMA node0 CPU(s):** 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30  
**NUMA node1 CPU(s):** 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31

**Flags:** fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl apicbi xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epbi invpcid_single pln pts dtherm intel_pt rsb_ctxsw spec_ctrl retelopine kaiser tpr_shadow vmlinux flexpriority ept vpl fsgsbase tsc_adjust bsmi hle avx2 smep bmi1 erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc pku ospke

/proc/cpuinfo cache data  
```  
cache size : 11264 KB
```

From numactl --hardware  
```  
WARNING: a numactl 'node' might or might not correspond to a physical chip.
```
```  
available: 2 nodes (0-1)  
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30  
node 0 size: 96297 MB  
node 0 free: 95871 MB  
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31  
node 1 size: 96748 MB  
node 1 free: 96419 MB  
node distances:  
```  
node 0 1  
0: 10 21  
1: 21 10
```

From /proc/meminfo  
```  
MemTotal: 197679868 kB
```
Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4108 CPU, 1.80GHz)

SPECrate2017_fp_base = 74.6
SPECrate2017_fp_peak = 76.4

Platform Notes (Continued)

HugePages_Total:       0
Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP3

From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 3
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.
    os-release:
        NAME="SLES"
        VERSION="12-SP3"
        VERSION_ID="12.3"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
    Linux linux-kuth 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
    x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 25 19:46

SPEC is set to: /root/cpu2017
    Filesystem Type  Size Used Avail Use% Mounted on
    /dev/sda2  xfs  890G  18G  873G  2% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    BIOS Dell Inc. 0.4.3 05/15/2018
    Memory:
        12x 00ADB063200AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666, configured at 2400
        12x Not Specified Not Specified

(End of data from sysinfo program)
Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4108 CPU, 1.80GHz)

Dell Inc.

SPEC CPU2017 Floating Point Rate Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

SPECRate2017_fp_base = 74.6
SPECRate2017_fp_peak = 76.4

Compiler Version Notes

==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

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CC  519.lbm_r(peak) 544.nab_r(peak)
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CXXC 508.namd_r(base) 510.parest_r(base)
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PowerEdge MX740c (Intel Xeon Silver 4108 CPU, 1.80GHz)  

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Test Date: May-2018  
Hardware Availability: Sep-2017  
Software Availability: Sep-2017

Compiler Version Notes (Continued)

FC 507.cactuBSSN_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
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FC 507.cactuBSSN_r(peak)
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icpc (ICC) 18.0.0 20170811  
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icc (ICC) 18.0.0 20170811  
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CC 521.wrf_r(peak) 527.cam4_r(peak)
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(Continued on next page)
Compiler Version Notes (Continued)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
Dell Inc.
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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: May-2018
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11
Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4108 CPU, 1.80GHz)

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**Hardware Availability:** Sep-2017
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---

## Peak Compiler Invocation

C benchmarks:
- icc

C++ benchmarks:
- icpc

Fortran benchmarks:
- ifort

Benchmarks using both Fortran and C:
- ifort icc

Benchmarks using both C and C++:
- icpc icc

Benchmarks using Fortran, C, and C++:
- icpc icc ifort

---

## Peak Portability Flags

Same as Base Portability Flags

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## Peak Optimization Flags

C benchmarks:
- 519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3
- 538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3
- 544.nab_r: Same as 519.lbm_r

C++ benchmarks:
- -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

Fortran benchmarks:

(Continued on next page)
Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4108 CPU, 1.80GHz)

SPECrate2017_fp_base = 74.6
SPECrate2017_fp_peak = 76.4

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
Test Date: May-2018
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Peak Optimization Flags (Continued)

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Peak Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

(Continued on next page)
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**SPEC CPU2017 Floating Point Rate Result**

**Dell Inc.**
PowerEdge MX740c (Intel Xeon Silver 4108 CPU, 1.80GHz)  

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>Test Date:</th>
</tr>
</thead>
<tbody>
<tr>
<td>55</td>
<td>May-2018</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Test Sponsor:</th>
<th>Tested by:</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dell Inc.</td>
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</tr>
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**Peak Other Flags (Continued)**

Benchmarks using Fortran, C, and C++:
- `-m64`  
- `-std=c11`

The flags files that were used to format this result can be browsed at:

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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