NEC Corporation
Express5800/D120h (Intel Xeon Gold 6142)

SPECspeed2017_fp_base = 67.6
SPECspeed2017_fp_peak = 68.9

CPU2017 License: 9006  Test Date: Sep-2018
Test Sponsor: NEC Corporation  Hardware Availability: Jan-2018
Tested by: NEC Corporation  Software Availability: Mar-2018

Hardware
CPU Name: Intel Xeon Gold 6142
Max MHz.: 3700
Nominal: 2600
Enabled: 16 cores, 1 chip
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 22 MB I+D on chip per chip
Other: None
Memory: 192 GB (6 x 32 GB 2Rx4 PC4-2666V-R)
Storage: 1 x 1 TB SATA, 7200 RPM
Other: None

Software
OS: Red Hat Enterprise Linux Server release 7.4 (Maipo)
Kernel 3.10.0-693.21.1.el7.x86_64
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux:
Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux:
Parallel: Yes
Firmware: Version F21 02/22/2018 released Apr-2018
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
SPEC CPU2017 Floating Point Speed Result

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NEC Corporation
Expression5800/D120h (Intel Xeon Gold 6142)

SPECspeed2017_fp_base = 67.6
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>16</td>
<td>236</td>
<td>250</td>
<td>240</td>
<td>246</td>
<td>240</td>
<td>246</td>
<td>16</td>
<td>238</td>
<td>248</td>
<td>240</td>
<td>246</td>
<td></td>
<td></td>
</tr>
<tr>
<td>607.cactubssn_s</td>
<td>16</td>
<td>179</td>
<td>93.1</td>
<td>179</td>
<td>94.3</td>
<td>179</td>
<td>93.3</td>
<td>16</td>
<td>177</td>
<td>94.2</td>
<td>177</td>
<td>94.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>16</td>
<td>237</td>
<td>22.1</td>
<td>239</td>
<td>21.9</td>
<td>240</td>
<td>21.9</td>
<td>16</td>
<td>241</td>
<td>21.8</td>
<td>237</td>
<td>22.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>16</td>
<td>176</td>
<td>75.1</td>
<td>176</td>
<td>75.0</td>
<td>177</td>
<td>74.6</td>
<td>16</td>
<td>165</td>
<td>80.2</td>
<td>164</td>
<td>80.6</td>
<td></td>
<td></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>16</td>
<td>190</td>
<td>46.6</td>
<td>190</td>
<td>46.6</td>
<td>190</td>
<td>46.6</td>
<td>16</td>
<td>190</td>
<td>46.6</td>
<td>190</td>
<td>46.6</td>
<td></td>
<td></td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>16</td>
<td>191</td>
<td>62.0</td>
<td>192</td>
<td>61.8</td>
<td>192</td>
<td>61.8</td>
<td>16</td>
<td>184</td>
<td>64.6</td>
<td>184</td>
<td>64.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>16</td>
<td>257</td>
<td>56.2</td>
<td>251</td>
<td>57.6</td>
<td>268</td>
<td>53.8</td>
<td>16</td>
<td>257</td>
<td>56.2</td>
<td>251</td>
<td>57.6</td>
<td></td>
<td></td>
</tr>
<tr>
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<td>16</td>
<td>162</td>
<td>108</td>
<td>162</td>
<td>108</td>
<td>162</td>
<td>108</td>
<td>16</td>
<td>162</td>
<td>108</td>
<td>162</td>
<td>108</td>
<td></td>
<td></td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>16</td>
<td>192</td>
<td>47.6</td>
<td>190</td>
<td>48.0</td>
<td>190</td>
<td>48.0</td>
<td>16</td>
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<td></td>
<td></td>
</tr>
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<td>16</td>
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<td>249</td>
<td>63.1</td>
<td>251</td>
<td>62.7</td>
<td>16</td>
<td>241</td>
<td>65.3</td>
<td>236</td>
<td>66.6</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "'/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
NEC Corporation

Express5800/D120h (Intel Xeon Gold 6142)

SPEC CPU2017 Floating Point Speed Result
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SPECspeed2017_fp_base = 67.6
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Test Sponsor: NEC Corporation
Hardware Availability: Jan-2018
Tested by: NEC Corporation
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BIOS Settings:
ENERGY_PERF_BIAS_CFG mode: Performance
Hyper-Threading [ALL]: Disable
LLC dead line alloc: Disable
Patrol Scrub: Disable
DCU Streamer Prefetcher: Disable
Adjacent Cache Prefetch: Disable
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on d120h Sun Sep 2 13:49:14 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6142 CPU @ 2.60GHz
  1 "physical id"s (chips)
  16 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 16
    siblings : 16
    physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6142 CPU @ 2.60GHz
Stepping: 4
CPU MHz: 3576.625
CPU max MHz: 3700.0000
CPU min MHz: 1000.0000
BogoMIPS: 5200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K

(Continued on next page)
**SPEC CPU2017 Floating Point Speed Result**

**NEC Corporation**

**Express5800/D120h (Intel Xeon Gold 6142)**

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**Platform Notes (Continued)**

```plaintext
L2 cache:              1024K
L3 cache:              22528K
NUMA node0 CPU(s):     0-15

Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                       pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
                       aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma
                       cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
                       xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_13 cd_p13 invpcid_single
                       intel_pt spec_ctrl ibpb_support tpr_shadow vmi flexpriority ept vpid fsgsbase
                       tsc_adjust bmi1 hle avx2 smep bmi2 erness invpcid rtm cqm mpx rdt_a avx512f avx512dq
                       rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xsavec
                       xcm_llc xcm_occup_llc xcm_mbm_total xcm_mbm_local dtherm ida arat pln pts hwp
                       hwp_act_window hwp_epp hwp_pkg_req

/proc/cpuinfo cache data
  cache size : 22528 KB

From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  node 0 size: 195236 MB
  node 0 free: 190080 MB
  node distances:
  node  0:
  0:  10

From /proc/meminfo
  MemTotal:       196476416 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.4 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VARIANT="Server"
    VARIANT_ID="server"
    VERSION_ID="7.4"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
  redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
  system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

(Continued on next page)
NEC Corporation

Express5800/D120h (Intel Xeon Gold 6142)

SPEC CPU2017 Floating Point Speed Result

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Platform Notes (Continued)

uname -a:
   Linux d120h 3.10.0-693.21.1.el17.x86_64 #1 SMP Fri Feb 23 18:54:16 UTC 2018 x86_64
   x86_64 x86_64 GNU/Linux

run-level 3 Sep 2 13:43

SPEC is set to: /home/cpu2017

Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda3      ext4  909G  420G  443G  49% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMI BIOS" standard.

BIOS GIGABYTE F21 02/22/2018
Memory: 10x NO DIMM NO DIMM
       6x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC   619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC   619.lbm_s(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC  607.cactuBSSN_s(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

(Continued on next page)
NEC Corporation
Express5800/D120h (Intel Xeon Gold 6142)

SPEC CPU2017 Floating Point Speed Result

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SPECspeed2017_fp_base = 67.6
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CPU2017 License: 9006
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Test Date: Sep-2018
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Compiler Version Notes (Continued)

===============================================================================
FC  607.cactuBSSN_s(peak)

------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
===============================================================================
FC  603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)

------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
===============================================================================
FC  603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)

------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
===============================================================================
CC  621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)

------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
===============================================================================
CC  621.wrf_s(peak) 628.pop2_s(peak)

------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
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icc (ICC) 18.0.0 20170811
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**NEC Corporation**

**Express5800/D120h (Intel Xeon Gold 6142)**

**SPECs**

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**CPU2017 License:** 9006

**Test Sponsor:** NEC Corporation

**Tested by:** NEC Corporation

**Test Date:** Sep-2018

**Hardware Availability:** Jan-2018

**Software Availability:** Mar-2018

---

**Base Compiler Invocation**

- **C benchmarks:** icc
- **Fortran benchmarks:** ifort
- **Benchmarks using both Fortran and C:** ifort icc
- **Benchmarks using Fortran, C, and C++:** icpc icc ifort

---

**Base Portability Flags**

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

---

**Base Optimization Flags**

- **C benchmarks:**
  -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
  -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

- **Fortran benchmarks:**
  -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
  -nostandard-realloc-lhs -align array32byte

- **Benchmarks using both Fortran and C:**
  -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
  -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
  -nostandard-realloc-lhs -align array32byte

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags
<table>
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<tr>
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<th>Flags and Options</th>
</tr>
</thead>
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<tr>
<td>619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP</td>
<td></td>
</tr>
<tr>
<td>638.imagick_s: basepeak = yes</td>
<td></td>
</tr>
<tr>
<td>644.nab_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP</td>
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<tr>
<td>649.fotonik3d_s: basepeak = yes</td>
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<td>654.roms_s: Same as 603.bwaves_s</td>
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<tr>
<td>Benchmarks using both Fortran and C:</td>
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<tr>
<td>621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte</td>
<td></td>
</tr>
<tr>
<td>627.cam4_s: basepeak = yes</td>
<td></td>
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<td>628.pop2_s: Same as 621.wrf_s</td>
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Peak Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-D120h-RevA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-D120h-RevA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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