**Huawei CH225 V5 (Intel Xeon Silver 4110)**

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2018  
**Test Date:** Sep-2018

**SPECrater2017_int_base = 74.0**  
**SPECrater2017_int_peak = 78.6**

### Hardware
- **CPU Name:** Intel Xeon Silver 4110  
  - **Max MHz.:** 3000  
  - **Nominal:** 2100  
  - **Enabled:** 16 cores, 2 chips, 2 threads/core  
  - **Orderable:** 1,2 chips  
  - **Cache L1:** 32 KB I + 32 KB D on chip per core  
  - **L2:** 1 MB I+D on chip per core  
  - **L3:** 11 MB I+D on chip per chip  
  - **Other:** None  
- **Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)  
- **Storage:** 1 x 1200 GB SAS, 10000 RPM  
- **Other:** None

### Software
- **OS:** Red Hat Enterprise Linux Server release 7.4 (Maipo)  
  - **Compiler:** C/C++: Version 18.0.2.199 of Intel C/C++  
  - **Compiler for Linux:** Fortran: Version 18.0.2.199 of Intel Fortran  
  - **File System:** xfs  
  - **System State:** Run level 3 (multi-user)  
  - **Base Pointers:** 64-bit  
  - **Peak Pointers:** 32/64-bit  
  - **Other:** jemalloc memory allocator V5.0.1
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
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<td>500.perlbench_r</td>
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<td>917</td>
<td>55.6</td>
<td>917</td>
<td>55.5</td>
<td>915</td>
<td>55.7</td>
</tr>
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<td>502.gcc_r</td>
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<td>66.4</td>
<td>683</td>
<td>66.3</td>
<td>683</td>
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</tr>
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<td>505.mcf_r</td>
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<td>92.9</td>
<td>562</td>
<td>92.1</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>32</td>
<td>823</td>
<td>51.0</td>
<td>822</td>
<td>51.1</td>
<td>820</td>
<td>51.2</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>32</td>
<td>439</td>
<td>77.0</td>
<td>439</td>
<td>77.0</td>
<td>440</td>
<td>76.8</td>
</tr>
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<td>525.x264_r</td>
<td>32</td>
<td>426</td>
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<td>422</td>
<td>133</td>
<td>427</td>
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<tr>
<td>531.deepsjeng_r</td>
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<td>64.5</td>
<td>568</td>
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<td>64.5</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>32</td>
<td>926</td>
<td>57.2</td>
<td>926</td>
<td>57.2</td>
<td>924</td>
<td>57.4</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>32</td>
<td>626</td>
<td>134</td>
<td>625</td>
<td>134</td>
<td>626</td>
<td>134</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>32</td>
<td>619</td>
<td>55.8</td>
<td>616</td>
<td>56.1</td>
<td>617</td>
<td>56.0</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)
SPEC CPU2017 Integer Rate Result

Huawei
Huawei CH225 V5 (Intel Xeon Silver 4110)

| SPECrate2017_int_base | 74.0 |
| SPECrate2017_int_peak | 78.6 |

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Spec CPU2017 Integer Rate Result

Huawei CH225 V5 (Intel Xeon Silver 4110) SPECrate2017_int_base = 74.0 SPECrate2017_int_peak = 78.6

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General Notes (Continued)
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS configuration:
Power Policy Set to Performance
XPT Prefetch Set to Enabled
Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on localhost.localdomain Mon Sep 3 18:06:05 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4110 CPU @ 2.10GHz
 2 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4110 CPU @ 2.10GHz
Stepping: 4
CPU MHz: 2100.000
BogoMIPS: 4200.00
Virtualization: VT-x

(Continued on next page)
Huawei

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Platform Notes (Continued)

L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 11264K
NUMA node0 CPU(s): 0-7,16-23
NUMA node1 CPU(s): 8-15,24-31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nop1 xtopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 fma cx16 xtrypdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx
f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 invpcid_single intel_pt
spec_ctrl ibpb_support tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust
bm1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx
snap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc
cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts

/doc/cpuintelinfo cache data

cache size : 11264 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
node 0 size: 391349 MB
node 0 free: 382086 MB
node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
node 1 size: 393216 MB
node 1 free: 381265 MB
node distances:
node 0 1
0: 10 21
1: 21 10

From /proc/meminfo

MemTotal: 790510872 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.4 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"

(Continued on next page)
Huawei

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Platform Notes (Continued)

VERSION_ID="7.4"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 3 18:04

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.80 06/27/2018
Memory:
24x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

====================================================================================================================================================================
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
557.xz_r(base)
====================================================================================================================================================================
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
====================================================================================================================================================================
CC 500.perlbench_r(peak) 502.gcc_r(peak) 505.mcf_r(peak) 525.x264_r(peak)
557.xz_r(peak)
====================================================================================================================================================================
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
====================================================================================================================================================================
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)

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### Huawei

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### Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Compilation Flags</th>
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</thead>
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<tr>
<td>541.leela_r(base)</td>
<td></td>
</tr>
<tr>
<td>icpc (ICC) 18.0.2 20180210</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
<tr>
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<td>523.xalancbmk_r(peak)</td>
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<tr>
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<td></td>
</tr>
<tr>
<td>FC 548.exchange2_r(base)</td>
<td></td>
</tr>
<tr>
<td>ifort (IFORT) 18.0.2 20180210</td>
<td></td>
</tr>
<tr>
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<td></td>
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<td></td>
</tr>
</tbody>
</table>

### Base Compiler Invocation

- **C benchmarks:**
  - `icc -m64 -std=c11`

- **C++ benchmarks:**
  - `icpc -m64`

- **Fortran benchmarks:**
  - `ifort -m64`

### Base Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>-DSPEC_LP64 -DSPEC_Linux_X64</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>-DSPEC_LP64</td>
</tr>
</tbody>
</table>

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SPEC CPU2017 Integer Rate Result
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Base Portability Flags (Continued)
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags
C benchmarks:
-W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Compiler Invocation
C benchmarks (except as noted below):
icc -m64 -std=c11

502.gcc_r: icc -m32 -std=c11 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):
icpc -m64

523.xalancbmk_r: icpc -m32 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64
## SPEC CPU2017 Integer Rate Result

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### Huawei

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### Peak Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
- 502.gcc_r: -D_FILE_OFFSET_BITS=64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

### Peak Optimization Flags

#### C benchmarks:

- 500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

- 502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

- 505.mcf_r: basepeak = yes

- 525.x264_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-alias -L/usr/local/je5.0.1-64/lib -ljemalloc

- 557.xz_r: basepeak = yes

#### C++ benchmarks:

- 520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

- 523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

- 531.deepsjeng_r: basepeak = yes

(Continued on next page)
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Peak Optimization Flags (Continued)

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-09-03 18:06:04-0400.
Originally published on 2018-10-02.