## SPEC® CPU2017 Integer Speed Result

**Dell Inc.**  
PowerEdge MX740c (Intel Xeon Gold 6144, 3.50GHz)  

<table>
<thead>
<tr>
<th>Test Sponsor</th>
<th>Dell Inc.</th>
<th>Copyright 2017-2018 Standard Performance Evaluation Corporation</th>
</tr>
</thead>
</table>
| Tested by    | Dell Inc. | Test Date: Mar-2018  
Hardware Availability: Sep-2018  
Software Availability: Feb-2018 |

### CPU2017 License: 55  
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### Software

<table>
<thead>
<tr>
<th>Software</th>
<th>Details</th>
</tr>
</thead>
</table>
| OS         | SUSE Linux Enterprise Server 12 SP3  
4.4.114-94.11-default |
| Compiler   | C/C++: Version 18.0.0.128 of Intel C/C++  
Fortran: Version 18.0.0.128 of Intel Fortran |
| Parallel   | Yes |
| Firmware   | Version 0.3.12 released Feb-2018 |
| File System | xfs |
| System State | Run level 3 (multi-user) |
| Base Pointers | 64-bit |
| Peak Pointers | 32/64-bit |
| Other       | jemalloc memory allocator library V5.0.1 |

### Hardware

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Details</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Gold 6144</td>
</tr>
<tr>
<td>Max MHz.</td>
<td>4200</td>
</tr>
<tr>
<td>Nominal</td>
<td>3500</td>
</tr>
<tr>
<td>Enabled</td>
<td>16 cores, 2 chips</td>
</tr>
<tr>
<td>Orderable</td>
<td>1,2 chips</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3</td>
<td>24.75 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Memory</td>
<td>192 GB (12 x 16 GB 2Rx8 PC4-2666V-R)</td>
</tr>
<tr>
<td>Storage</td>
<td>960 GB SAS SSD</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

### Threads

<table>
<thead>
<tr>
<th>Spec</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench_s</td>
<td>6.94</td>
</tr>
<tr>
<td>gcc_s</td>
<td>6.94</td>
</tr>
<tr>
<td>mcf_s</td>
<td>6.94</td>
</tr>
<tr>
<td>omnetpp_s</td>
<td>6.94</td>
</tr>
<tr>
<td>xalancbmk_s</td>
<td>6.94</td>
</tr>
<tr>
<td>x264_s</td>
<td>6.94</td>
</tr>
<tr>
<td>deepsjeng_s</td>
<td>6.94</td>
</tr>
<tr>
<td>leela_s</td>
<td>6.94</td>
</tr>
<tr>
<td>exchange2_s</td>
<td>6.94</td>
</tr>
<tr>
<td>xz_s</td>
<td>6.94</td>
</tr>
</tbody>
</table>

### Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>8.29</td>
</tr>
<tr>
<td>gcc</td>
<td>10.4</td>
</tr>
<tr>
<td>mcf</td>
<td>11.8</td>
</tr>
<tr>
<td>omnetpp</td>
<td>6.61</td>
</tr>
<tr>
<td>xalancbmk</td>
<td>10.5</td>
</tr>
<tr>
<td>x264</td>
<td>13.0</td>
</tr>
<tr>
<td>deepsjeng</td>
<td>5.60</td>
</tr>
<tr>
<td>leela</td>
<td>4.91</td>
</tr>
<tr>
<td>exchange2</td>
<td>15.1</td>
</tr>
<tr>
<td>xz</td>
<td>21.6</td>
</tr>
</tbody>
</table>

**SPECspeed2017_int_base = 9.62**  
**SPECspeed2017_int_peak = 9.91**
SPEC CPU2017 Integer Speed Result

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PowerEdge MX740c (Intel Xeon Gold 6144, 3.50GHz)

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Software Availability: Feb-2018
Hardware Availability: Sep-2018
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CPU2017 License: 55

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Baseline Threads</th>
<th>Baseline Seconds</th>
<th>Baseline Ratio</th>
<th>Baseline Seconds</th>
<th>Baseline Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>16</td>
<td>256</td>
<td>6.94</td>
<td>254</td>
<td>6.98</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>16</td>
<td>394</td>
<td>10.1</td>
<td>397</td>
<td>10.0</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>16</td>
<td>402</td>
<td>11.7</td>
<td>390</td>
<td>12.1</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>16</td>
<td>257</td>
<td>6.33</td>
<td>247</td>
<td>6.61</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>16</td>
<td>134</td>
<td>10.6</td>
<td>134</td>
<td>10.5</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>16</td>
<td>135</td>
<td>15.1</td>
<td>195</td>
<td>15.1</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>16</td>
<td>256</td>
<td>10.5</td>
<td>256</td>
<td>10.5</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>16</td>
<td>347</td>
<td>4.91</td>
<td>347</td>
<td>4.92</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>16</td>
<td>195</td>
<td>15.1</td>
<td>195</td>
<td>15.1</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>16</td>
<td>288</td>
<td>21.5</td>
<td>285</td>
<td>21.7</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
jemalloc: sources available via jemalloc.net

Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3>     /proc/sys/vm/drop_caches
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**Platform Notes**

BIOS settings:  
Sub NUMA Cluster Disabled  
Virtualization Technology Disabled  
System Profile set to Custom  
CPU Performance set to Maximum Performance  
C States set to Autonomous  
C1EE Disabled  
Uncore Frequency set to Dynamic  
Energy Efficiency Policy set to Performance  
Memory Patrol Scrub Disabled  
Logical Processor Disabled  
CPU Interconnect Bus Link Power Management Disabled  
PCI ASPM L1 Link Power Management Disabled  
Sysinfo program /root/cpu2017/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on linux-kuth Tue Mar 6 05:36:40 2018

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
  model name : Intel(R) Xeon(R) Gold 6144 CPU @ 3.50GHz  
    2 "physical id"s (chips)  
    16 "processors"  
    cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
    cpu cores : 8  
    siblings : 8  
    physical 0: cores 0 2 3 9 16 19 26 27  
    physical 1: cores 0 2 3 9 16 19 26 27

From lscpu:  
  Architecture: x86_64  
  CPU op-mode(s): 32-bit, 64-bit  
  Byte Order: Little Endian  
  CPU(s): 16  
  On-line CPU(s) list: 0-15  
  Thread(s) per core: 1  
  Core(s) per socket: 8  
  Socket(s): 2  
  NUMA node(s): 2  
  Vendor ID: GenuineIntel  
  CPU family: 6  
  Model: 85  
  Model name: Intel(R) Xeon(R) Gold 6144 CPU @ 3.50GHz  
  Stepping: 4

(Continued on next page)
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---

### Platform Notes (Continued)

- **CPU MHz:** 3491.767
- **BogoMIPS:** 6983.53
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 25344K
- **NUMA node0 CPU(s):** 0, 2, 4, 6, 8, 10, 12, 14
- **NUMA node1 CPU(s):** 1, 3, 5, 7, 9, 11, 13, 15
- **Flags:**
  - fpu
  - vme
  - pse
  - tsc
  - msr
  - pae
  - mce
  - cx8
  - apic
  - sep
  - mtrr
  - pge
  - mca
  - cmov
  - pat
  - pse36
  - clflush
  - dts
  - acpi
  - mmx
  - fxsr
  - sse
  - sse2
  - ss
  - ht
  - tm
  - pbe
  - syscall
  - nx
  - pdpe1gb
  - rdtscp
  - lm
  - constant_tsc
  - arch_perfmon
  - pebs
  - rep_good
  - nonstop_tsc
  - aperfmperf
  - eagerfpu
  - pni
  - pclmulqdq
  - dtes64
  - monitor
  - ds_cpl
  - vmx
  - smx
  - est
  - tm
  - pbe
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  - smx
  - est
  - tm
  - pbe
  - syscall
  - nx
  - pdpe1gb
  - rdtscp
  - lm
  - constant_tsc

- From /proc/cpuinfo cache data
  - cache size : 25344 KB

- From numactl --hardware
  - available: 2 nodes (0-1)
  - node 0 cpus: 0 2 4 6 8 10 12 14
  - node 0 size: 95354 MB
  - node 0 free: 94682 MB
  - node 1 cpus: 1 3 5 7 9 11 13 15
  - node 1 size: 96749 MB
  - node 1 free: 96242 MB

- From /proc/meminfo
  - MemTotal: 196715324 kB
  - HugePages_Total: 0
  - Hugepagesize: 2048 kB

- From /usr/bin/lsb_release -d
  - SUSE Linux Enterprise Server 12 SP3

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</table>

---

**Platform Notes (Continued)**

SUSE Linux Enterprise Server 12 (x86_64)

VERSION = 12
PATCHLEVEL = 3

# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

```
os-release:
    NAME="SLES"
    VERSION="12-SP3"
    VERSION_ID="12.3"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```
uname -a:
    Linux linux-kuth 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
    x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Mar 6 05:34
```

SPEC is set to: /root/cpu2017

```
Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda2      xfs   890G   16G  874G   2% /
```

Additional information from dmidecode follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.3.12 02/06/2018
Memory:
12x 00AD063200AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666
12x Not Specified Not Specified

(End of data from sysinfo program)

---

**Compiler Version Notes**

```
CC  600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)
```

```
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

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CC 600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
641.leela_s(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 620.omnetpp_s(peak) 623.xalancbmk_s(peak) 631.deepsjeng_s(peak)
641.leela_s(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 648.exchange2_s(base, peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64

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#### Base Portability Flags (Continued)

- 602.gcc_s: -DSPEC_LP64
- 605.mcf_s: -DSPEC_LP64
- 620.omnetpp_s: -DSPEC_LP64
- 623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
- 625.x264_s: -DSPEC_LP64
- 631.deepsjeng_s: -DSPEC_LP64
- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64

#### Base Optimization Flags

**C benchmarks:**

- Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
- L/usr/local/je5.0.1-64/lib -ljemalloc

**C++ benchmarks:**

- Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

**Fortran benchmarks:**

- Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
- L/usr/local/je5.0.1-64/lib -ljemalloc

#### Base Other Flags

**C benchmarks:**

- m64 -std=c11

**C++ benchmarks:**

- m64

**Fortran benchmarks:**

- m64
### Peak Compiler Invocation

<table>
<thead>
<tr>
<th>C benchmarks:</th>
<th>icc</th>
</tr>
</thead>
<tbody>
<tr>
<td>C++ benchmarks:</td>
<td>icpc</td>
</tr>
<tr>
<td>Fortran benchmarks:</td>
<td>ifort</td>
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### Peak Portability Flags

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<td>600.perlbmk_s: -DSPEC_LP64 -DSPEC_LINUX_X64</td>
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<tr>
<td>602.gcc_s: -DSPEC_LP64</td>
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<td>620.omnetpp_s: -DSPEC_LP64</td>
</tr>
<tr>
<td>623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX</td>
</tr>
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<td>625.x264_s: -DSPEC_LP64</td>
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<tr>
<td>600.perlbmk_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -fno-strict-overflow -L/usr/local/je5.0.1-64/lib -ljemalloc</td>
</tr>
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<td>602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc</td>
</tr>
<tr>
<td>605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -qopt-mem-layout-trans=3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc</td>
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### Peak Optimization Flags (Continued)

**625.x264_s**: `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -gopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc`

**657.xz_s**: Same as 602.gcc_s

**C++ benchmarks:**

- **620.omnetpp_s**: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc`


- **631.deepsjeng_s**: Same as 620.omnetpp_s

- **641.leela_s**: Same as 620.omnetpp_s

**Fortran benchmarks:**


### Peak Other Flags

**C benchmarks:**

- `-m64 -std=c11`

**C++ benchmarks (except as noted below):**

- `-m64`

- **623.xalancbmk_s**: `-m32`

**Fortran benchmarks:**

- `-m64`
SPEC CPU2017 Integer Speed Result

Dell Inc.
PowerEdge MX740c (Intel Xeon Gold 6144, 3.50GHz)

SPECspeed2017_int_base = 9.62
SPECspeed2017_int_peak = 9.91

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Mar-2018
Hardware Availability: Sep-2018
Software Availability: Feb-2018

The flags files that were used to format this result can be browsed at:

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-03-05 16:36:39-0500.
Originally published on 2018-10-16.