SPEC® CPU2017 Integer Rate Result

Supermicro
SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4109T)

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Sep-2018
Hardware Availability: Jul-2017
Software Availability: Mar-2018

SPECrate2017_int_base = 70.4
SPECrate2017_int_peak = 74.8

### Hardware
- **CPU Name:** Intel Xeon Silver 4109T
- **Max MHz.:** 3000
- **Nominal:** 2000
- **Enabled:** 16 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I-D on chip per core
- **L3:** 11 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)
- **Storage:** 1 x 200 GB SATA III SSD
- **Other:** None

### Software
- **OS:** SUSE Linux Enterprise Server 12 SP3 (x86_64)
- **Kernel:** 4.4.114-94.11-default
- **Compiler:** C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux;
  Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux
- **Parallel:** No
- **Firmware:** Supermicro BIOS version 2.1a released Aug-2018
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 32/64-bit
- **Other:** jemalloc memory allocator library V5.0.1
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**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>SEC Base</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>SEC Peak</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>32</td>
<td>963</td>
<td>52.9</td>
<td>961</td>
<td>53.0</td>
<td>960</td>
<td>53.1</td>
<td>32</td>
<td>784</td>
<td>65.0</td>
<td>778</td>
<td>65.5</td>
<td>784</td>
<td>65.0</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>32</td>
<td>739</td>
<td>61.3</td>
<td>724</td>
<td>62.6</td>
<td>721</td>
<td>62.8</td>
<td>32</td>
<td>619</td>
<td>73.3</td>
<td>619</td>
<td>73.2</td>
<td>621</td>
<td>73.0</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>32</td>
<td>594</td>
<td>87.0</td>
<td>576</td>
<td>89.8</td>
<td>586</td>
<td>88.3</td>
<td>32</td>
<td>594</td>
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</tr>
<tr>
<td>520.omnetpp_r</td>
<td>32</td>
<td>911</td>
<td>46.1</td>
<td>893</td>
<td>47.0</td>
<td>880</td>
<td>47.7</td>
<td>32</td>
<td>911</td>
<td>46.1</td>
<td>893</td>
<td>47.0</td>
<td>880</td>
<td>47.7</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>32</td>
<td>493</td>
<td>68.5</td>
<td>472</td>
<td>71.6</td>
<td>462</td>
<td>73.2</td>
<td>32</td>
<td>378</td>
<td>89.5</td>
<td>376</td>
<td>89.9</td>
<td>376</td>
<td>89.8</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>32</td>
<td>427</td>
<td>131</td>
<td>428</td>
<td>131</td>
<td>424</td>
<td>132</td>
<td>32</td>
<td>427</td>
<td>131</td>
<td>428</td>
<td>131</td>
<td>424</td>
<td>132</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>32</td>
<td>601</td>
<td>61.0</td>
<td>601</td>
<td>61.1</td>
<td>600</td>
<td>61.1</td>
<td>32</td>
<td>601</td>
<td>61.0</td>
<td>601</td>
<td>61.1</td>
<td>600</td>
<td>61.1</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>32</td>
<td>955</td>
<td>55.5</td>
<td>955</td>
<td>55.4</td>
<td>956</td>
<td>55.4</td>
<td>32</td>
<td>929</td>
<td>57.0</td>
<td>939</td>
<td>56.4</td>
<td>944</td>
<td>56.1</td>
</tr>
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<td>32</td>
<td>644</td>
<td>130</td>
<td>645</td>
<td>130</td>
<td>644</td>
<td>130</td>
<td>32</td>
<td>644</td>
<td>130</td>
<td>645</td>
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<td>32</td>
<td>666</td>
<td>51.9</td>
<td>657</td>
<td>52.6</td>
<td>658</td>
<td>52.5</td>
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</tbody>
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---

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

---

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

---

**General Notes**

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM  
Memory using Redhat Enterprise Linux 7.5  
Transparent Huge Pages enabled by default

Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
```
sync; echo 3 > /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:  
```
numactl --interleave=all runcpu <etc>
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)
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General Notes (Continued)

is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
LLC prefetch = Enable
Power Technology = Custom
Power Performance Tuning = BIOS Controls EPB
ENERGY_PERF_BIAS_CFG mode = Extreme Performance
Hardware P-state = Out of Band Mode
XPT Prefetch = Enable
Stale AtoS = Enable
LLC dead line alloc = Disable
SDDC Plus One = Disable
ADDCC Sparing = Disable
Patrol Scrub = Disable
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-ima8 Fri Sep 14 14:34:22 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4109T CPU @ 2.00GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 8

(Continued on next page)
Platform Notes (Continued)

Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4109T CPU @ 2.00GHz
Stepping: 4
CPU MHz: 1999.995
BogoMIPS: 3999.99
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 11264K
NUMA node0 CPU(s): 0-7,16-23
NUMA node1 CPU(s): 8-15,24-31
Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpre pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts dtherm intel_pt rsb_ctxsw spec_ctrl retprobe kaiser tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ersed msinvcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc pku ospke

From /proc/cpuinfo cache data
  cache size : 11264 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
  node 0 size: 193038 MB
  node 0 free: 192639 MB
  node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
  node 1 size: 193516 MB
  node 1 free: 193176 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10

From /proc/meminfo
  MemTotal: 395832880 kB
  HugePages_Total: 0

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### Platform Notes (Continued)

- **Hugepagesize:** 2048 kB
- **From /etc/*release*/etc/*version*/
  - **SuSE-release:**
    - SUSE Linux Enterprise Server 12 (x86_64)  
    - VERSION = 12  
    - PATCHLEVEL = 3
    - # This file is deprecated and will be removed in a future service pack or release.  
    - # Please check /etc/os-release for details about this release.
  - **os-release:**
    - NAME="SLES"  
    - VERSION="12-SP3"  
    - VERSION_ID="12.3"
    - PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
    - ID="sles"
    - ANSI_COLOR="0;32"
    - CPE_NAME="cpe:/o:suse:sles:12:sp3"
  - **uname -a:**
    - Linux linux-ima8 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)  
    - x86_64 x86_64 x86_64 GNU/Linux

- **Kernel self-reported vulnerability status:**
  - **CVE-2017-5754 (Meltdown):** Mitigation: PTI
  - **CVE-2017-5753 (Spectre variant 1):** Mitigation: Barriers
  - **CVE-2017-5715 (Spectre variant 2):** Mitigation: IBRS+IBPB

- **run-level:** 3 Sep 14 14:14

- **SPEC is set to:** /home/cpu2017
  - **Filesystem**  
    - **Type**  
    - **Size**  
    - **Used**  
    - **Avail**  
    - **Use%**  
    - **Mounted on**  
  - /dev/sda4  
    - xfs  
    - 145G  
    - 4.0G  
    - 141G  
    - 3%  
    - /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- **BIOS American Megatrends Inc. 2.1a 08/23/2018**
- **Memory:**
  - 12x NO DIMM NO DIMM
  - 12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)
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Compiler Version Notes

==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)  
557.xz_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC  500.perlbench_r(peak) 502.gcc_r(peak) 505.mcf_r(peak) 525.x264_r(peak)  
557.xz_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)  
541.leela_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)  
541.leela_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC  548.exchange2_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC  548.exchange2_r(peak)
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Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc
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Peak Compiler Invocation

C benchmarks (except as noted below):
  icc -m64 -std=c11

  502.gcc_r: icc -m32 -std=c11 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):
  icpc -m64

  523.xalancbmk_r: icpc -m32 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin

Fortran benchmarks:
  ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.onetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
  500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
  -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
  -fno-strict-overflow -L/usr/local/je5.0.1-64/lib
  -ljemalloc

  502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
  -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
  -L/usr/local/je5.0.1-32/lib -ljemalloc

  505.mcf_r: basepeak = yes

(Continued on next page)
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Peak Optimization Flags (Continued)

525.x264_r: basepeak = yes
557.xz_r: basepeak = yes

C++ benchmarks:
520.omnetpp_r: basepeak = yes
523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc
531.deepsjeng_r: basepeak = yes
541.leela_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revD.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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