## SPEC® CPU2017 Integer Rate Result

**Supermicro**

SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Silver 4109T)

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>35.2</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_int_peak</td>
<td>37.4</td>
</tr>
</tbody>
</table>

### CPU2017 License:
001176

**Test Sponsor:** Supermicro

**Test Date:** Sep-2018

**Hardware Availability:** Jul-2017

**Software Availability:** Mar-2018

### Hardware

<table>
<thead>
<tr>
<th>CPU Name</th>
<th>Intel Xeon Silver 4109T</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz.</td>
<td>3000</td>
</tr>
<tr>
<td>Nominal</td>
<td>2000</td>
</tr>
<tr>
<td>Enabled</td>
<td>8 cores, 1 chip, 2 threads/core</td>
</tr>
<tr>
<td>Orderable</td>
<td>1 chip</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Cache L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>Cache L3</td>
<td>11 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>192 GB (6 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x 200 GB SATA III SSD</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>OS</th>
<th>SUSE Linux Enterprise Server 12 SP3 (x86_64)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler</td>
<td>C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux: Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Parallel</td>
<td>No</td>
</tr>
<tr>
<td>Firmware</td>
<td>Supermicro BIOS version 2.1 released Jun-2018</td>
</tr>
<tr>
<td>File System</td>
<td>xfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>jemalloc memory allocator library V5.0.1</td>
</tr>
</tbody>
</table>

### Test Details

<table>
<thead>
<tr>
<th>Test Date</th>
<th>Sep-2018</th>
</tr>
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<tbody>
<tr>
<td>Test Sponsor</td>
<td>Supermicro</td>
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<tr>
<td>Hardware Availability</td>
<td>Jul-2017</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Mar-2018</td>
</tr>
</tbody>
</table>

### Benchmarks

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate2017_int_base</th>
<th>SPECrate2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench_r</td>
<td>16</td>
<td>27.4</td>
<td>27.4</td>
</tr>
<tr>
<td>gcc_r</td>
<td>16</td>
<td>32.8</td>
<td>36.5</td>
</tr>
<tr>
<td>mcf_r</td>
<td>16</td>
<td>44.7</td>
<td>41.6</td>
</tr>
<tr>
<td>omnetpp_r</td>
<td>16</td>
<td>23.5</td>
<td>33.5</td>
</tr>
<tr>
<td>xalancbmk_r</td>
<td>16</td>
<td>68.7</td>
<td>71.0</td>
</tr>
<tr>
<td>x264_r</td>
<td>16</td>
<td>30.5</td>
<td>30.5</td>
</tr>
<tr>
<td>deepsjeng_r</td>
<td>16</td>
<td>28.3</td>
<td>28.3</td>
</tr>
<tr>
<td>leela_r</td>
<td>16</td>
<td>64.5</td>
<td>64.5</td>
</tr>
<tr>
<td>exchange2_r</td>
<td>16</td>
<td>25.5</td>
<td>25.5</td>
</tr>
</tbody>
</table>
### SPEC CPU2017 Integer Rate Result

**Supermicro**

SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Silver 4109T)

**SPEC CPU2017 License:** 001176

**Test Sponsor:** Supermicro

**Test Date:** Sep-2018

**Tested by:** Supermicro

**Hardware Availability:** Jul-2017

**Software Availability:** Mar-2018

---

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>16</td>
<td>931</td>
<td>27.4</td>
<td>931</td>
<td>27.3</td>
<td>928</td>
<td>27.5</td>
<td>16</td>
<td>780</td>
<td>32.7</td>
<td>777</td>
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<tr>
<td>502.gcc_r</td>
<td>16</td>
<td>732</td>
<td>30.9</td>
<td>732</td>
<td>31.0</td>
<td>735</td>
<td>30.8</td>
<td>16</td>
<td>621</td>
<td>36.5</td>
<td>621</td>
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<tr>
<td>505.mcf_r</td>
<td>16</td>
<td>579</td>
<td>44.7</td>
<td>578</td>
<td>44.7</td>
<td>578</td>
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<td>579</td>
<td>44.7</td>
<td>578</td>
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<tr>
<td>520.omnetpp_r</td>
<td>16</td>
<td>895</td>
<td>23.5</td>
<td>892</td>
<td>23.5</td>
<td>896</td>
<td>23.4</td>
<td>16</td>
<td>895</td>
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<td>892</td>
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<tr>
<td>523.xalancbmk_r</td>
<td>16</td>
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<td>33.5</td>
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<td>33.5</td>
<td>16</td>
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</tr>
<tr>
<td>525.x264_r</td>
<td>16</td>
<td>408</td>
<td>68.6</td>
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<td>68.7</td>
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<td>68.7</td>
<td>16</td>
<td>395</td>
<td>71.0</td>
<td>393</td>
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<tr>
<td>531.deepsjeng_r</td>
<td>16</td>
<td>602</td>
<td>30.4</td>
<td>602</td>
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<td>602</td>
<td>30.5</td>
<td>16</td>
<td>602</td>
<td>30.4</td>
<td>602</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>16</td>
<td>937</td>
<td>28.3</td>
<td>937</td>
<td>28.3</td>
<td>937</td>
<td>28.3</td>
<td>16</td>
<td>922</td>
<td>28.7</td>
<td>922</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>16</td>
<td>650</td>
<td>64.5</td>
<td>650</td>
<td>64.5</td>
<td>651</td>
<td>64.4</td>
<td>16</td>
<td>650</td>
<td>64.5</td>
<td>650</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>16</td>
<td>677</td>
<td>25.5</td>
<td>678</td>
<td>25.5</td>
<td>677</td>
<td>25.5</td>
<td>16</td>
<td>677</td>
<td>25.5</td>
<td>678</td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base = 35.2**

**SPECrate2017_int_peak = 37.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)
Supermicro
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Silver 4109T)

SPECrate2017_int_base = 35.2
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CPU2017 License: 001176
Test Sponsor: Supermicro
Test Date: Sep-2018
Tested by: Supermicro
Hardware Availability: Jul-2017
Software Availability: Mar-2018

General Notes (Continued)

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
LLC prefetch = Enable
Power Technology = Custom
Power Performance Tuning = BIOS Controls EPB
ENERGY_PERF_BIAS_CFG mode = Maximum Performance
Hardware P-state = Out of Band Mode
XPT Prefetch = Enable
Stale AtoS = Enable
LLC dead line alloc = Disable
SDDC Plus One = Disable
ADDDC Sparing = Disable
Patrol Scrub = Disable
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-52ma Fri Sep 21 17:59:34 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4109T CPU @ 2.00GHz
  1 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 1
NUMA node(s): 1
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**Supermicro**

*SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Silver 4109T)*

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**Software Availability:** Mar-2018

### Platform Notes (Continued)

- **Vendor ID:** GenuineIntel  
- **CPU family:** 6  
- **Model:** 85  
- **Model name:** Intel(R) Xeon(R) Silver 4109T CPU @ 2.00GHz  
- **Stepping:** 4  
- **CPU MHz:** 2000.001  
- **BogoMIPS:** 4000.00  
- **Virtualization:** VT-x  
- **L1d cache:** 32K  
- **L1i cache:** 32K  
- **L2 cache:** 1024K  
- **L3 cache:** 11264K  
- **NUMA node0 CPU(s):** 0-15  
- **Flags:** fpu vme de pse ts cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl apic pmx nonstop_tsc cpuid p经 freqm pbe intel_pstate  
- **/proc/cpuinfo cache data**  
  - cache size : 11264 KB

From `numactl --hardware`  
**WARNING:** a numactl 'node' might or might not correspond to a physical chip.  
- available: 1 nodes (0)  
- node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
- node 0 size: 192076 MB  
- node 0 free: 191376 MB  
- node distances:  
  - node 0  
  - 0: 10

From `/proc/meminfo`  
- MemTotal: 196686132 kB  
- HugePages_Total: 0  
- Hugepagesize: 2048 kB

From `/etc/*release* /etc/*version*`  
**SuSE-release:**  
- SUSE Linux Enterprise Server 12 (x86_64)  
  - VERSION = 12  
  - PATCHLEVEL = 3

(Continued on next page)
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Platform Notes (Continued)

# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
   NAME="SLES"
   VERSION="12-SP3"
   VERSION_ID="12.3"
   PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
   ID="sles"
   ANSIColor="0;32"
   CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
   Linux linux-52ma 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

   CVE-2017-5754 (Meltdown): Mitigation: PTI
   CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
   CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Sep 21 17:12

SPEC is set to: /home/cpu2017
   Files  Type  Size  Used  Avail  Use%  Mounted on
   /dev/sda4  xfs  145G  17G  128G  12%  /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS American Megatrends Inc. 2.1 06/15/2018
   Memory:
      2x NO DIMM NO DIMM
      6x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
      557.xz_r(base)
==============================================================================

icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

(Continued on next page)
Supermicro
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Silver 4109T)

SPEC CPU2017 Integer Rate Result
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Compiler Version Notes (Continued)

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   557.xz_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
   541.leela_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
   541.leela_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
FC  548.exchange2_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
FC  548.exchange2_r(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
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---

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

(Continued on next page)
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### Base Compiler Invocation (Continued)

**C++ benchmarks:**

```bash
cpc -m64
```

**Fortran benchmarks:**

```bash
ifort -m64
```

### Base Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

### Base Optimization Flags

**C benchmarks:**

```bash
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

**C++ benchmarks:**

```bash
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

**Fortran benchmarks:**

```bash
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc
```

### Peak Compiler Invocation

**C benchmarks (except as noted below):**

```bash
icc -m64 -std=c11
```

(Continued on next page)
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Peak Compiler Invocation (Continued)

502.gcc_r:icc -m32 -std=c11 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin
C++ benchmarks (except as noted below):
icpc -m64
523.xalancbmk_r icpc -m32 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin
Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc
502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc
505.mcf_r: basepeak = yes
525.x264_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-alias -L/usr/local/je5.0.1-64/lib -ljemalloc

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Peak Optimization Flags (Continued)

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: basepeak = yes

541.leela_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revD.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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