## SPEC® CPU2017 Floating Point Speed Result

**Supermicro**  
SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4108)  

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
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</table>

### Hardware
- **CPU Name:** Intel Xeon Silver 4108  
- **Max MHz.:** 3000  
- **Nominal:** 1800  
- **Enabled:** 16 cores, 2 chips  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 11 MB I+D on chip per chip  
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)  
- **Storage:** 1 x 200 GB SATA III SSD  
- **Other:** None

### Software
- **OS:** SUSE Linux Enterprise Server 12 SP3 (x86_64)  
- **Kernel:** 4.4.114-94.11-default  
- **Compiler:** C/C++: Version 18.0.2.199 of Intel C/C++  
- **Compiler for Linux:**  
  - Fortran: Version 18.0.2.199 of Intel Fortran  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator library V5.0.1

---

**CPU2017 License:** 001176  
**Test Date:** Sep-2018  
**Test Sponsor:** Supermicro  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2018  
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SPEC CPU2017 Floating Point Speed Result

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Supermicro
SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4108)

SPECspeed2017_fp_base = 56.6
SPECspeed2017_fp_peak = 57.6

Results Table

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<th>Benchmark</th>
<th>Threads</th>
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</table>

SPECspeed2017_fp_base = 56.6
SPECspeed2017_fp_peak = 57.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/jem5.0.1-32:/home/cpu2017/jem5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
Supermicro
SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4108)

SPEC speed2017_fp_base = 56.6
SPEC speed2017_fp_peak = 57.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro
Test Date: Sep-2018
Hardware Availability: Jul-2017
Software Availability: Mar-2018

Platform Notes

BIOS Settings:
Hyper-Threading [ALL] = Disable
LLC dead line alloc = Disable
SDDC Plus One = Disable
ADDDC Sparing = Disable
Patrol Scrub = Disable
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcd6f2999c336d1f64985e458599a9
running on linux-ima8 Sat Sep 29 01:49:01 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Silver 4108 CPU @ 1.80GHz
    2 "physical id"s (chips)
    16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 8
  siblings : 8
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  On-line CPU(s) list: 0-15
  Thread(s) per core: 1
  Core(s) per socket: 8
  Socket(s): 2
  NUMA node(s): 2
  Vendor ID: GenuineIntel
  CPU family: 6
  Model: 85
  Model name: Intel(R) Xeon(R) Silver 4108 CPU @ 1.80GHz
  Stepping: 4
  CPU MHz: 1000.000
  CPU max MHz: 1801.0000
  CPU min MHz: 800.0000
  BogoMIPS: 3599.99
  Virtualization: VT-x
  L1d cache: 32K
  L1i cache: 32K

(Continued on next page)
Supermicro
SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4108)

| SPECspeed2017_fp_base | 56.6 |
| SPECspeed2017_fp_peak | 57.6 |

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Platform Notes (Continued)

L2 cache: 1024K
L3 cache: 11264K
NUMA node0 CPU(s): 0-7
NUMA node1 CPU(s): 8-15
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good ntopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epbi invpcid_single pln pts
dtherm intel_pt rsb_ctxiw spec_ctrl retpoline kaiser tpr_shadow invpcid_single pln pts
dtprfcr intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow invpcid_single pln pts
dtprfcr intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow invpcid_single pln pts
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dtprfcr intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow invpcid_single pln pts
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dtprfcr intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow invpcid_single pln pts
dtprfcr intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow invpcid_single pln pts

From /proc/cpuinfo cache data

    cache size : 11264 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

    available: 2 nodes (0-1)
    node 0 cpus: 0 1 2 3 4 5 6 7
    node 0 size: 192095 MB
    node 0 free: 182557 MB
    node 1 cpus: 8 9 10 11 12 13 14 15
    node 1 size: 193517 MB
    node 1 free: 187782 MB
    node distances:
        node   0   1
        0:  10  21
        1:  21  10

From /proc/meminfo

    MemTotal:       394868464 kB
    HugePages_Total:       0
    Hugepagesize:           2048 kB

From /etc/*release* /etc/*version*

    SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 3
        # This file is deprecated and will be removed in a future service pack or release.
        # Please check /etc/os-release for details about this release.
        os-release:
            NAME="SLES"
            VERSION="12-SP3"

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

Supermicro
SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4108)

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SPECspeed2017_fp_base = 56.6
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Platform Notes (Continued)

```plaintext
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```plaintext
uname -a:
Linux linux-ima8 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- CVE-2017-5754 (Meltdown): Mitigation: PTI
- CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
- CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

```plaintext
run-level 3 Sep 27 14:59
```

SPEC is set to: /home/cpu2017

```plaintext
Filesystem    Type  Size  Used Avail Use% Mounted on
/dev/sda4      xfs   145G   42G  103G  30% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- BIOS American Megatrends Inc. 2.1a 08/23/2018
- Memory:
  - 12x NO DIMM NO DIMM
  - 12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| CC 619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak) |
==============================================================================
```
```
```
```
```
```
```
```
Supermicro

SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4108)

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Copyright (C) 1998-2018 Intel Corporation. All rights reserved.

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FC 607.cactuBSSN_s(base, peak)

---

icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

---

FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base, peak)

---

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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FC 603.bwaves_s(peak) 649.fotonik3d_s(peak)

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Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)

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icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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CC 621.wrf_s(peak) 628.pop2_s(peak)

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</table>

### Base Compiler Invocation

C benchmarks:

```bash
icc -m64 -std=c11
```

Fortran benchmarks:

```bash
ifort -m64
```

Benchmarks using both Fortran and C:

```bash
ifort -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```bash
icpc -m64 icc -m64 -std=c11 ifort -m64
```

### Base Portability Flags

- 603.bwaves_s: -DSPEC_LP64
- 607.cactuBSSN_s: -DSPEC_LP64
- 619.lbm_s: -DSPEC_LP64
- 621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl
- 638.imagick_s: -DSPEC_LP64
- 644.nab_s: -DSPEC_LP64
- 649.fotonik3d_s: -DSPEC_LP64
- 654.roms_s: -DSPEC_LP64

### Base Optimization Flags

C benchmarks:

```bash
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -gopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:

```bash
```

Benchmarks using both Fortran and C:

```bash
```

(Continued on next page)
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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: basepeak = yes
638.imagick_s: basepeak = yes
644.nab_s: basepeak = yes

Fortran benchmarks:
603.bwaves_s: basepeak = yes

(Continued on next page)
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SPECspeed2017_fp_base = 56.6
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Peak Optimization Flags (Continued)

649.fotonik3d_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -02 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-qfinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-qopenmp -nostandard-realloc-lhs

654.roms_s: -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3
-qopenmp -nostandard-realloc-lhs

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -02 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs

627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qfinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revD.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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