**Supermicro**

SuperWorkstation 5039A-i (X11SRA, Intel Xeon W-2195)

**SPECrate2017_fp_base = 79.5**

**SPECrate2017_fp_peak = 81.2**

<table>
<thead>
<tr>
<th>CPU2017 License: 001176</th>
<th>Test Date: Sep-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Supermicro</td>
<td>Hardware Availability: Jul-2017</td>
</tr>
<tr>
<td>Tested by: Supermicro</td>
<td>Software Availability: Feb-2018</td>
</tr>
</tbody>
</table>

**Hardware**

<table>
<thead>
<tr>
<th>CPU Name: Intel Xeon W-2195</th>
<th>OS: SUSE Linux Enterprise Server 12 SP3 (x86_64)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHZ.: 4300</td>
<td>Kernel 4.4.114-94.11-default</td>
</tr>
<tr>
<td>Nominal: 2300</td>
<td>Compiler: C/C++: Version 18.0.0.128 of Intel C/C++</td>
</tr>
<tr>
<td>Enabled: 18 cores, 1 chip, 2 threads/core</td>
<td>Compiler for Linux;</td>
</tr>
<tr>
<td>Orderable: 1 chip</td>
<td>Fortran: Version 18.0.0.128 of Intel Fortran</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
<td>Compiler for Linux</td>
</tr>
<tr>
<td>L2: 1 MB I+D on chip per core</td>
<td>Parallel: No</td>
</tr>
<tr>
<td>L3: 24.75 MB I+D on chip per chip</td>
<td>Firmware: Supermicro BIOS version 1.2 released Aug-2018</td>
</tr>
<tr>
<td>Other: None</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2666V-E)</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Storage: 1 x 200 GB SATA III SSD</td>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>Other: None</td>
<td>Peak Pointers: 64-bit</td>
</tr>
</tbody>
</table>

**Software**

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base (79.5)</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak (81.2)</td>
</tr>
</tbody>
</table>
Supermicro
SuperWorkstation 5039A-i (X11SRA , Intel Xeon W-2195)

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Sep-2018
Hardware Availability: Jul-2017
Software Availability: Feb-2018

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>36</td>
<td>2219</td>
<td>163</td>
<td>2220</td>
<td>163</td>
<td>2221</td>
<td>163</td>
<td>36</td>
<td>2215</td>
<td>163</td>
<td>2210</td>
<td>163</td>
<td>2205</td>
<td>164</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>36</td>
<td>568</td>
<td>80.3</td>
<td>569</td>
<td>80.1</td>
<td>569</td>
<td>80.1</td>
<td>36</td>
<td>568</td>
<td>80.3</td>
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<td>80.1</td>
<td>569</td>
<td>80.1</td>
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<tr>
<td>508.namd_r</td>
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<td>448</td>
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<td>448</td>
<td>76.3</td>
<td>449</td>
<td>76.2</td>
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<td>77.2</td>
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<tr>
<td>510.parest_r</td>
<td>36</td>
<td>2273</td>
<td>41.4</td>
<td>2277</td>
<td>41.4</td>
<td>2293</td>
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<td>2277</td>
<td>41.4</td>
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<td>41.1</td>
</tr>
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<td>511.povray_r</td>
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<tr>
<td>519.lbm_r</td>
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<td>963</td>
<td>39.4</td>
<td>961</td>
<td>39.5</td>
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<td>951</td>
<td>39.9</td>
<td>950</td>
<td>40.0</td>
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<tr>
<td>521.wrf_r</td>
<td>36</td>
<td>1099</td>
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<td>1100</td>
<td>73.3</td>
<td>1099</td>
<td>73.3</td>
<td>36</td>
<td>1099</td>
<td>73.4</td>
<td>1100</td>
<td>73.3</td>
<td>1099</td>
<td>73.3</td>
</tr>
<tr>
<td>526.blender_r</td>
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<td>528</td>
<td>104</td>
<td>526</td>
<td>104</td>
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<td>105</td>
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<td>105</td>
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<td>105</td>
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<tr>
<td>527.cam4_r</td>
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<td>659</td>
<td>95.5</td>
<td>656</td>
<td>96.0</td>
<td>659</td>
<td>95.5</td>
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<td>646</td>
<td>97.5</td>
<td>647</td>
<td>97.3</td>
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<tr>
<td>538.imagick_r</td>
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<td>547</td>
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<td>546</td>
<td>164</td>
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<td>544.nab_r</td>
<td>36</td>
<td>430</td>
<td>141</td>
<td>427</td>
<td>142</td>
<td>431</td>
<td>141</td>
<td>36</td>
<td>418</td>
<td>145</td>
<td>420</td>
<td>144</td>
<td>416</td>
<td>145</td>
</tr>
<tr>
<td>549.fotonik3D_r</td>
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<td>2876</td>
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<td>2874</td>
<td>48.8</td>
<td>2875</td>
<td>48.8</td>
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<td>49.0</td>
<td>2867</td>
<td>48.9</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>36</td>
<td>1818</td>
<td>31.5</td>
<td>1814</td>
<td>31.5</td>
<td>1815</td>
<td>31.5</td>
<td>36</td>
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<td>32.0</td>
<td>1790</td>
<td>32.0</td>
<td>1792</td>
<td>31.9</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"
CPU frequency governor set with:
cpupower -c all frequency-set -g performance

General Notes
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
**SPEC CPU2017 Floating Point Rate Result**

**Supermicro**

**SuperWorkstation 5039A-i (X11SRA , Intel Xeon W-2195)**

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>79.5</td>
<td>81.2</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 001176  
**Test Date:** Sep-2018  
**Test Sponsor:** Supermicro  
**Hardware Availability:** Jul-2017  
**Tested by:** Supermicro  
**Software Availability:** Feb-2018

---

**General Notes (Continued)**

is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

---

**Platform Notes**

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f  
running on linux-k7zv Fri Sep 14 09:33:29 2018

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

- model name : Intel(R) Xeon(R) W-2195 CPU @ 2.30GHz  
- 1 "physical id"s (chips)  
- 36 "processors"  
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
  - cpu cores : 18  
  - siblings : 36  
  - physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

From lscpu:

- Architecture: x86_64  
- CPU op-mode(s): 32-bit, 64-bit  
- Byte Order: Little Endian  
- CPU(s): 36  
- On-line CPU(s) list: 0-35  
- Thread(s) per core: 2  
- Core(s) per socket: 18  
- Socket(s): 1  
- NUMA node(s): 1  
- Vendor ID: GenuineIntel  
- CPU family: 6  
- Model: 85  
- Model name: Intel(R) Xeon(R) W-2195 CPU @ 2.30GHz  
- Stepping: 4  
- CPU MHz: 1000.000  
- CPU max MHz: 2301.0000  
- CPU min MHz: 1000.0000  
- BogoMIPS: 4607.89  
- Virtualization: VT-x

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Supermicro
SuperWorkstation 5039A-i (X11SRA, Intel Xeon W-2195)

Copyright 2017-2018 Standard Performance Evaluation Corporation

SPECrate2017_fp_base = 79.5
SPECrate2017_fp_peak = 81.2

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Sep-2018
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Platform Notes (Continued)

L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0-35
Flags:

/cache data

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35
node 0 size: 64116 MB
node 0 free: 63521 MB
node distances:
node 0: 0
0: 10

From /proc/meminfo
MemTotal: 65655080 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be remove in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"

(Continued on next page)
### Platform Notes (Continued)

```
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
    Linux linux-k7zv 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
    x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 14 09:30

SPEC is set to: /home/cpu2017
```

```
Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda4      xfs   145G   13G  132G   9% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS* standard.

- **BIOS** American Megatrends Inc. 1.2 08/23/2018
- **Memory:**
  - 4x Micron 18ADF2G72AZ-2G6H1R 16 GB 2 rank 2666
  - 4x NO DIMM NO DIMM

(End of data from sysinfo program)

### Compiler Version Notes

```
==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
CC   519.lbm_r(peak) 544.nab_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
CXXC 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

(Continued on next page)
Supermicro
SuperWorkstation 5039A-i (X11SRA, Intel Xeon W-2195)

SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Supermicro

SPECrate2017_fp_base = 79.5
SPECrate2017_fp_peak = 81.2

CPU2017 License: 001176
Test Sponsor: Supermicro
Test Date: Sep-2018
Tested by: Supermicro
Hardware Availability: Jul-2017
Software Availability: Feb-2018

Compiler Version Notes (Continued)

------------------------------------------------------------------------------
| CXXC 508.namd_r(peak) 510.parest_r(peak) |
| icpc (ICC) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |
------------------------------------------------------------------------------

------------------------------------------------------------------------------
| CC 511.povray_r(base) 526.blender_r(base) |
| icpc (ICC) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |
| icc (ICC) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |
------------------------------------------------------------------------------

------------------------------------------------------------------------------
| CC 511.povray_r(peak) 526.blender_r(peak) |
| icpc (ICC) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |
| icc (ICC) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |
------------------------------------------------------------------------------

------------------------------------------------------------------------------
| FC 507.cactuBSSN_r(base, peak) |
| icpc (ICC) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |
| icc (ICC) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |
| ifort (IFORT) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |
------------------------------------------------------------------------------

------------------------------------------------------------------------------
| FC 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base) |
| ifort (IFORT) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |
------------------------------------------------------------------------------

------------------------------------------------------------------------------
| FC 554.roms_r(peak) |

(Continued on next page)
Supermicro
SuperWorkstation 5039A-i (X11SRA, Intel Xeon W-2195)

SPECrate2017_fp_base = 79.5
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CPU2017 License: 001176
Test Sponsor: Supermicro
Test Date: Sep-2018
Tested by: Supermicro
Hardware Availability: Jul-2017
Software Availability: Feb-2018

Compiler Version Notes (Continued)

```
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.

Cc 521.wrf_r(base, peak) 527.cam4_r(base)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.

Cc 527.cam4_r(peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort
Supermicro
SuperWorkstation 5039A-i (X11SRA , Intel Xeon W-2195)

SPECrate2017_fp_base = 79.5
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CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro
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Hardware Availability: Jul-2017
Software Availability: Feb-2018

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags
Supermicro
SuperWorkstation 5039A-i (X11SRA, Intel Xeon W-2195)

SPECrate2017_fp_base = 79.5
SPECrate2017_fp_peak = 81.2

CPU2017 License: 001176
Test Sponsor: Supermicro
Test Date: Sep-2018
Tested by: Supermicro
Hardware Availability: Jul-2017
Software Availability: Feb-2018

Peak Optimization Flags

C benchmarks:

519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: Same as 519.lbm_r

C++ benchmarks:

508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

510.parest_r: basepeak = yes

Fortran benchmarks:

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:

521.wrf_r: basepeak = yes

527.cam4_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

(Continued on next page)
Supermicro
SuperWorkstation 5039A-i (X11SRA, Intel Xeon W-2195)

<table>
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<tr>
<th>SPECrate2017_fp_base</th>
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CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Sep-2018
Hardware Availability: Jul-2017
Software Availability: Feb-2018

Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r : basepeak = yes

Peak Other Flags

C benchmarks:
- m64 - std=c11

C++ benchmarks:
- m64

Fortran benchmarks:
- m64

Benchmarks using both Fortran and C:
- m64 - std=c11

Benchmarks using both C and C++:
- m64 - std=c11

Benchmarks using Fortran, C, and C++:
- m64 - std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-BSF-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-BSF-revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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