## SPEC® CPU2017 Floating Point Rate Result

**Huawei**

**Huawei XH628 V5 (Intel Xeon Gold 5118)**

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>136</td>
<td>139</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Hardware Availability:** Aug-2018  
**Software Availability:** Mar-2018

### Hardware

<table>
<thead>
<tr>
<th>Program</th>
<th>Copies</th>
<th>SPECrate2017_fp_base</th>
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<tbody>
<tr>
<td>bwaves_r</td>
<td>48</td>
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<tr>
<td>cactuBSSN_r</td>
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<td>cam4_r</td>
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<td>imagick_r</td>
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<td>nab_r</td>
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<td>fotonik3d_r</td>
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<tr>
<td>roms_r</td>
<td>48</td>
<td>68.4</td>
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</tbody>
</table>

### Software

| OS: Red Hat Enterprise Linux Server release 7.4 (Maipo) |
| Compiler: C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux |
| Firmware: No |
| File System: xfs |
| System State: Run level 3 (multi-user) |

### Hardware

| CPU Name: Intel Xeon Gold 5118 |
| Max MHz.: 3200 |
| Nominal: 2300 |
| Enabled: 24 cores, 2 chips, 2 threads/core |
| Orderable: 1.2 chips |
| Cache L1: 32 KB I + 32 KB D on chip per core |
| L2: 1 MB I+D on chip per core |
| L3: 16.5 MB I+D on chip per chip |
| Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2400) |
| Storage: 1 x 1800 GB SAS, 10000 RPM |
| Other: None |
Huawei

Huawei XH628 V5 (Intel Xeon Gold 5118)

SPECrate2017_fp_base = 136
SPECrate2017_fp_peak = 139

Results Table

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<tr>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
Huawei

Huawei XH628 V5 (Intel Xeon Gold 5118)

SPECrate2017_fp_base = 136
SPECrate2017_fp_peak = 139

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

<table>
<thead>
<tr>
<th>Test Date:</th>
<th>Aug-2018</th>
</tr>
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<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Aug-2018</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Mar-2018</td>
</tr>
</tbody>
</table>

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Power Policy Set to Performance
SNC Set to Enabled
IMC Interleaving Set to 1-way Interleave
XPT Prefetch Set to Enabled
Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on localhost.localdomain Wed Aug 15 00:25:55 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5118 CPU @ 2.30GHz
  2 "physical id"s (chips)
  48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
  siblings : 24
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 2
Core(s) per socket: 12
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5118 CPU @ 2.30GHz
Stepping: 4

(Continued on next page)
Platform Notes (Continued)

CPU MHz: 2300.000
BogoMIPS: 4600.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 16896K
NUMA node0 CPU(s): 0-2, 6-8, 24-26, 30-32
NUMA node1 CPU(s): 3-5, 9-11, 27-29, 33-35
NUMA node2 CPU(s): 12-14, 18-20, 36-38, 42-44
NUMA node3 CPU(s): 15-17, 21-23, 39-41, 45-47
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
 lm constant_tsc art arch_perfmon pebs bts rep_good xtopology nonstop_tsc
aperfmperf eagerfpu pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 fma cx16 xtpr
 pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx
 f16c rdrand lahf_lm ablhf lm 3dnowprefetch cpuid cat llc cd ckacmp cmov pat pse36 clflush
 dts mce acpi mtrr pge sse2 sse3 sse4 f16c tsc msr pae mce cx8 3dnow

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 6 7 8 24 25 26 30 31 32
node 0 free: 96437 MB
node 1 cpus: 3 4 5 9 10 11 27 28 29 33 34 35
node 1 size: 98304 MB
node 1 free: 95576 MB
node 2 cpus: 12 13 14 18 19 20 36 37 38 42 43 44
node 2 size: 98304 MB
node 2 free: 95284 MB
node 3 cpus: 15 16 17 21 22 23 39 40 41 45 46 47
node 3 size: 98304 MB
node 3 free: 95632 MB
node distances:
node 0 1 2 3
 0: 10 11 21 21
 1: 11 10 21 21
 2: 21 21 10 11
 3: 21 21 11 10

(Continued on next page)
Huawei

Huawei XH628 V5 (Intel Xeon Gold 5118)

SPECrater2017_fp_base = 136
SPECrater2017_fp_peak = 139

CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Aug-2018
Hardware Availability: Aug-2018
CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Aug-2018
Hardware Availability: Aug-2018

**Platform Notes (Continued)**

From /proc/meminfo
   MemTotal: 394174368 kB
   HugePages_Total: 0
   Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.4 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VARIANT="Server"
      VARIANT_ID="server"
      VERSION_ID="7.4"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
   redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server
uname -a:
   Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 14 14:05

SPEC is set to: /spec2017
   Filesystem Type Size Used Avail Use% Mounted on
   /dev/sda4 xfs 553G 8.2G 545G 2% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS INSYDE Corp. 0.86 08/06/2018
   Memory:
      4x NO DIMM NO DIMM
      12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

**Compiler Version Notes**

==============================================================================
|  CC  | 519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base, peak) |
|------------------------------------------|
| icc (ICC) | 18.0.2 20180210 |
==============================================================================

(Continued on next page)
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Huawei XH628 V5 (Intel Xeon Gold 5118)

SPECrate2017_fp_base = 136
SPECrate2017_fp_peak = 139

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Compiler Version Notes (Continued)

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==============================================================================
CC  519.lbm_r(peak)
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icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CXXC 508.namd_r(base) 510.parest_r(base, peak)
==============================================================================
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC  511.povray_r(base) 526.blender_r(base, peak)
==============================================================================
icpc (ICC) 18.0.2 20180210
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icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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FC  507.cactuBSSN_r(base, peak)
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Huawei XH628 V5 (Intel Xeon Gold 5118)

SPEC CPU2017 Floating Point Rate Result

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Huawei
Huawei XH628 V5 (Intel Xeon Gold 5118)

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Aug-2018
Hardware Availability: Aug-2018
Software Availability: Mar-2018

SPECrate2017_fp_base = 136
SPECrate2017_fp_peak = 139

Compiler Version Notes (Continued)

icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC  503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
==============================================================================
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC   554.roms_r(peak)
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icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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CC   521.wrf_r(peak) 527.cam4_r(peak)
==============================================================================
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

(Continued on next page)
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Tested by: Huawei

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactusBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.ibm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

(Continued on next page)
Huawei
Huawei XH628 V5 (Intel Xeon Gold 5118)

SPECrate\textsubscript{FP}2017 = 136
SPECrate\textsubscript{FP\_peak} = 139

CPU\textsubscript{2017} License: 3175
Test Sponsor: Huawei
Test Date: Aug-2018
Tested by: Huawei
Hardware Availability: Aug-2018
Software Availability: Mar-2018

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
-\texttt{xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only}
-\texttt{-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs}

Benchmarks using both C and C++:
-\texttt{xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only}
-\texttt{-qopt-mem-layout-trans=3}

Benchmarks using Fortran, C, and C++:
-\texttt{xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only}
-\texttt{-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs}

Peak Compiler Invocation

C benchmarks:
\texttt{icc -m64 -std=c11}

C++ benchmarks:
\texttt{icpc -m64}

Fortran benchmarks:
\texttt{ifort -m64}

Benchmarks using both Fortran and C:
\texttt{ifort -m64 icc -m64 -std=c11}

Benchmarks using both C and C++:
\texttt{icpc -m64 icc -m64 -std=c11}

Benchmarks using Fortran, C, and C++:
\texttt{icpc -m64 icc -m64 -std=c11 ifort -m64}

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

(Continued on next page)
Huawei

Huawei XH628 V5 (Intel Xeon Gold 5118)

SPECrate2017_fp_base = 136
SPECrate2017_fp_peak = 139

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Aug-2018
Hardware Availability: Aug-2018
Software Availability: Mar-2018

Peak Optimization Flags (Continued)

519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: Same as 538.imagick_r

C++ benchmarks:

508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

510.parest_r: basepeak = yes

Fortran benchmarks:

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -auto
-nostandard-realloc-lhs

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:

511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

526.blender_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
**Huawei**

**Huawei XH628 V5 (Intel Xeon Gold 5118)**

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The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml

http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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