# SPEC® CPU2017 Integer Rate Result

## Huawei

### Huawei 2288 V5 (Intel Xeon Bronze 3104)

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>SPECrate2017_int_peak</th>
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<tbody>
<tr>
<td>33.7</td>
<td>35.1</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei  
**Test Date:** Jul-2018  
**Hardware Availability:** Sep-2018  
**Software Availability:** Jan-2018

### Hardware

- **CPU Name:** Intel Xeon Bronze 3104  
- **Max MHz.:** 1700  
- **Nominal:** 1700  
- **Enabled:** 12 cores, 2 chips  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 8.25 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2133)  
- **Storage:** 1 x 2000 GB SATA, 7200 RPM  
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux Server release 7.4 (Maipo)  
  3.10.0-693.11.6.el7.x86_64  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++  
  Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran  
  Compiler for Linux  
- **Parallel:** No  
- **Firmware:** Version 0.52 Released Jul-2018  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc: jemalloc memory allocator library V5.0.1;
SPEC CPU2017 Integer Rate Result

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Huawei 2288 V5 (Intel Xeon Bronze 3104)

SPECrate2017_int_base = 33.7

SPECrate2017_int_peak = 35.1

Results Table

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<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Base</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>500.perlbench_r</td>
<td>12</td>
<td>676</td>
<td>28.2</td>
<td>679</td>
<td>28.1</td>
<td>687</td>
<td>27.8</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>12</td>
<td>523</td>
<td>32.5</td>
<td>523</td>
<td>32.5</td>
<td>524</td>
<td>32.4</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>12</td>
<td>496</td>
<td>39.1</td>
<td>496</td>
<td>39.1</td>
<td>496</td>
<td>39.1</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>12</td>
<td>636</td>
<td>24.8</td>
<td>638</td>
<td>24.7</td>
<td>635</td>
<td>24.8</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>12</td>
<td>351</td>
<td>36.1</td>
<td>351</td>
<td>36.1</td>
<td>351</td>
<td>36.1</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>12</td>
<td>348</td>
<td>60.3</td>
<td>349</td>
<td>60.2</td>
<td>348</td>
<td>60.4</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>12</td>
<td>476</td>
<td>28.9</td>
<td>476</td>
<td>28.9</td>
<td>476</td>
<td>28.9</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>12</td>
<td>869</td>
<td>22.9</td>
<td>867</td>
<td>22.9</td>
<td>866</td>
<td>23.0</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>12</td>
<td>481</td>
<td>65.3</td>
<td>482</td>
<td>65.2</td>
<td>483</td>
<td>65.0</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>12</td>
<td>574</td>
<td>22.6</td>
<td>573</td>
<td>22.6</td>
<td>574</td>
<td>22.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
jemalloc: sources available from jemalloc.net or

(Continued on next page)
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CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Power Policy Set to Performance
Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on localhost.localdomain Mon Jul 23 08:37:08 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
  2 "physical id"s (chips)
  12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 6
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Thread(s) per core: 1
Core(s) per socket: 6
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
Stepping: 4

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Huawei 2288 V5 (Intel Xeon Bronze 3104)

**Huawei**

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**Platform Notes (Continued)**

- CPU MHz: 1700.000
- BogoMIPS: 3400.00
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 8448K
- NUMA node0 CPU(s): 0-5
- NUMA node1 CPU(s): 6-11
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 fma cx16 xtpr pdcm pcl id dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 invpcid_single intel_pt spec_ctrl ibpb_support tpr_shadow vnumi flexpriority vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mxr rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xsaveopt xgetbv1 cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm arat pln pts

/proc/cpuinfo cache data

- cache size : 8448 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

- available: 2 nodes (0-1)
- node 0 cpus: 0 1 2 3 4 5
- node 0 size: 194741 MB
- node 0 free: 189974 MB
- node 1 cpus: 6 7 8 9 10 11
- node 1 size: 196608 MB
- node 1 free: 191954 MB
- node distances:
  - node 0 1
  - 0: 10 21
  - 1: 21 10

From /proc/meminfo

- MemTotal: 3941748888 KB
- HugePages_Total: 0
- Hugepagesize: 2048 KB

From /etc/*release* /etc/*version*

- os-release:
  - NAME="Red Hat Enterprise Linux Server"
  - VERSION="7.4 (Maipo)"
  - ID="rhel"

---

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Platform Notes (Continued)

ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 23 08:27

SPEC is set to: /spec2017

Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
---|---|---|---|---|---|---|
/dev/sda3 | xfs | 734G | 79G | 655G | 11% | /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.52 07/18/2018
Memory:
4x NO DIMM NO DIMM
12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2133

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
  525.x264_r(base, peak) 557.xz_r(base, peak)
==============================================================================

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC  500.perlbench_r(peak) 502.gcc_r(peak)
==============================================================================

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
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### Compiler Version Notes (Continued)

```plaintext
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```plaintext
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
541.leela_r(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```plaintext
FC 548.exchange2_r(base, peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

---

### Base Compiler Invocation

- **C benchmarks:** icc
- **C++ benchmarks:** icpc
- **Fortran benchmarks:** ifort

---

### Base Portability Flags

- 500.perlbench_r: -DSPEC_LP64  -DSPEC_LINUX_X64
- 502.gcc_r: -DSPEC_LP64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -DSPEC_LP64  -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64

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### Base Portability Flags (Continued)

- `531.deepsjeng_r`: `-DSPEC_LP64`
- `541.leela_r`: `-DSPEC_LP64`
- `548.exchange2_r`: `-DSPEC_LP64`
- `557.xz_r`: `-DSPEC_LP64`

---

### Base Optimization Flags

C benchmarks:
- `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

C++ benchmarks:
- `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

Fortran benchmarks:
- `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

---

### Base Other Flags

C benchmarks:
- `-m64 -std=c11`

C++ benchmarks:
- `-m64`

Fortran benchmarks:
- `-m64`

---

### Peak Compiler Invocation

C benchmarks:
- `icc`

C++ benchmarks:
- `icpc`

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- **Test Sponsor:** Huawei
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### Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort

### Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

### Peak Optimization Flags

#### C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
-ljemalloc

525.x264_r: basepeak = yes

557.xz_r: Same as 505.mcf_r

#### C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3

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### Peak Optimization Flags (Continued)

520.omnetpp_r (continued):
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

523.xalancbmk_r: `-L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32`
- `-W1,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo`
- `-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3`
- `-L/usr/local/je5.0.1-32/lib -ljemalloc`

531.deepsjeng_r: `basepeak = yes`

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
- `-W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

### Peak Other Flags

**C** benchmarks (except as noted below):
- `-m64 -std=c11`

502.gcc_r: `-m32 -std=c11`

**C++** benchmarks (except as noted below):
- `-m64`

523.xalancbmk_r: `-m32`

Fortran benchmarks:
- `-m64`

The flags files that were used to format this result can be browsed at:
- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html)

You can also download the XML flags sources by saving the following links:
- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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