**SPEC® CPU2017 Floating Point Rate Result**

**Supermicro SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4112)**

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>Test Date</th>
</tr>
</thead>
<tbody>
<tr>
<td>001176</td>
<td>Oct-2018</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Test Sponsor</th>
<th>Tested by</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supermicro</td>
<td>Supermicro</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name:</td>
<td>OS: SUSE Linux Enterprise Server 12 SP3 (x86_64)</td>
</tr>
<tr>
<td>Max MHz.:</td>
<td>Kernel 4.4.114-94.11-default</td>
</tr>
<tr>
<td>Nominal:</td>
<td>Compiler: C/C++: Version 18.0.2.199 of Intel C/C++</td>
</tr>
<tr>
<td>Enabled:</td>
<td>Compiler for Linux; Fortran: Version 18.0.2.199 of Intel Fortran</td>
</tr>
<tr>
<td>Orderable:</td>
<td>Compiler for Linux</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>Parallel: No</td>
</tr>
<tr>
<td>Cache L2:</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>Cache L3:</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Other:</td>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>Memory:</td>
<td>Peak Pointers: 64-bit</td>
</tr>
<tr>
<td>Storage:</td>
<td>Other: None</td>
</tr>
</tbody>
</table>

**SPECrate2017_fp_peak = 56.4**

**SPECrate2017_fp_base = 55.0**
## SPEC CPU2017 Floating Point Rate Result

**Supermicro**

SuperServer 6029U-TR4 (X11DPU , Intel Xeon Silver 4112)

SPECrate2017_fp_base = 55.0

SPECrate2017_fp_peak = 56.4

- **CPU2017 License:** 001176
- **Test Sponsor:** Supermicro
- **Tested by:** Supermicro
- **Test Date:** Oct-2018
- **Hardware Availability:** Jul-2017
- **Software Availability:** Mar-2018

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>16</td>
<td>770</td>
<td>208</td>
<td>771</td>
<td>208</td>
<td>768</td>
<td>209</td>
<td>771</td>
<td>208</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>16</td>
<td>517</td>
<td>39.2</td>
<td>517</td>
<td>39.2</td>
<td>520</td>
<td>38.9</td>
<td>517</td>
<td>39.2</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>16</td>
<td>441</td>
<td>34.5</td>
<td>445</td>
<td>34.1</td>
<td>441</td>
<td>34.4</td>
<td>441</td>
<td>34.5</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>16</td>
<td>1070</td>
<td>39.1</td>
<td>1069</td>
<td>39.2</td>
<td>1074</td>
<td>39.0</td>
<td>1076</td>
<td>39.1</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>16</td>
<td>676</td>
<td>55.3</td>
<td>677</td>
<td>55.2</td>
<td>680</td>
<td>54.9</td>
<td>689</td>
<td>63.4</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>16</td>
<td>453</td>
<td>37.3</td>
<td>455</td>
<td>37.1</td>
<td>453</td>
<td>37.2</td>
<td>455</td>
<td>37.2</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>16</td>
<td>662</td>
<td>54.1</td>
<td>662</td>
<td>54.2</td>
<td>663</td>
<td>54.1</td>
<td>662</td>
<td>55.8</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>16</td>
<td>495</td>
<td>49.2</td>
<td>494</td>
<td>49.3</td>
<td>495</td>
<td>49.3</td>
<td>495</td>
<td>49.3</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>16</td>
<td>720</td>
<td>38.9</td>
<td>706</td>
<td>39.6</td>
<td>711</td>
<td>39.3</td>
<td>693</td>
<td>40.4</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>16</td>
<td>376</td>
<td>106</td>
<td>368</td>
<td>108</td>
<td>368</td>
<td>108</td>
<td>368</td>
<td>108</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>16</td>
<td>355</td>
<td>75.8</td>
<td>358</td>
<td>75.2</td>
<td>354</td>
<td>76.0</td>
<td>354</td>
<td>75.8</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>16</td>
<td>1010</td>
<td>61.7</td>
<td>1018</td>
<td>61.2</td>
<td>1020</td>
<td>61.1</td>
<td>1018</td>
<td>61.2</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>16</td>
<td>710</td>
<td>35.8</td>
<td>721</td>
<td>35.3</td>
<td>715</td>
<td>35.6</td>
<td>701</td>
<td>35.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:
```
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:
```
sync; echo 3> /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:
```
numactl --interleave=all runcpu <etc>
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
Supermicro
SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4112)

SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

SPECrate2017_fp_base = 55.0
SPECrate2017_fp_peak = 56.4

CPU2017 License: 001176
Test Sponsor: Supermicro
Test Date: Oct-2018
Tested by: Supermicro

Hardware Availability: Jul-2017
Software Availability: Mar-2018

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
LLC prefetch = Enable
Power Technology = Custom
Power Performance Tuning = BIOS Controls EPB
ENERGY_PERF_BIAS_CFG mode = Extreme Performance
Hardware P-state = Out of Band Mode
XPT Prefetch = Enable
Stale AtoS = Enable
LLC dead line alloc = Disable
SDDC Plus One = Disable
ADDDC Sparing = Disable
Patrol Scrub = Disable
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-ima8 Tue Oct 9 00:56:58 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name: Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores: 4
siblings: 8
physical 0: cores 1 2 4 5
physical 1: cores 0 2 3 4

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Thread(s) per core: 2

(Continued on next page)
Supermicro
SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4112)

SPECrate2017_fp_base = 55.0
SPECrate2017_fp_peak = 56.4

CPU2017 License: 001176
Test Sponsor: Supermicro
Test Date: Oct-2018
Tested by: Supermicro
Hardware Availability: Jul-2017
Software Availability: Mar-2018

Platform Notes (Continued)

Core(s) per socket: 4
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
Stepping: 4
CPU MHz: 2600.001
BogoMIPS: 5200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 8448K
NUMA node0 CPU(s): 0-3, 8-11
NUMA node1 CPU(s): 4-7, 12-15
Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pclid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm ablv_mce mmx+fma2 popcnt apicid lmx1 Pvcrts mtm mpt msirm pdeaseopt xsaveopt)

From /proc/cpuinfo cache data
  cache size: 8448 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 8 9 10 11
  node 0 size: 193039 MB
  node 0 free: 184909 MB
  node 1 cpus: 4 5 6 7 12 13 14 15
  node 1 size: 193517 MB
  node 1 free: 187128 MB
node distances:
  node 0: 10 21
  node 1: 21 10

From /proc/meminfo
  MemTotal: 395835116 KB

(Continued on next page)
Supermicro
SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4112)

SPEC CPU2017 Floating Point Rate Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

**SPECrate2017_fp_base = 55.0**

**SPECrate2017_fp_peak = 56.4**

<table>
<thead>
<tr>
<th>CPU2017 License: 001176</th>
<th>Test Date: Oct-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Supermicro</td>
<td>Hardware Availability: Jul-2017</td>
</tr>
<tr>
<td>Tested by: Supermicro</td>
<td>Software Availability: Mar-2018</td>
</tr>
</tbody>
</table>

### Platform Notes (Continued)

HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

**SuSE-release:**
- SUSE Linux Enterprise Server 12 (x86_64)
- VERSION = 12
- PATCHLEVEL = 3
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.

**os-release:**
- NAME="SLES"
- VERSION="12-SP3"
- VERSION_ID="12.3"
- PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
- ID="sles"
- ANSI_COLOR="0;32"
- CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
- Linux linux-ima8 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
- CVE-2017-5754 (Meltdown): Mitigation: PTI
- CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
- CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Oct 8 15:16

SPEC is set to: /home/cpu2017

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda4</td>
<td>xfs</td>
<td>145G</td>
<td>44G</td>
<td>101G</td>
<td>31%</td>
<td>/home</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
- BIOS American Megatrends Inc. 2.1a 08/23/2018
- Memory:
  - 12x NO DIMM NO DIMM
  - 12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)
Supermicro

SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4112)

Copyright 2017-2018 Standard Performance Evaluation Corporation

Compiler Version Notes

==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base, peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CC  519.lbm_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CXXC 508.namd_r(base) 510.parest_r(base, peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
CXXC 508.namd_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
CC  511.povray_r(base) 526.blender_r(base, peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
iccc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
CC  511.povray_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
iccc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

(Continued on next page)
Supermicro
SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4112)

SPEC CPU2017 Floating Point Rate Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

SPECrate2017_fp_base = 55.0
SPECrate2017_fp_peak = 56.4

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Oct-2018
Hardware Availability: Jul-2017
Software Availability: Mar-2018

Compiler Version Notes (Continued)

FC 507.cactuBSSN_r(base, peak)

icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

----------------------------------------------------------------------------------
FC 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
----------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

----------------------------------------------------------------------------------
FC 554.roms_r(peak)
----------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

----------------------------------------------------------------------------------
CC 521.wrf_r(base) 527.cam4_r(base)
----------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

----------------------------------------------------------------------------------
CC 521.wrf_r(peak) 527.cam4_r(peak)
----------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Supermicro
SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4112)

SPECrate2017_fp_base = 55.0
SPECrate2017_fp_peak = 56.4

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro
Test Date: Oct-2018
Hardware Availability: Jul-2017
Software Availability: Mar-2018

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

(Continued on next page)
Supermicro
SuperServer 6029U-TR4 (X11DPU , Intel Xeon Silver 4112)

SPECraten2017_fp_base = 55.0
SPECraten2017_fp_peak = 56.4

CPU2017 License: 001176
Test Sponsor: Supermicro
Test Date: Oct-2018
Hardware Availability: Jul-2017
Tested by: Supermicro
Software Availability: Mar-2018

Base Optimization Flags (Continued)

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags
Peak Optimization Flags

C benchmarks:

519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

538.imagick_r: basepeak = yes

544.nab_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

C++ benchmarks:

508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

510.parest_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:

511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

526.blender_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:

(Continued on next page)
# SPEC CPU2017 Floating Point Rate Result

## Supermicro

SuperServer 6029U-TR4 (X11DPU, Intel Xeon Silver 4112)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>55.0</td>
<td>56.4</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro  
**Test Date:** Oct-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2018

---

### Peak Optimization Flags (Continued)

507.cactuBSSN_r: basepeak = yes

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


---

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2018-10-08 12:56:57-0400.  
Originally published on 2018-10-30.