## SPEC® CPU2017 Floating Point Speed Result

Supermicro
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Silver 4114T)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>41.9</td>
<td>42.0</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro  
**Test Date:** Oct-2018  
**Software Availability:** Mar-2018

### Hardware

<table>
<thead>
<tr>
<th><strong>CPU Name</strong></th>
<th>Intel Xeon Silver 4114T</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Max MHz.</strong></td>
<td>3000</td>
</tr>
<tr>
<td><strong>Nominal</strong></td>
<td>2200</td>
</tr>
<tr>
<td><strong>Enabled</strong></td>
<td>10 cores, 1 chip</td>
</tr>
<tr>
<td><strong>Orderable</strong></td>
<td>1 chip</td>
</tr>
<tr>
<td><strong>Cache L1</strong></td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td><strong>Cache L2</strong></td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td><strong>Cache L3</strong></td>
<td>13.75 MB I+D on chip per chip</td>
</tr>
<tr>
<td><strong>Other</strong></td>
<td>None</td>
</tr>
<tr>
<td><strong>Memory</strong></td>
<td>192 GB (6 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)</td>
</tr>
<tr>
<td><strong>Storage</strong></td>
<td>1 x 200 GB SATA III SSD</td>
</tr>
<tr>
<td><strong>Other</strong></td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th><strong>OS</strong></th>
<th>SUSE Linux Enterprise Server 12 SP3 (x86_64)</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Kernel</strong></td>
<td>4.4.114-94.11-default</td>
</tr>
<tr>
<td><strong>Compiler</strong></td>
<td>C/C++: Version 18.0.2.199 of Intel C/C++</td>
</tr>
<tr>
<td><strong>Compiler for Linux:</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Firmware</strong></td>
<td>Supermicro BIOS version 2.1 released Jun-2018</td>
</tr>
<tr>
<td><strong>File System</strong></td>
<td>xfs</td>
</tr>
<tr>
<td><strong>System State</strong></td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td><strong>Base Pointers</strong></td>
<td>64-bit</td>
</tr>
<tr>
<td><strong>Peak Pointers</strong></td>
<td>64-bit</td>
</tr>
<tr>
<td><strong>Other</strong></td>
<td>jemalloc memory allocator library V5.0.1</td>
</tr>
</tbody>
</table>
SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Supermicro

SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Silver 4114T)

SPECspeed2017_fp_base = 41.9
SPECspeed2017_fp_peak = 42.0

Results Table

Benchmark | Threads | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Threads | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio
--- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | ---
603.bwaves_s | 10 | 314 | 188 | 313 | 189 | 312 | 189 | 10 | 313 | 189 | 313 | 189 | 312 | 189
607.cactuBSSN_s | 10 | 289 | 57.7 | 288 | 57.9 | 286 | 58.3 | 10 | 289 | 57.7 | 288 | 57.9 | 286 | 58.3
619.lbm_s | 10 | 286 | 18.3 | 284 | 18.4 | 286 | 18.3 | 10 | 286 | 18.3 | 284 | 18.4 | 286 | 18.3
621.wrf_s | 10 | 319 | 41.5 | 320 | 41.3 | 318 | 41.6 | 10 | 318 | 41.5 | 319 | 41.5 | 318 | 41.6
627.cam4_s | 10 | 452 | 19.6 | 451 | 19.6 | 452 | 19.6 | 10 | 451 | 19.6 | 452 | 19.6 | 451 | 19.6
628.pop2_s | 10 | 289 | 41.1 | 288 | 41.2 | 289 | 41.1 | 10 | 280 | 42.4 | 283 | 42.0 | 281 | 42.2
638.imagick_s | 10 | 503 | 28.7 | 517 | 27.9 | 508 | 28.4 | 10 | 504 | 28.6 | 504 | 28.7 | 503 | 28.7
644.nab_s | 10 | 306 | 57.0 | 306 | 57.0 | 306 | 57.0 | 10 | 306 | 57.0 | 306 | 57.0 | 306 | 57.0
649.fotonik3d_s | 10 | 238 | 38.3 | 237 | 38.5 | 238 | 38.4 | 10 | 238 | 38.3 | 237 | 38.5 | 238 | 38.4
654.roms_s | 10 | 396 | 39.8 | 393 | 40.1 | 394 | 39.9 | 10 | 395 | 39.9 | 394 | 39.9 | 394 | 40.0

SPECspeed2017_fp_base = 41.9
SPECspeed2017_fp_peak = 42.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
SPEC CPU2017 Floating Point Speed Result

Supermicro
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Silver 4114T)

SPECspeed2017_fp_base = 41.9
SPECspeed2017_fp_peak = 42.0

CPU2017 License: 001176
Test Sponsor: Supermicro
Test Date: Oct-2018
Tested by: Supermicro
Hardware Availability: Jul-2017
Software Availability: Mar-2018

Platform Notes

BIOS Settings:
Hyper-Threading [ALL] = Disable
LLC dead line alloc = Disable
Patrol Scrub = Disable
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcd6f2999c33d61f64985e45859ea9
running on linux-52ma Thu Oct 18 20:28:57 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4114T CPU @ 2.20GHz
 1 "physical id"s (chips)
 10 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 10
physical 0: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 10
On-line CPU(s) list: 0-9
Thread(s) per core: 1
Core(s) per socket: 10
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4114T CPU @ 2.20GHz
Stepping: 4
CPU MHz: 2201.000
CPU max MHz: 2201.0000
CPU min MHz: 800.0000
BogoMIPS: 4399.99
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 14080K
NUMA node0 CPU(s): 0-9

(Continued on next page)
**SPEC CPU2017 Floating Point Speed Result**

**Supermicro**
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Silver 4114T)

---

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro  
**Test Date:** Oct-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2018

---

**SPECspeed2017_fp_base =** 41.9  
**SPECspeed2017_fp_peak =** 42.0

---

**Platform Notes (Continued)**

Flags:  
```
fpu vme de pse mce cx8 apic sep mtrr pge mca cmov  
pat pse36 clflush dts acpi mmx fxsr ssse2 ss ht tm pbe syscall nx pdpe1gb rdtscp  
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc  
aperfmonperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg  
fma cx16 xptr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes  
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts  
dts intel_pt rsb_ctxtsw spec_ctrl retpoline kaiser tpr_shadow vmm_i nvprior ei  
pt ept vptpm fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx  
avx512f avx512dq rdseed adx rlfshopt clwb avx512cd avx512bw avx512vl xsaveopt  
xsavec xgetbv1 cqm_llc cqm_occup_llc pku ospke
```

```
/proc/cpuinfo cache data  
cache size : 14080 KB
```

```
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a  
physical chip.  
available: 1 nodes (0)  
node 0 cpus: 0 1 2 3 4 5 6 7 8 9  
node 0 size: 192076 MB  
node 0 free: 184736 MB  
node distances:  
node 0  
0: 10
```

```
From /proc/meminfo  
MemTotal: 196686636 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB
```

```
From /etc/*release* /etc/*version*  
SuSE-release:  
SUSE Linux Enterprise Server 12 (x86_64)  
VERSION = 12  
PATCHLEVEL = 3  
# This file is deprecated and will be removed in a future service pack or release.  
# Please check /etc/os-release for details about this release.
```

```
os-release:  
NAME="SLES"  
VERSION="12-SP3"  
VERSION_ID="12.3"  
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"  
ID="sles"  
ANSI_COLOR="0;32"  
CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```
uname -a:  
Linux linux-52ma 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
```

(Continued on next page)
**Supermicro**
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Silver 4114T)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>41.9</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_fp_peak</td>
<td>42.0</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Platform Notes (Continued)**

```
x86_64  x86_64  x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Mitigation: PTI  
CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers  
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Oct 18 13:44

SPEC is set to: /home/cpu2017

Filesystem Type  Size  Used Avail Use% Mounted on
/dev/sda4      xfs   145G   48G   97G  33% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS American Megatrends Inc. 2.1 06/15/2018
  Memory:
  2x NO DIMM NO DIMM
  6x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)
```

<table>
<thead>
<tr>
<th>Compiler Version Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>CC 619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)</td>
</tr>
<tr>
<td>icc (ICC) 18.0.2 20180210</td>
</tr>
<tr>
<td>Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>CC 619.lbm_s(peak)</td>
</tr>
<tr>
<td>icc (ICC) 18.0.2 20180210</td>
</tr>
<tr>
<td>Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>FC 607.cactuBSSN_s(base, peak)</td>
</tr>
<tr>
<td>icpc (ICC) 18.0.2 20180210</td>
</tr>
<tr>
<td>Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

(Continued on next page)
Supermicro
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Silver 4114T)

**SPEC CPU2017 Floating Point Speed Result**

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>41.9</td>
<td>42.0</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 001176
**Test Sponsor:** Supermicro
**Tested by:** Supermicro

**Test Date:** Oct-2018
**Hardware Availability:** Jul-2017
**Software Availability:** Mar-2018

---

**Compiler Version Notes (Continued)**

icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

----

FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base, peak)
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

----

FC 603.bwaves_s(peak) 649.fotonik3d_s(peak)
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

----

CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

----

CC 621.wrf_s(peak) 628.pop2_s(peak)
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

---

**Base Compiler Invocation**

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

Supermicro
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Silver 4114T)

SPECspeed2017_fp_base = 41.9
SPECspeed2017_fp_peak = 42.0

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Oct-2018
Hardware Availability: Jul-2017
Software Availability: Mar-2018

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-W1,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -gopenmp
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc

Benchmarks using both Fortran and C:
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -gopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -gopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc
Supermicro
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Silver 4114T)

SPECspeed2017_fp_base = 41.9
SPECspeed2017_fp_peak = 42.0

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>Test Date:</th>
</tr>
</thead>
<tbody>
<tr>
<td>001176</td>
<td>Oct-2018</td>
</tr>
<tr>
<td>Test Sponsor:</td>
<td>Hardware Availability:</td>
</tr>
<tr>
<td>Supermicro</td>
<td>Jul-2017</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Software Availability:</td>
</tr>
<tr>
<td>Supermicro</td>
<td>Mar-2018</td>
</tr>
</tbody>
</table>

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: basepeak = yes
638.imagick_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
644.nab_s: basepeak = yes

Fortran benchmarks:
603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp -nostandard-realloc-lhs
649.fotonik3d_s: basepeak = yes
654.roms_s: -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -nostandard-realloc-lhs

(Continued on next page)
Supermicro
SuperStorage 5049P-E1CR45H (X11SPL-F, Intel Xeon Silver 4114T)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>41.9</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_fp_peak</td>
<td>42.0</td>
</tr>
</tbody>
</table>

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Oct-2018
Hardware Availability: Jul-2017
Software Availability: Mar-2018

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs

627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revD.xml

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2018-10-18 08:28:56-0400.