### SPEC® CPU2017 Integer Rate Result

**Fujitsu**

**PRIMERGY TX1330 M4, Intel Xeon E-2126G, 3.30GHz**

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>34.5</th>
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<tbody>
<tr>
<td>SPECrate2017_int_peak</td>
<td>36.8</td>
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</tbody>
</table>

**CPU2017 License:** 19  
**Test Date:** Oct-2018  
**Hardware Availability:** Nov-2018  
**Test Sponsor:** Fujitsu  
**Software Availability:** Sep-2018  
**Tested by:** Fujitsu

<table>
<thead>
<tr>
<th>Test</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU Name:</strong></td>
<td>Intel Xeon E-2126G</td>
</tr>
<tr>
<td><strong>Max MHz:</strong></td>
<td>4500</td>
</tr>
<tr>
<td><strong>Nominal:</strong></td>
<td>3300</td>
</tr>
<tr>
<td><strong>Enabled:</strong></td>
<td>6 cores, 1 chip</td>
</tr>
<tr>
<td><strong>Orderable:</strong></td>
<td>1 chip</td>
</tr>
<tr>
<td><strong>Cache L1:</strong></td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td><strong>Cache L2:</strong></td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td><strong>Cache L3:</strong></td>
<td>12 MB I+D on chip per chip</td>
</tr>
<tr>
<td><strong>Memory:</strong></td>
<td>64 GB (4 x 16 GB 2Rx8 PC4-2666V-E)</td>
</tr>
<tr>
<td><strong>Storage:</strong></td>
<td>1 x SATA HDD, 1TB, 7200RPM</td>
</tr>
<tr>
<td><strong>Other:</strong></td>
<td>None</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 19.0.0.117 of Intel C/C++ Compiler for Linux; Fortran: Version 19.0.0.117 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td><strong>Parallel:</strong></td>
<td>No</td>
</tr>
<tr>
<td>Firmware:</td>
<td>xfs</td>
</tr>
<tr>
<td><strong>System State:</strong></td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td><strong>Base Pointers:</strong></td>
<td>64-bit</td>
</tr>
<tr>
<td><strong>Peak Pointers:</strong></td>
<td>32/64-bit</td>
</tr>
<tr>
<td><strong>Other:</strong></td>
<td>jemalloc memory allocator library V5.0.1</td>
</tr>
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</table>

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<table>
<thead>
<tr>
<th><strong>500.perlbench_r</strong></th>
<th>6</th>
<th>35.1</th>
</tr>
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<tbody>
<tr>
<td><strong>502.gcc_r</strong></td>
<td>6</td>
<td>32.4</td>
</tr>
<tr>
<td><strong>505.mcf_r</strong></td>
<td>6</td>
<td>38.1</td>
</tr>
<tr>
<td><strong>520.omnetpp_r</strong></td>
<td>6</td>
<td>38.1</td>
</tr>
<tr>
<td><strong>523.xalancbmk_r</strong></td>
<td>6</td>
<td>38.8</td>
</tr>
<tr>
<td><strong>525.x264_r</strong></td>
<td>6</td>
<td>88.0</td>
</tr>
<tr>
<td><strong>531.deepsjeng_r</strong></td>
<td>6</td>
<td>85.7</td>
</tr>
<tr>
<td><strong>541.leela_r</strong></td>
<td>6</td>
<td>77.0</td>
</tr>
<tr>
<td><strong>548.exchange2_r</strong></td>
<td>6</td>
<td>77.4</td>
</tr>
<tr>
<td><strong>557.xz_r</strong></td>
<td>6</td>
<td>21.2</td>
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</tbody>
</table>

**Hardware**

**Software**
## SPEC CPU2017 Integer Rate Result

**CPU2017 License:** 19  |  **Test Date:** Oct-2018  
**Test Sponsor:** Fujitsu |  **Hardware Availability:** Nov-2018
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## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
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<tbody>
<tr>
<td>500.perlbench_r</td>
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<td>317</td>
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<td>520.omnetpp_r</td>
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<tr>
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<td>531.deepsjeng_r</td>
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<td>206</td>
<td>33.4</td>
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<td>208</td>
<td>33.1</td>
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<tr>
<td>541.leela_r</td>
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<td>77.6</td>
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<td>203</td>
<td>77.4</td>
<td>204</td>
<td>77.0</td>
<td>205</td>
<td>76.6</td>
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</tbody>
</table>

**SPECrate2017_int_base = 34.5**  
**SPECrate2017_int_peak = 36.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Process tuning settings:
```bash
echo 500000 > /proc/sys/kernel/sched_cfs_bandwidth_slice_us
```

### General Notes

Environment variables set by runcpu before the start of the run:
```bash
LD_LIBRARY_PATH = "/home/Benchmark/speccpu2017-ic19-20181011/icc19-lib/ia32"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/Benchmark/speccpu2017-ic19-20181011/icc19-lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/Benchmark/speccpu2017-ic19-20181011/je5.0.1-32"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/Benchmark/speccpu2017-ic19-20181011/je5.0.1-64"
```

Binaries compiled on a system with 2x Intel Xeon Silver 4108 CPU + 384GB RAM memory using SUSE Linux Enterprise Server 12 SP2  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:
```bash
sync; echo 3 > /proc/sys/vm/drop_caches
jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;  
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
```

(Continued on next page)
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---

**General Notes (Continued)**

jemalloc: sources available via jemalloc.net;

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

---

**Platform Notes**

BIOS configuration:
Hardware Prefetcher = Disabled
Adjacent Cache Line Prefetch = Disabled
VT-d = Disabled
Fan Control = Full
Race To Halt (RTH) = Disabled
DMI Link ASPM Control = L0s
REFRESH_2X_MODE = 2- Enabled HOT only
Sysinfo program /home/Benchmark/speccpu2017-ic19-20181011/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618b091c0f
running on TX1330M4 Fri Oct 19 18:53:29 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```plaintext
model name : Intel(R) Xeon(R) E-2126G CPU @ 3.30GHz
 1 "physical id"s (chips)
 6 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 6
physical 0: cores 0 1 2 3 4 5
```

From lscpu:
```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 6
On-line CPU(s) list: 0-5
Thread(s) per core: 1
Core(s) per socket: 6
Socket(s): 1
```

(Continued on next page)
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<td>Sep-2018</td>
</tr>
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</table>

## Platform Notes (Continued)

- **NUMA node(s):** 1
- **Vendor ID:** GenuineIntel
- **CPU family:** 6
- **Model:** 158
- **Model name:** Intel(R) Xeon(R) E-2126G CPU @ 3.30GHz
- **Stepping:** 10
- **CPU MHz:** 3300.000
- **CPU max MHz:** 4500.0000
- **CPU min MHz:** 800.0000
- **BogoMIPS:** 6624.00
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 256K
- **L3 cache:** 12288K
- **NUMA node0 CPU(s):** 0-5
- **Flags:** fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
- **/proc/cpuinfo cache data**
  - cache size : 12288 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
- available: 1 nodes (0)
- node 0 cpus: 0 1 2 3 4 5
- node 0 size: 63915 MB
- node 0 free: 63428 MB
- node distances:
  - node 0
  - 0: 10

From /proc/meminfo
- MemTotal: 65449584 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
- os-release:

(Continued on next page)
## Fujitsu

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- **SPECrate2017_int_peak = 36.8**

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</tbody>
</table>

---

### Platform Notes (Continued)

```bash
NAME="SLES"
VERSION="15"
VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"
```

```bash
uname -a:
Linux TX1330M4 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018 (cd0437b) x86_64
x86_64 x86_64 GNU/Linux
run-level 3 Oct 19 18:48
```

**SPEC is set to:** /home/Benchmark/speccpu2017-ic19-20181011

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda3</td>
<td>xfs</td>
<td>828G</td>
<td>102G</td>
<td>726G</td>
<td>13%</td>
<td>/home</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- BIOS FUJITSU // American Megatrends Inc. V5.0.0.13 R1.0.0 for D3673-A1x 09/14/2018
- Memory:
  - 4x Samsung M391A2K43BB1-CTD 16 GB 2 rank 2667

(End of data from sysinfo program)

### Compiler Version Notes

<table>
<thead>
<tr>
<th>CC</th>
<th>500.perlbench_r(base)</th>
<th>502.gcc_r(base)</th>
<th>505.mcf_r(base, peak)</th>
<th>525.x264_r(base, peak)</th>
<th>557.xz_r(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>icc (ICC)</td>
<td>19.0.0.117 20180804</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

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<table>
<thead>
<tr>
<th>CC</th>
<th>500.perlbench_r(peak)</th>
<th>502.gcc_r(peak)</th>
</tr>
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<td>icc (ICC)</td>
<td>19.0.0.117 20180804</td>
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### Compiler Version Notes (Continued)

```plaintext
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)
```  
```plaintext
icpc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```  
```plaintext
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak) 541.leela_r(peak)
```  
```plaintext
icpc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```  
```plaintext
FC 548.exchange2_r(base, peak)
```  
```plaintext
ifort (IFORT) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

### Base Compiler Invocation

- **C benchmarks:**
  - `icc -m64 -std=c11`

- **C++ benchmarks:**
  - `icpc -m64`

- **Fortran benchmarks:**
  - `ifort -m64`

### Base Portability Flags

- `500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64`
- `502.gcc_r: -DSPEC_LP64`
- `505.mcf_r: -DSPEC_LP64`
- `520.omnetpp_r: -DSPEC_LP64`
- `523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX`
- `525.x264_r: -DSPEC_LP64`

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Fujitsu
PRIMERGY TX1330 M4, Intel Xeon E-2126G, 3.30GHz

SPECrater2017_int_base = 34.5
SPECrater2017_int_peak = 36.8

Base Portability Flags (Continued)

531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64 -std=c11

502.gcc_r: icc -m32 -std=c11 -L/opt/intel/compilers_and_libraries_2019/linux/lib/ia32

C++ benchmarks (except as noted below):
icpc -m64

523.xalancbmk_r: icpc -m32 -L/opt/intel/compilers_and_libraries_2019/linux/lib/ia32

Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64

(Continued on next page)
Peak Portability Flags (Continued)

505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
-ljemalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -fno-alias
-L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

(Continued on next page)
## Fujitsu

**PRIMERGY TX1330 M4, Intel Xeon E-2126G, 3.30GHz**

<table>
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**CPU2017 License:** 19  
**Test Date:** Oct-2018

**Tested by:** Fujitsu  
**Hardware Availability:** Nov-2018

**Test Sponsor:** Fujitsu  
**Software Availability:** Sep-2018

### Peak Optimization Flags (Continued)

541.leela_r: Same as 520.omnetpp_r

**Fortran benchmarks:**
- `-Wl,-z,muldefs`  
  - `xCORE-AVX2`  
  - `-ipo`  
  - `-O3`  
  - `-no-prec-div`  
  - `-qopt-mem-layout-trans=3`  
  - `-nostandard-realloc-lhs`  
  - `-align array32byte`  
  - `-L/usr/local/je5.0.1-64/lib`  
  - `-ljemalloc`

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0.2-CFL-RevA.xml](http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0.2-CFL-RevA.xml)

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Tested with SPEC CPU2017 v1.0.2 on 2018-10-19 05:53:29-0400.  