## NEC Corporation

**Express5800/R120h-2M (Intel Xeon Bronze 3104)**

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
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</thead>
<tbody>
<tr>
<td>SPECrate2017_int_peak</td>
<td>34.5</td>
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</tbody>
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**CPU2017 License**: 9006

**Test Sponsor**: NEC Corporation

**Test Date**: Nov-2018

**Tested by**: NEC Corporation

**Hardware Availability**: Aug-2017

**Software Availability**: Mar-2018

### Hardware

- **CPU Name**: Intel Xeon Bronze 3104
- **Max MHz.**: 1700
- **Nominal**: 1700
- **Enabled**: 12 cores, 2 chips
- **Orderable**: 1,2 chips
- **Cache L1**: 32 KB I + 32 KB D on chip per core
- **L2**: 1 MB I+D on chip per core
- **L3**: 8.25 MB I+D on chip per chip
- **Memory**: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2133)
- **Storage**: 1 x 1 TB SATA, 7200 RPM, RAID 0

### Software

- **OS**: Red Hat Enterprise Linux Server release 7.4 (Maipo)
  - Kernel 3.10.0-693.21.1.el7.x86_64
- **Compiler**: C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux;
  - Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux
- **Parallel**: No
- **Firmware**: NEC BIOS Version U30 02/15/2018 released Mar-2018
- **File System**: ext4
- **System State**: Run level 3 (multi-user)
- **Base Pointers**: 64-bit
- **Peak Pointers**: 32/64-bit
- **Other**: jemalloc memory allocator V5.0.1

### Specified Software

<table>
<thead>
<tr>
<th>Copy</th>
<th>perlbench_r</th>
<th>gcc_r</th>
<th>mcf_r</th>
<th>omnetpp_r</th>
<th>xalancbmk_r</th>
<th>x264_r</th>
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</tr>
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<tbody>
<tr>
<td>12</td>
<td></td>
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<td></td>
<td></td>
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<td></td>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

SPECrate2017_int_base (33.0) SPECrate2017_int_peak (34.5)
<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>12</td>
<td>689</td>
<td>27.7</td>
<td>690</td>
<td>27.7</td>
<td>689</td>
<td>27.7</td>
<td>12</td>
<td>590</td>
<td>32.4</td>
<td>588</td>
<td>32.5</td>
<td>591</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>12</td>
<td>541</td>
<td>31.4</td>
<td>542</td>
<td>31.3</td>
<td>542</td>
<td>31.3</td>
<td>12</td>
<td>476</td>
<td>35.7</td>
<td>475</td>
<td>35.7</td>
<td></td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>12</td>
<td>504</td>
<td>38.5</td>
<td>504</td>
<td>38.5</td>
<td>504</td>
<td>38.5</td>
<td>12</td>
<td>504</td>
<td>38.5</td>
<td>504</td>
<td>38.5</td>
<td>504</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>12</td>
<td>659</td>
<td>23.9</td>
<td>656</td>
<td>24.0</td>
<td>660</td>
<td>23.9</td>
<td>12</td>
<td>651</td>
<td>24.2</td>
<td>656</td>
<td>24.0</td>
<td>653</td>
</tr>
<tr>
<td>523.xalanbmk_r</td>
<td>12</td>
<td>361</td>
<td>35.1</td>
<td>363</td>
<td>35.0</td>
<td>362</td>
<td>35.0</td>
<td>12</td>
<td>330</td>
<td>38.4</td>
<td>330</td>
<td>38.5</td>
<td>330</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>12</td>
<td>354</td>
<td>59.4</td>
<td>353</td>
<td>59.5</td>
<td>354</td>
<td>59.4</td>
<td>12</td>
<td>344</td>
<td>61.1</td>
<td>343</td>
<td>61.2</td>
<td>344</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>12</td>
<td>482</td>
<td>28.5</td>
<td>483</td>
<td>28.5</td>
<td>483</td>
<td>28.5</td>
<td>12</td>
<td>482</td>
<td>28.5</td>
<td>483</td>
<td>28.5</td>
<td>483</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>12</td>
<td>858</td>
<td>23.2</td>
<td>857</td>
<td>23.2</td>
<td>860</td>
<td>23.1</td>
<td>12</td>
<td>847</td>
<td>23.5</td>
<td>848</td>
<td>23.4</td>
<td>847</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>12</td>
<td>486</td>
<td>64.7</td>
<td>487</td>
<td>64.6</td>
<td>487</td>
<td>64.6</td>
<td>12</td>
<td>486</td>
<td>64.7</td>
<td>487</td>
<td>64.6</td>
<td>487</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>12</td>
<td>591</td>
<td>21.9</td>
<td>591</td>
<td>21.9</td>
<td>591</td>
<td>21.9</td>
<td>12</td>
<td>591</td>
<td>21.9</td>
<td>591</td>
<td>21.9</td>
<td>591</td>
</tr>
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</table>

SPECrack2017_int_base = 33.0

SPECrack2017_int_peak = 34.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

(Continued on next page)
General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
Thermal Configuration: Maximum Cooling
Workload Profile: General Throughput Compute
Memory Patrol Scrubbing: Disabled
LLC Dead Line Allocation: Disabled
LLC Prefetch: Enabled
Workload Profile: Custom
Sub-NUMA Clustering: Disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcede8f2999c33d61f64985e45859ea9
running on r120h2m Mon Nov 12 13:18:16 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
  2 "physical id"s (chips)
  12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 6
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Thread(s) per core: 1
Core(s) per socket: 6
Socket(s): 2
NUMA node(s): 2

(Continued on next page)
SPEC CPU2017 Integer Rate Result

NEC Corporation

Express5800/R120h-2M (Intel Xeon Bronze 3104)

SPECrate2017_int_base = 33.0
SPECrate2017_int_peak = 34.5

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Test Date: Nov-2018
Hardware Availability: Aug-2017
Tested by: NEC Corporation
Software Availability: Mar-2018

Platform Notes (Continued)

Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
Stepping: 4
CPU MHz: 1700.000
BogoMIPS: 3400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 8448K
NUMA node0 CPU(s): 0-2, 6-8
NUMA node1 CPU(s): 3-5, 9-11

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bs rep_good nopl xtopology nonstop_tsc
aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma
cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3nowprefetch ebpb cat_l3 cdp_l3 invpcid_single
intel_pt spec_ctrl ibpb_support tpr_shadow vnmi flexpriority ept vpid fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 3dnowprefetch epb cat_l3 cdp_l3 invpcid_single
adx smap clflushopt clwb avx512cd avx512f avx512dq
rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1
cqm_llc cqm_occp LLC cqm_mbm_total cqm_mbm_local dtherm arat pln pts

/proccpuinfo cache data
  cache size: 8448 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 6 7 8
  node 0 size: 196268 MB
  node 0 free: 191660 MB
  node 1 cpus: 3 4 5 9 10 11
  node 1 size: 196607 MB
  node 1 free: 192201 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10

From /proc/meminfo
  MemTotal: 395932708 KB
  HugePages_Total: 0
  Hugepagesize: 2048 KB

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Platform Notes (Continued)

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.4 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VARIANT="Server"
    VARIANT_ID="server"
    VERSION_ID="7.4"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:
  Linux r120h2m 3.10.0-693.21.1.el7.x86_64 #1 SMP Fri Feb 23 18:54:16 UTC 2018 x86_64
  x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)

run-level 3 Nov 12 13:12

SPEC is set to: /home/cpu2017
  Filesystem   Type  Size  Used Avail Use% Mounted on
  /dev/sda3    ext4  909G  381G  482G  45% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS NEC U30 02/15/2018
  Memory:
    24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666, configured at 2133

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
  CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
  557.xz_r(base)
==============================================================================

(Continued on next page)
SPEC CPU2017 Integer Rate Result

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Compiler Version Notes (Continued)

icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC  500.perlbench_r(peak) 502.gcc_r(peak) 505.mcf_r(peak) 525.x264_r(peak)
    557.xz_r(peak)
==============================================================================
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
    541.leela_r(base)
==============================================================================
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
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icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC  548.exchange2_r(base)
==============================================================================
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC  548.exchange2_r(peak)
==============================================================================
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

(Continued on next page)
## NEC Corporation

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### Base Compiler Invocation (Continued)

C++ benchmarks:
- icpc -m64

Fortran benchmarks:
- ifort -m64

### Base Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
- 502.gcc_r: -DSPEC_LP64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

**C++ benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

**Fortran benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -nostandard-realloc-lhs`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

### Peak Compiler Invocation

C benchmarks (except as noted below):
- `icc -m64 -std=c11`

(Continued on next page)
SERC CPU2017 Integer Rate Result

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Copyright 2017-2018 Standard Performance Evaluation Corporation

CPU2017 License: 9006
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Tested by: NEC Corporation

Test Date: Nov-2018
Hardware Availability: Aug-2017
Software Availability: Mar-2018

Peak Compiler Invocation (Continued)

502.gcc_r: icc -m32 -std=c11 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):
icpc -m64
523.xalancbmk_r: icpc -m32 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: basepeak = yes
525.x264_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3

(Continued on next page)
SPEC CPU2017 Integer Rate Result

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Peak Optimization Flags (Continued)

525.x264_r (continued):
  -fno-alias -L/usr/local/je5.0.1-64/lib -ljemalloc
557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
  -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
  -L/usr/local/je5.0.1-64/lib -ljemalloc
523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
  -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
  -L/usr/local/je5.0.1-32/lib -ljemalloc
531.deepsjeng_r: basepeak = yes
541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevB.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2018-11-11 23:18:15-0500.
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