## NEC Corporation

### Express5800/R120h-1E (Intel Xeon Gold 5120)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>87.3</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_fp_peak</td>
<td>88.2</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_fp_base (87.3)</th>
<th>SPECspeed2017_fp_peak (88.2)</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>28</td>
<td>113</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>28</td>
<td>113</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>28</td>
<td>113</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>28</td>
<td>113</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>28</td>
<td>113</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>28</td>
<td>113</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>28</td>
<td>113</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>28</td>
<td>113</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>28</td>
<td>113</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>28</td>
<td>113</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 5120
- **Max MHz.:** 3200
- **Nominal:** 2200
- **Enabled:** 28 cores, 2 chips
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **Cache L2:** 1 MB I+D on chip per core
- **Cache L3:** 19.25 MB I+D on chip per core
- **Other:** None
- **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)
- **Storage:** 1 x 1 TB SATA, 7200 RPM, RAID 0
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux Server release 7.4 (Maipo)
- **Kernel:** 3.10.0-693.21.1.el7.x86_64
- **Compiler:** C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux;
  Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux
- **Parallel:** Yes
- **Firmware:** NEC BIOS Version U31 06/20/2018 released Sep-2018
- **File System:** ext4
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
SPEC CPU2017 Floating Point Speed Result

NEC Corporation

Express5800/R120h-1E (Intel Xeon Gold 5120)

SPECspeed2017_fp_base = 87.3
SPECspeed2017_fp_peak = 88.2

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<td></td>
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<tr>
<td>603.bwaves_s</td>
<td>28</td>
<td>147</td>
<td>401</td>
<td>148</td>
<td>399</td>
<td>147</td>
<td>400</td>
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<tr>
<td>607.cactuBSSN_s</td>
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<td>114</td>
<td>148</td>
<td>113</td>
<td>152</td>
<td>109</td>
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<tr>
<td>619.lbm_s</td>
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<td>140</td>
<td>37.4</td>
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<td>67.9</td>
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<td>67.7</td>
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<td>67.8</td>
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<tr>
<td>627.cam4_s</td>
<td>28</td>
<td>148</td>
<td>59.7</td>
<td>148</td>
<td>60.0</td>
<td>148</td>
<td>60.0</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>28</td>
<td>229</td>
<td>51.8</td>
<td>228</td>
<td>52.2</td>
<td>230</td>
<td>51.6</td>
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<tr>
<td>638.imagick_s</td>
<td>28</td>
<td>191</td>
<td>75.5</td>
<td>191</td>
<td>75.5</td>
<td>192</td>
<td>75.3</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>28</td>
<td>118</td>
<td>148</td>
<td>118</td>
<td>148</td>
<td>118</td>
<td>148</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>28</td>
<td>129</td>
<td>70.9</td>
<td>128</td>
<td>71.1</td>
<td>129</td>
<td>70.7</td>
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<tr>
<td>654.roms_s</td>
<td>28</td>
<td>174</td>
<td>90.7</td>
<td>172</td>
<td>91.8</td>
<td>173</td>
<td>91.3</td>
</tr>
</tbody>
</table>

SPECspeed2017_fp_base = 87.3
SPECspeed2017_fp_peak = 88.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

SPEC CPU2017 Floating Point Speed Result

NEC Corporation

Express5800/R120h-1E (Intel Xeon Gold 5120)

SPECspeed2017_fp_peak = 88.2
SPECspeed2017_fp_base = 87.3

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Test Date: Dec-2018
Hardware Availability: Nov-2017
Tested by: NEC Corporation
Software Availability: Mar-2018

Platform Notes

BIOS Settings:
Thermal Configuration: Maximum Cooling
Workload Profile: General Peak Frequency Compute
Intel Hyper-Threading: Disabled
Memory Patrol Scrubbing: Disabled
Energy/Performance Bias: Maximum Performance
LLC Dead Line Allocation: Disabled
Workload Profile: Custom
NUMA Group Size Optimization: Flat
Adjacent Sector Prefetch: Disabled
DCU Stream Prefetcher: Disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on r120h1e Sun Dec 9 20:33:59 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 5120 CPU @ 2.20GHz
  2 "physical id"s (chips)
  28 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following
  excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 14
  siblings : 14
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  CPU(s): 28
  On-line CPU(s) list: 0-27
  Thread(s) per core: 1
  Core(s) per socket: 14
  Socket(s): 2
  NUMA node(s): 2
  Vendor ID: GenuineIntel
  CPU family: 6
  Model: 85
  Model name: Intel(R) Xeon(R) Gold 5120 CPU @ 2.20GHz
  Stepping: 4
  CPU MHz: 2200.000
  BogoMIPS: 4400.00

(Continued on next page)
### SPEC CPU2017 Floating Point Speed Result

**NEC Corporation**

**Express5800/R120h-1E (Intel Xeon Gold 5120)**

**CPU2017 License:** 9006

**Test Sponsor:** NEC Corporation

**Test Date:** Dec-2018

**Tested by:** NEC Corporation

**Hardware Availability:** Nov-2017

**Software Availability:** Mar-2018

**SPECspeed2017_fp_base** = 87.3

**SPECspeed2017_fp_peak** = 88.2

---

**Platform Notes (Continued)**

<table>
<thead>
<tr>
<th>Virtualization:</th>
<th>VT-x</th>
</tr>
</thead>
<tbody>
<tr>
<td>L1d cache:</td>
<td>32K</td>
</tr>
<tr>
<td>L1i cache:</td>
<td>32K</td>
</tr>
<tr>
<td>L2 cache:</td>
<td>1024K</td>
</tr>
<tr>
<td>L3 cache:</td>
<td>19712K</td>
</tr>
<tr>
<td>NUMA node0 CPU(s):</td>
<td>0-13</td>
</tr>
<tr>
<td>NUMA node1 CPU(s):</td>
<td>14-27</td>
</tr>
<tr>
<td>Flags:</td>
<td>fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nop1 xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma cx16 xptr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdpl_l3 invpcid_single intel_pt spec_ctrl ibpb_support tpr_shadow vmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts</td>
</tr>
<tr>
<td></td>
<td>/proc/cpuinfo cache data</td>
</tr>
<tr>
<td></td>
<td>cache size : 19712 KB</td>
</tr>
</tbody>
</table>

From `numactl --hardware` WARNING: a numactl 'node' might or might not correspond to a physical chip.

<table>
<thead>
<tr>
<th>node</th>
<th>cpus</th>
<th>size</th>
<th>free</th>
</tr>
</thead>
<tbody>
<tr>
<td>node 0</td>
<td>0 1 2 3 4 5 6 7 8 9 10 11 12 13</td>
<td>97953 MB</td>
<td>95328 MB</td>
</tr>
<tr>
<td>node 1</td>
<td>14 15 16 17 18 19 20 21 22 23 24 25 26 27</td>
<td>98303 MB</td>
<td>95991 MB</td>
</tr>
</tbody>
</table>

From `/proc/meminfo`

| MemTotal:   | 197737808 kB |
| HugePages_Total: | 0 |
| Hugepagesize: | 2048 KB |

From `/etc/*release* /etc/*version*`

<table>
<thead>
<tr>
<th>os-release:</th>
</tr>
</thead>
<tbody>
<tr>
<td>NAME=&quot;Red Hat Enterprise Linux Server&quot;</td>
</tr>
<tr>
<td>VERSION=&quot;7.4 (Maipo)&quot;</td>
</tr>
<tr>
<td>ID=&quot;rhel&quot;</td>
</tr>
<tr>
<td>ID_LIKE=&quot;fedora&quot;</td>
</tr>
<tr>
<td>VARIANT=&quot;Server&quot;</td>
</tr>
</tbody>
</table>

(Continued on next page)
NEC Corporation

Express5800/R120h-1E (Intel Xeon Gold 5120)

SPEC CPU2017 Floating Point Speed Result

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SPECspeed2017_fp_base = 87.3
SPECspeed2017_fp_peak = 88.2

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Test Date: Dec-2018
CPU2017 License: 9006
Test Sponsor: NEC Corporation
Test Date: Dec-2018

Tested by: NEC Corporation
Hardware Availability: Nov-2017
Software Availability: Mar-2018

Platform Notes (Continued)

VARIANT_ID="server"
VERSION_ID="7.4"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:
Linux r120h1e 3.10.0-693.21.1.el7.x86_64 #1 SMP Fri Feb 23 18:54:16 UTC 2018 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)

run-level 3 Dec 9 20:28

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 ext4 909G 209G 654G 25% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS NEC U31 06/20/2018
Memory:
  4x UNKNOWN NOT AVAILABLE
  12x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================

CC  619.lbm_s(peak)

(Continued on next page)
NEC Corporation
Express5800/R120h-1E (Intel Xeon Gold 5120)

SPEC CPU2017 Floating Point Speed Result

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SPECspeed2017_fp_base = 87.3
SPECspeed2017_fp_peak = 88.2

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Test Date: Dec-2018
Hardware Availability: Nov-2017
Tested by: NEC Corporation
Software Availability: Mar-2018

Compiler Version Notes (Continued)

---

icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
FC 607.cactuBSSN_s(base, peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
==============================================================================
FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base, peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
==============================================================================
CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
==============================================================================
CC 621.wrf_s(peak) 628.pop2_s(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
**SPEC CPU2017 Floating Point Speed Result**

<table>
<thead>
<tr>
<th>NEC Corporation</th>
<th>SPECspeed2017_fp_peak = 88.2</th>
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<tr>
<td>Express5800/R120h-1E (Intel Xeon Gold 5120)</td>
<td>SPECspeed2017_fp_base = 87.3</td>
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</table>

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation  
**Test Date:** Dec-2018  
**Hardware Availability:** Nov-2017  
**Software Availability:** Mar-2018

### Base Compiler Invocation

C benchmarks:
```
icc -m64 -std=c11
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:
```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

### Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

### Base Optimization Flags

C benchmarks:
```
-W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:
```
-W1,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -gopenmp
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Benchmarks using both Fortran and C:
```
-W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -gopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc
```

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

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NEC Corporation
Express5800/R120h-1E (Intel Xeon Gold 5120)

| SPECspeed2017_fp_base = 87.3 |
| SPECspeed2017_fp_peak = 88.2 |

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation

Test Date: Dec-2018
Hardware Availability: Nov-2017
Software Availability: Mar-2018

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:

icc -m64 -std=c11

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:

icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: basepeak = yes

Fortran benchmarks:

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

NEC Corporation

Express5800/R120h-1E (Intel Xeon Gold 5120)

SPECspeed2017_fp_base = 87.3
SPECspeed2017_fp_peak = 88.2

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation

Test Date: Dec-2018
Hardware Availability: Nov-2017
Software Availability: Mar-2018

Peak Optimization Flags (Continued)

603.bwaves_s: basepeak = yes
649.fotonik3d_s: basepeak = yes
654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
627.cam4_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevB.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2018-12-09 06:33:58-0500.
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