**SPEC® CPU2017 Floating Point Speed Result**

Supermicro
SuperServer E300-9C (X11SCV-Q, Intel Core i3-8100)

<table>
<thead>
<tr>
<th>Software Availability:</th>
<th>Apr-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Date:</td>
<td>Nov-2018</td>
</tr>
<tr>
<td>Test Sponsor:</td>
<td>Supermicro</td>
</tr>
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<td>Tested by:</td>
<td>Supermicro</td>
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<tr>
<td>CPU2017 License:</td>
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| SPECspeed2017_fp_base | 21.3 |
| SPECspeed2017_fp_peak | 21.6 |

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<tr>
<th>Threads</th>
<th>SPECspeed2017_fp_base (21.3)</th>
<th>SPECspeed2017_fp_peak (21.6)</th>
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<tr>
<td>4</td>
<td>603.bwaves_s 35.5</td>
<td>603.bwaves_s 35.5</td>
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<tr>
<td></td>
<td>607.cactuBSSN_s 35.5</td>
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<td></td>
<td>619.lbm_s 6.34</td>
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<tr>
<td></td>
<td>621.wrf_s 25.9</td>
<td>621.wrf_s 25.9</td>
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<tr>
<td></td>
<td>627.cam4_s 15.6</td>
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<td>628.pop2_s 25.9</td>
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<td>644.nab_s 32.5</td>
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<tr>
<td></td>
<td>654.roms_s 12.9</td>
<td>654.roms_s 12.9</td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Core i3-8100
- **Max MHz.:** 3600
- **Nominal:** 3600
- **Enabled:** 4 cores, 1 chip
- **Orderable:** 1 chip
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 256 KB I+D on chip per core
- **L3:** 6 MB I+D on chip per chip
- **Other:** None
- **Memory:** 32 GB (2 x 16 GB 2Rx8 PC4-2666V-S, running at 2400)
- **Storage:** 1 x 1 TB SATA III, 7200RPM
- **Other:** None

**Software**

- **OS:** SUSE Linux Enterprise Server 12 SP3 (x86_64)
- **Kernel:** 4.4.114-94.11-default
- **Compiler:** C/C++: Version 18.0.2.199 of Intel C/C++
- **Compiler for Linux:** Fortran: Version 18.0.2.199 of Intel Fortran
- **Compiler for Linux:**
- **Parallel:** Yes
- **Firmware:** Version 1.0 released Aug-2018
- **File System:** xfs
- **System State:** Run level 5 (multi-user with display manager)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator library V5.0.1
SPEC CPU2017 Floating Point Speed Result

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SPECspeed2017_fp_base = 21.3
SPECspeed2017_fp_peak = 21.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Nov-2018
Hardware Availability: Apr-2018
Software Availability: Mar-2018

Results Table

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<tr>
<th>Benchmark</th>
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</tr>
</tbody>
</table>

SPECspeed2017_fp_base = 21.3
SPECspeed2017_fp_peak = 21.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
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Supermicro
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<td>21.6</td>
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**Platform Notes**

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-cg rt Sun Nov 18 14:09:58 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

- model name: Intel(R) Core(TM) i3-8100 CPU @ 3.60GHz
- 1 "physical id"s (chips)
- 4 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 4
  - siblings: 4
  - physical 0: cores 0 1 2 3

From lscpu:

- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 4
- On-line CPU(s) list: 0-3
- Thread(s) per core: 1
- Core(s) per socket: 4
- Socket(s): 1
- NUMA node(s): 1
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 158
- Model name: Intel(R) Core(TM) i3-8100 CPU @ 3.60GHz
- Stepping: 10
- CPU MHz: 3600.001
- CPU max MHz: 3600.0000
- CPU min MHz: 800.0000
- BogoMIPS: 7199.96
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 256K
- L3 cache: 6144K
- NUMA node0 CPU(s): 0-3
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg fma

(Continued on next page)
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**CPU2017 License:** 001176  
**Test Date:** Nov-2018  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro  

**Hardware Availability:** Apr-2018  
**Software Availability:** Mar-2018

---

**Platform Notes (Continued)**

cx16 xtpr pdcmd pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch arat epb invpcid_single pni pln pts dtherm hwp_notify hwp_act_window hwp_epp intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow vmx flexpriority ept vpid fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid mpx rdseed adx smap clflushopt xsaveopt xsavec xgetbv1

```
/proc/cpuinfo cache data
  cache size : 6144 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 1 nodes (0)
node 0 cpus: 0 1 2 3
node 0 size: 31808 MB
node 0 free: 18009 MB
node distances:
  node 0
    0:  10

From /proc/meminfo
  MemTotal:       32572016 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

```
/usr/bin/lsb_release -d
  SUSE Linux Enterprise Server 12 SP3
```

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 3
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP3"
    VERSION_ID="12.3"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp3"

```
uname -a:
  Linux linux-cgert 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
  x86_64 x86_64 x86_64 GNU/Linux
```

(Continued on next page)
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| Test Date: | Nov-2018 |
| Hardware Availability: | Apr-2018 |
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**Platform Notes (Continued)**

Kernel self-reported vulnerability status:

- CVE-2017-5754 (Meltdown): Mitigation: PTI
- CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
- CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

**run-level 5 Nov 17 19:08**

```bash
SPEC is set to: /home/cpu2017
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- BIOS American Megatrends Inc. 1.0 08/03/2018
- Memory:
  - 2x Micron 16ATF2G64HZ-2G6E1 16 GB 2 rank 2667, configured at 2400

(End of data from sysinfo program)

**Compiler Version Notes**

```
CC 619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
 ICC 18.0.2 20180210
 Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

CC 619.lbm_s(peak)
 ICC 18.0.2 20180210
 Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

FC 607.cactuBSSN_s(base, peak)
 icpc (ICC) 18.0.2 20180210
 Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
 icc (ICC) 18.0.2 20180210
 Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
 ifort (IFORT) 18.0.2 20180210
```

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Test Sponsor: Supermicro
Tested by: Supermicro

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Test Date: Nov-2018
Hardware Availability: Apr-2018
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Compiler Version Notes (Continued)
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC   603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base, peak)
ifort (IFORT) 18.0.2 20180210
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==============================================================================

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icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

(Continued on next page)
**Supermicro**

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**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Specspeed2017_fp_base** = 21.3  
**Specspeed2017_fp_peak** = 21.6

**Base Compiler Invocation (Continued)**

Benchmarks using Fortran, C, and C++:

icpc -m64 icc -m64 -std=c11 ifort -m64

---

**Base Portability Flags**

603.bwaves_s: -DSPEC_LP64  
607.cactuBSSN_s: -DSPEC_LP64  
619.lbm_s: -DSPEC_LP64  
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG  
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl  
638.imagick_s: -DSPEC_LP64  
644.nab_s: -DSPEC_LP64  
649.fotonik3d_s: -DSPEC_LP64  
654.roms_s: -DSPEC_LP64

**Base Optimization Flags**

**C benchmarks:**

-W1, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc

**Fortran benchmarks:**

-W1, -z, muldefs -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc

**Benchmarks using both Fortran and C:**

-W1, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP  
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc

**Benchmarks using Fortran, C, and C++:**

-W1, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP  
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CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro
Test Date: Nov-2018
Hardware Availability: Apr-2018
Software Availability: Mar-2018

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: basepeak = yes

649.fotonik3d_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp -nostandard-realloc-lhs

654.roms_s: -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -nostandard-realloc-lhs

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CPU2017 License: 001176
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Tested by: Supermicro
Software Availability: Mar-2018

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs

627.cam4_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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