## SPEC® CPU2017 Floating Point Speed Result

**Dell Inc.**

PowerEdge R740xd2 (Intel Xeon Gold 5117, 2.00GHz)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>77.7</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_fp_peak</td>
<td>78.9</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 5117
- **Max MHz.:** 2800
- **Nominal:** 2000
- **Enabled:** 28 cores, 2 chips
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 19.25 MB I+D on chip per core
- **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)
- **Storage:** 1 x 250 GB M.2 SATA SSD
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 12 SP3
- **Compiler:** C/C++: Version 18.0.2.20180210 of Intel C/C++ Compiler for Linux;
- **Fortran:** Version 18.0.2.20180210 of Intel Fortran Compiler for Linux
- **Parallel:** Yes
- **Firmware:** Version 1.0.3 released Oct-2018
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** None

### Test Details

- **CPU2017 License:** 55
- **Test Sponsor:** Dell Inc.
- **Test by:** Dell Inc.
- **Test Date:** Nov-2018
- **Hardware Availability:** Dec-2018
- **Software Availability:** Feb-2018

---

### SPEC Speed Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>28</td>
<td>36.3</td>
<td>78.9</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>28</td>
<td>54.3</td>
<td>77.7</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>28</td>
<td>46.7</td>
<td>60.0</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>28</td>
<td>43.2</td>
<td>67.0</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>28</td>
<td>43.7</td>
<td>80.0</td>
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<tr>
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<td>28</td>
<td>43.7</td>
<td>80.0</td>
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<td>28</td>
<td>63.1</td>
<td>104</td>
</tr>
<tr>
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<td>28</td>
<td>94.5</td>
<td>125</td>
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<tr>
<td>649.fotonik3d_s</td>
<td>28</td>
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<td>125</td>
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---

### Other

- **Other:** None
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SPEC CPU2017 Floating Point Speed Result

SPECspeed2017_fp_base = 77.7
SPECspeed2017_fp_peak = 78.9

Results Table

<table>
<thead>
<tr>
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<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<tr>
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<tr>
<td>607.cactuBSSN_s</td>
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<tr>
<td>619.lbm_s</td>
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<td>67.0</td>
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<td>67.3</td>
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<td>167</td>
<td>94.4</td>
<td>167</td>
<td>94.5</td>
<td>166</td>
<td>94.8</td>
</tr>
</tbody>
</table>

SPECspeed2017_fp_base = 77.7
SPECspeed2017_fp_peak = 78.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
OMP_STACKSIZE = "192M"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3> /proc/sys/vm/drop_caches

Platform Notes

BIOS settings:
Sub NUMA Cluster disabled
Virtualization Technology disabled

(Continued on next page)
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</table>

System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor disabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-m8ku Thu Nov 15 18:59:04 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 5117 CPU @ 2.00GHz
    2 "physical id"s (chips)
    28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
core excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 14
siblings : 14
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
```

From lscpu:

```
Architecture:       x86_64
CPU op-mode(s):     32-bit, 64-bit
Byte Order:         Little Endian
CPU(s):             28
On-line CPU(s) list: 0-27
Thread(s) per core: 1
Core(s) per socket: 14
Socket(s):          2
NUMA node(s):       2
Vendor ID:          GenuineIntel
CPU family:         6
Model:              85
Model name:         Intel(R) Xeon(R) Gold 5117 CPU @ 2.00GHz
Stepping:           4
CPU MHz:            1995.320
BogoMIPS:           3990.64
Virtualization:     VT-x
```

(Continued on next page)
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Platform Notes (Continued)

```
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 19712K
NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26
NUMA node1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
       pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
       lm constant_tsc art arch_perfmon pebs bts rep_good nopl mce cx88 apic sep mtrr pge mca cmov
       pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
       lm constant_tsc art arch_perfmon pebs bts rep_good nopl mce cx88 apic sep mtrr pge mca cmov
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       pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
```

_FROM numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
_available: 2 nodes (0-1)
_node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26
_node 0 size: 95285 MB
_node 0 free: 91043 MB
_node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27
_node 1 size: 96749 MB
_node 1 free: 92827 MB
_node distances:
_node 0 1
 0: 10 21
 1: 21 10

_From /proc/meminfo
MemTotal: 196642996 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP3

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 3

(Continued on next page)
Platform Notes (Continued)

# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
   NAME="SLES"
   VERSION="12-SP3"
   VERSION_ID="12.3"
   PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
   ID="sles"
   ANSI_COLOR="0;32"
   CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
   Linux linux-m8ku 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Nov 15 12:08 last=5

SPEC is set to: /home/cpu2017

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sdz4</td>
<td>xfs</td>
<td>182G</td>
<td>10G</td>
<td>172G</td>
<td>6%</td>
<td>/home</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMI BIOS" standard.

BIOS Dell Inc. 1.0.3 10/25/2018
Memory:
   12x 002C04B3002C 18ASF2G72FD2-2G6E1 16 GB 2 rank 2666, configured at 2400
   4x Not Specified Not Specified

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
==============================================================================
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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------------------------------------------------------------------------------
==============================================================================
FC  607.cactuBSSN_s(base)
------------------------------------------------------------------------------
icpC (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
FC   607.cactuBSSN_s(peak)
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FC  603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
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Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
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**Base Optimization Flags**

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

**Peak Compiler Invocation**

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

**Peak Portability Flags**

Same as Base Portability Flags
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Peak Optimization Flags

C benchmarks:
619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: basepeak = yes
644.nab_s: basepeak = yes

Fortran benchmarks:
603.bwaves_s: basepeak = yes
649.fotonik3d_s: basepeak = yes

654.roms_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-qopenmp -nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
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Hardware Availability: Dec-2018  
Software Availability: Feb-2018  

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Tested with SPEC CPU2017 v1.0.5 on 2018-11-15 19:59:04-0500.  
Report generated on 2018-12-26 13:02:29 by CPU2017 PDF formatter v6067.  
Originally published on 2018-12-25.