## SPEC® CPU2017 Floating Point Rate Result

**Supermicro**

SuperWorkstation 5039C-T (X11SCA, Intel Core i7-9700K)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>42.6</td>
<td>43.3</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro  
**Hardware Availability:** Oct-2018  
**Software Availability:** Mar-2018  
**Test Date:** Jan-2019

### Hardware

<table>
<thead>
<tr>
<th>CPU Name</th>
<th>Intel Core i7-9700K</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz.</td>
<td>4900</td>
</tr>
<tr>
<td>Nominal</td>
<td>3600</td>
</tr>
<tr>
<td>Enabled</td>
<td>8 cores, 1 chip</td>
</tr>
<tr>
<td>Orderable</td>
<td>1 chip</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Cache L2</td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td>Cache L3</td>
<td>12 MB I+D on chip per core</td>
</tr>
<tr>
<td>Memory</td>
<td>64 GB (4 x 16 GB 2Rx8 PC4-2666V-E)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x 200 GB SATA III SSD</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>OS</th>
<th>SUSE Linux Enterprise Server 12 SP3 (x86_64)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler</td>
<td>C/C++: Version 18.0.2.199 of Intel C/C++</td>
</tr>
<tr>
<td>Compiler for Linux</td>
<td>Compiler for Linux;</td>
</tr>
<tr>
<td>Compiler for Fortran</td>
<td>Fortran: Version 18.0.2.199 of Intel Fortran</td>
</tr>
<tr>
<td>Firmware</td>
<td>Version 1.0a released Sep-2018</td>
</tr>
<tr>
<td>File System</td>
<td>xfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>64-bit</td>
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<tr>
<td>Other</td>
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</table>

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**Graph:**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
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<tbody>
<tr>
<td>503.bwaves_r</td>
<td>8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.caCTuBSSN_r</td>
<td>8</td>
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<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>8</td>
<td></td>
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</tr>
<tr>
<td>510.parest_r</td>
<td>8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>8</td>
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</tr>
<tr>
<td>544.nab_r</td>
<td>8</td>
<td></td>
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</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>8</td>
<td></td>
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</tr>
<tr>
<td>554.roms_r</td>
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</table>

**SPECrate2017_fp_base (42.6)**  
**SPECrate2017_fp_peak (43.3)**
## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<tbody>
<tr>
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<td>857</td>
<td>14.8</td>
<td>867</td>
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</tr>
</tbody>
</table>

### Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "~/home/cpu2017/lib/intel64"
```

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-65nv Fri Jan  4 19:02:17 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Core(TM) i7-9700K CPU @ 3.60GHz
   1 "physical id"s (chips)
     8 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 8
    siblings : 8
    physical 0: cores 0 1 2 3 4 5 6 7

From lscpu:
  Architecture:          x86_64
  CPU op-mode(s):        32-bit, 64-bit
  Byte Order:            Little Endian
  CPU(s):                8
  On-line CPU(s) list:   0-7
  Thread(s) per core:    1
  Core(s) per socket:    8
  Socket(s):             1
  NUMA node(s):          1
  Vendor ID:             GenuineIntel
  CPU family:            6
  Model:                 158
  Model name:            Intel(R) Core(TM) i7-9700K CPU @ 3.60GHz
  Stepping:              12
  CPU MHz:               4870.129
  CPU max MHz:           4900.0000
  CPU min MHz:           800.0000
  BogoMIPS:              7199.97
  Virtualization:        VT-x
  L1d cache:             32K
  L1i cache:             32K

(Continued on next page)
Supermicro
SuperWorkstation 5039C-T (X11SCA , Intel Core i7-9700K)  

**SPEC CPU2017 Floating Point Rate Result**

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro  
**Test Date:** Jan-2019  
**Hardware Availability:** Oct-2018  
**Software Availability:** Mar-2018

---

**Platform Notes (Continued)**

L2 cache: 256K  
L3 cache: 12288K  
NUMA node0 CPU(s): 0-7  

**Flags:**  
- fpu  
- vme  
- de  
- pae  
- mce  
- cmov  
- pat  
- pse36  
- clflush  
- dts  
- acpi  
- mtrr  
- pge  
- mca  
- cmov  
- xtopology  
- nonstop_tsc  
- arch_perfmon  
- pebs  
- tsc_deadline_timer  
- xsave  
- avx  
- f16c  
- rdrand  
- lahf_lm  
- abm  
- 3dnowprefetch  
- ida  
- arat  
- epb  
- invpcid_single  
- pln  
- pts  
- dtherm  
- hwp_notify  
- hwp_act_window  
- hwp_epp  
- intel_pt  
- rsb_ctxsw  
- spec_ctrl  
- retpoline  
- kaiser  
- tpr_shadow  
- vnmi  
- flexpriority  
- ept  
- vpid  
- fsqbase  
- tsc_adjust  
- bm1  
- hle  
- avx2  
- smep  
- bmi2  
- erms  
- invpcid  
- rtm  
- mpx  
- rdseed  
- adx  
- smap  
- clflushopt  
- xsaveopt  
- xsave  
- xgetbv1

/proc/cpuinfo cache data  
- cache size: 12288 KB

From numactl --hardware  
- WARNING: a numactl 'node' might or might not correspond to a physical chip.  
- available: 1 nodes (0)  
- node 0 cpus: 0 1 2 3 4 5 6 7  
- node 0 size: 64283 MB  
- node 0 free: 52685 MB  
- node distances:  
- node 0  
- 0: 10

From /proc/meminfo  
- MemTotal: 65825824 kB  
- HugePages_Total: 0  
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*  
**SuSE-release:**  
- SUSE Linux Enterprise Server 12 (x86_64)  
- VERSION = 12  
- PATCHLEVEL = 3  
- # This file is deprecated and will be removed in a future service pack or release.  
- # Please check /etc/os-release for details about this release.  
- os-release:  
- NAME="SLES"  
- VERSION="12-SP3"  
- VERSION_ID="12.3"  
- PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"  
- ID="sles"  
- ANSI_COLOR="0;32"  
- CPE_NAME="cpe:/o:suse:sles:12:sp3"

---

(Continued on next page)
Supermicro
SuperWorkstation 5039C-T (X11SCA, Intel Core i7-9700K)

SPECbench 2017 Floating Point Rate Result

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>Test Date</th>
</tr>
</thead>
<tbody>
<tr>
<td>001176</td>
<td>Jan-2019</td>
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</table>

<table>
<thead>
<tr>
<th>Test Sponsor</th>
<th>Hardware Availability</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supermicro</td>
<td>Oct-2018</td>
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</tbody>
</table>

<table>
<thead>
<tr>
<th>Tested by</th>
<th>Software Availability</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supermicro</td>
<td>Mar-2018</td>
</tr>
</tbody>
</table>

---

Platform Notes (Continued)

```
uname -a:
    Linux linux-65nv 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Jan 4 14:01

SPEC is set to: /home/cpu2017
```

```
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 145G 35G 110G 25% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard. BIOS American Megatrends Inc. 1.0a 09/27/2018
Memory:
4x Micron 18ADF2G72AZ-2G6H1R 16 GB 2 rank 2667

(End of data from sysinfo program)

---

Compiler Version Notes

```
CC 519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base, peak)
```

```
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
CC 519.lbm_r(peak)
```

```
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
CXXC 508.namd_r(base) 510.parest_r(base, peak)
```

```
icpc (ICC) 18.0.2 20180210
```

(Continued on next page)
### Compiler Version Notes (Continued)

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

---

**CXXC 508.namd_r(peak)**

```
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

---

**CC  511.povray_r(base) 526.blender_r(base, peak)**

```
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

---

**FC  507.cactuBSSN_r(base, peak)**

```
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

---

**FC  503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)**

```
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

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*(Continued on next page)*
Supermicro
SuperWorkstation 5039C-T (X11SCA, Intel Core i7-9700K)

**SPECrate2017_fp_base** = 42.6
**SPECrate2017_fp_peak** = 43.3

**CPU2017 License:** 001176
**Test Sponsor:** Supermicro
**Tested by:** Supermicro

**Test Date:** Jan-2019
**Hardware Availability:** Oct-2018
**Software Availability:** Mar-2018

---

**Compiler Version Notes (Continued)**

```bash
FC  554.roms_r(peak)

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

CC  521.wrf_r(base) 527.cam4_r(base)

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

CC  521.wrf_r(peak) 527.cam4_r(peak)

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

---

**Base Compiler Invocation**

**C benchmarks:**
icc -m64 -std=c11

**C++ benchmarks:**
icpc -m64

**Fortran benchmarks:**
ifort -m64

**Benchmarks using both Fortran and C:**
ifort -m64 icc -m64 -std=c11

**Benchmarks using both C and C++:**
icpc -m64 icc -m64 -std=c11

**Benchmarks using Fortran, C, and C++:**
icpc -m64 icc -m64 -std=c11 ifort -m64
Supermicro
SuperWorkstation 5039C-T (X11SCA, Intel Core i7-9700K)

SPECrate2017_fp_base = 42.6
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CPU2017 License: 001176
Test Sponsor: Supermicro
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Tested by: Supermicro
Software Availability: Mar-2018

Base Portatility Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3
538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3
544.nab_r: basepeak = yes

C++ benchmarks:
508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

(Continued on next page)
Peak Optimization Flags (Continued)

510.parest_r: basepeak = yes

Fortran benchmarks:

503.bwaves_r: basepeak = yes
549.fotonik3d_r: basepeak = yes
554.roms_r: --prof-gen(pass 1) --prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:

521.wrf_r: --prof-gen(pass 1) --prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

511.povray_r: --prof-gen(pass 1) --prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

526.blender_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revD.xml
Supermicro
SuperWorkstation 5039C-T (X11SCA, Intel Core i7-9700K)

| SPECrate2017_fp_base = 42.6 |
| SPECrate2017_fp_peak = 43.3 |

| CPU2017 License: 001176 | Test Date: Jan-2019 |
| Test Sponsor: Supermicro | Hardware Availability: Oct-2018 |
| Tested by: Supermicro | Software Availability: Mar-2018 |

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