SPEC® CPU2017 Integer Speed Result

Supermicro
SuperWorkstation 5039C-T (X11SCA, Intel Core i7-9700K)

SPECspeed2017_int_base = 10.6
SPECspeed2017_int_peak = 11.0

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>8</td>
<td>7.80</td>
<td>9.39</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>8</td>
<td></td>
<td>12.5</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>8</td>
<td>7.15</td>
<td>15.3</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>8</td>
<td>7.15</td>
<td>15.4</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>8</td>
<td></td>
<td>12.7</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>8</td>
<td></td>
<td>14.5</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>8</td>
<td>6.40</td>
<td>14.9</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>8</td>
<td>5.68</td>
<td>15.0</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>8</td>
<td></td>
<td>13.4</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>8</td>
<td></td>
<td>13.8</td>
</tr>
</tbody>
</table>

Hardware

CPU Name: Intel Core i7-9700K
Max MHz.: 4900
Nominal: 3600
Enabled: 8 cores, 1 chip
Orderable: 1 chip
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 256 KB I+D on chip per core
L3: 12 MB I+D on chip per chip
Other: None
Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2666V-E)
Storage: 1 x 200 GB SATA III SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 12 SP3 (x86_64)
Kernel: 4.4.114-94.11-default
Compiler: C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux;
Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux
Parallel: Yes
Firmware: Version 1.0a released Sep-2018
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator library V5.0.1
Supermicro
SuperWorkstation 5039C-T (X11SCA , Intel Core i7-9700K)

SPECspeed2017_int_base = 10.6
SPECspeed2017_int_peak = 11.0

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>8</td>
<td>228</td>
<td>7.80</td>
<td>229</td>
<td>7.74</td>
<td>227</td>
<td>7.82</td>
<td>8</td>
<td>189</td>
<td>9.39</td>
<td>187</td>
<td>9.48</td>
<td>189</td>
<td>9.37</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>8</td>
<td>316</td>
<td>12.6</td>
<td>325</td>
<td>12.3</td>
<td>328</td>
<td>12.1</td>
<td>8</td>
<td>317</td>
<td>12.6</td>
<td>320</td>
<td>12.5</td>
<td>318</td>
<td>12.5</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>8</td>
<td>306</td>
<td>15.4</td>
<td>316</td>
<td>14.9</td>
<td>308</td>
<td>15.3</td>
<td>8</td>
<td>310</td>
<td>15.3</td>
<td>307</td>
<td>15.4</td>
<td>305</td>
<td>15.5</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>8</td>
<td>228</td>
<td>7.15</td>
<td>227</td>
<td>7.18</td>
<td>231</td>
<td>7.05</td>
<td>8</td>
<td>228</td>
<td>7.16</td>
<td>232</td>
<td>7.04</td>
<td>228</td>
<td>7.15</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>8</td>
<td>111</td>
<td>12.7</td>
<td>111</td>
<td>12.8</td>
<td>112</td>
<td>12.7</td>
<td>8</td>
<td>98.1</td>
<td>14.4</td>
<td>96.8</td>
<td>14.6</td>
<td>98.0</td>
<td>14.5</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>8</td>
<td>118</td>
<td>14.9</td>
<td>118</td>
<td>14.9</td>
<td>118</td>
<td>15.0</td>
<td>8</td>
<td>117</td>
<td>15.1</td>
<td>118</td>
<td>15.0</td>
<td>118</td>
<td>15.0</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>8</td>
<td>300</td>
<td>5.68</td>
<td>300</td>
<td>5.68</td>
<td>300</td>
<td>5.68</td>
<td>8</td>
<td>300</td>
<td>5.68</td>
<td>300</td>
<td>5.68</td>
<td>300</td>
<td>5.68</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>8</td>
<td>166</td>
<td>17.7</td>
<td>166</td>
<td>17.8</td>
<td>166</td>
<td>17.7</td>
<td>8</td>
<td>165</td>
<td>17.8</td>
<td>167</td>
<td>17.6</td>
<td>166</td>
<td>17.8</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>8</td>
<td>462</td>
<td>13.4</td>
<td>462</td>
<td>13.4</td>
<td>462</td>
<td>13.4</td>
<td>8</td>
<td>449</td>
<td>13.8</td>
<td>448</td>
<td>13.8</td>
<td>448</td>
<td>13.8</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
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SPECspeed2017_int_peak = 11.0

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jan-2019
Hardware Availability: Oct-2018
Software Availability: Mar-2018

Platform Notes
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-65nv Sat Jan  5 06:22:46 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Core(TM) i7-9700K CPU @ 3.60GHz
  1 "physical id"s (chips)
  8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 1 2 3 4 5 6 7

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 8
On-line CPU(s) list: 0-7
Thread(s) per core: 1
Core(s) per socket: 8
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Core(TM) i7-9700K CPU @ 3.60GHz
Stepping: 12
CPU MHz: 4809.600
CPU max MHz: 4900.0000
CPU min MHz: 800.0000
BogoMIPS: 7199.97
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 256K
L3 cache: 12288K
NUMA node0 CPU(s): 0-7
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg

(Continued on next page)
SPEC CPU2017 Integer Speed Result

Standard Performance Evaluation Corporation

SPEC CPU2017 license: 001176

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Supermicro

SuperWorkstation 5039C-T (X11SCA, Intel Core i7-9700K)

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Platform Notes (Continued)

fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
dtherm hwp hwp_notify hwp_act_window hwp_epp intel_pt rsb_ctxsw spec_ctrl retpoline
kaiser tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep
bmi2 erms invpcid rtm mpx rdseed adx smap clflushopt xsaveopt xsavec xgetbv1

/proc/cpuinfo cache data

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.

available: 1 nodes (0)

node 0 cpus: 0 1 2 3 4 5 6 7
node 0 size: 64283 MB
node 0 free: 47712 MB

node distances:
	node 0
	0: 10

From /proc/meminfo

MemTotal: 65825824 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3

# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:

Linux linux-65nv 4.4.144-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown):

Mitigation: PTI

(Continued on next page)
**SPEC CPU2017 Integer Speed Result**

**Supermicro**
SuperWorkstation 5039C-T (X11SCA, Intel Core i7-9700K)

<table>
<thead>
<tr>
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</thead>
<tbody>
<tr>
<td>SPECspeed2017 int_peak</td>
<td>11.0</td>
</tr>
</tbody>
</table>

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<thead>
<tr>
<th>CPU2017 License:</th>
<th>001176</th>
</tr>
</thead>
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<td>Supermicro</td>
</tr>
<tr>
<td>Tested by:</td>
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</tr>
<tr>
<td>Test Date:</td>
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</tr>
<tr>
<td>Software Availability:</td>
<td>Mar-2018</td>
</tr>
</tbody>
</table>

**Platform Notes (Continued)**

CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Jan 4 14:01

SPEC is set to: /home/cpu2017

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda3</td>
<td>xfs</td>
<td>145G</td>
<td>36G</td>
<td>110G</td>
<td>25%</td>
<td>/home</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- BIOS American Megatrends Inc. 1.0a 09/27/2018
- Memory:
  - 4x Micron 18ADF2G72AZ-2G6H1R 16 GB 2 rank 2667

(End of data from sysinfo program)

**Compiler Version Notes**

```
==============================================================================
CC  600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)
==============================================================================
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
641.leela_s(base)
==============================================================================
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
```

(Continued on next page)
Supermicro
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Compiler Version Notes (Continued)
641.leela_s(peak)

icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

FC 648.exchange2_s(base, peak)

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags
C benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div

(Continued on next page)
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Base Optimization Flags (Continued)

C benchmarks (continued):
-qopt-mem-layout-trans=3 -qopenmp -DSPEC/OpenMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks (except as noted below):
icpc -m64

623.xalancbmk_s: icpc -m32 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
### SPEC CPU2017 Integer Speed Result

**Supermicro**  
SuperWorkstation 5039C-T (X11SCA, Intel Core i7-9700K)  

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</tr>
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<td></td>
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</tbody>
</table>

**Peak Optimization Flags**

#### C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-prefetch -ipo -O3
-qopt-mem-layout-trans=3 -no-prec-div
-DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC/OpenMP
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-prefetch -ipo -O3
-qopt-mem-layout-trans=3 -no-prec-div
-DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC/OpenMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -gopenmp
-DSPEC/OpenMP -L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -qopt-mem-layout-trans=3 -gopenmp
-DSPEC/OpenMP -L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: Same as 602.gcc_s

#### C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -gopenmp
-DSPEC/OpenMP -L/usr/local/je5.0.1-64/lib -ljemalloc

623.xalancbmk_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -gopenmp
-DSPEC/OpenMP -L/usr/local/je5.0.1-32/lib -ljemalloc

631.deepsjeng_s: basepeak = yes

641.leelu_s: basepeak = yes

#### Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc
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Supermicro
SuperWorkstation 5039C-T (X11SCA, Intel Core i7-9700K)

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**Hardware Availability:** Oct-2018  
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The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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Tested with SPEC CPU2017 v1.0.5 on 2019-01-04 17:22:45-0500.  
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